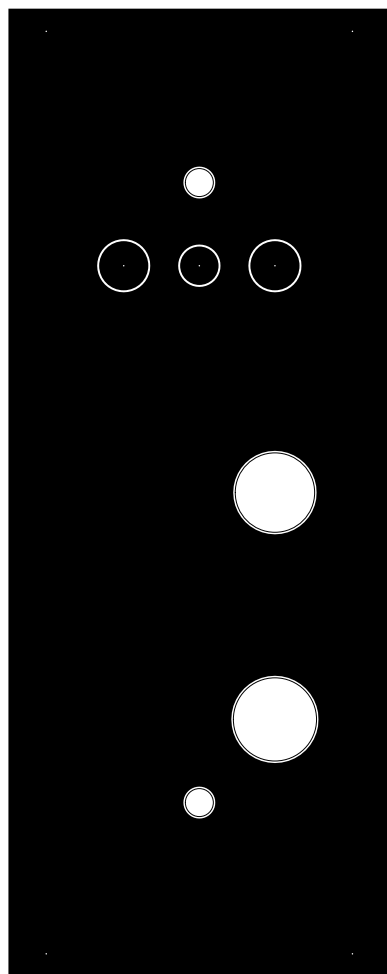
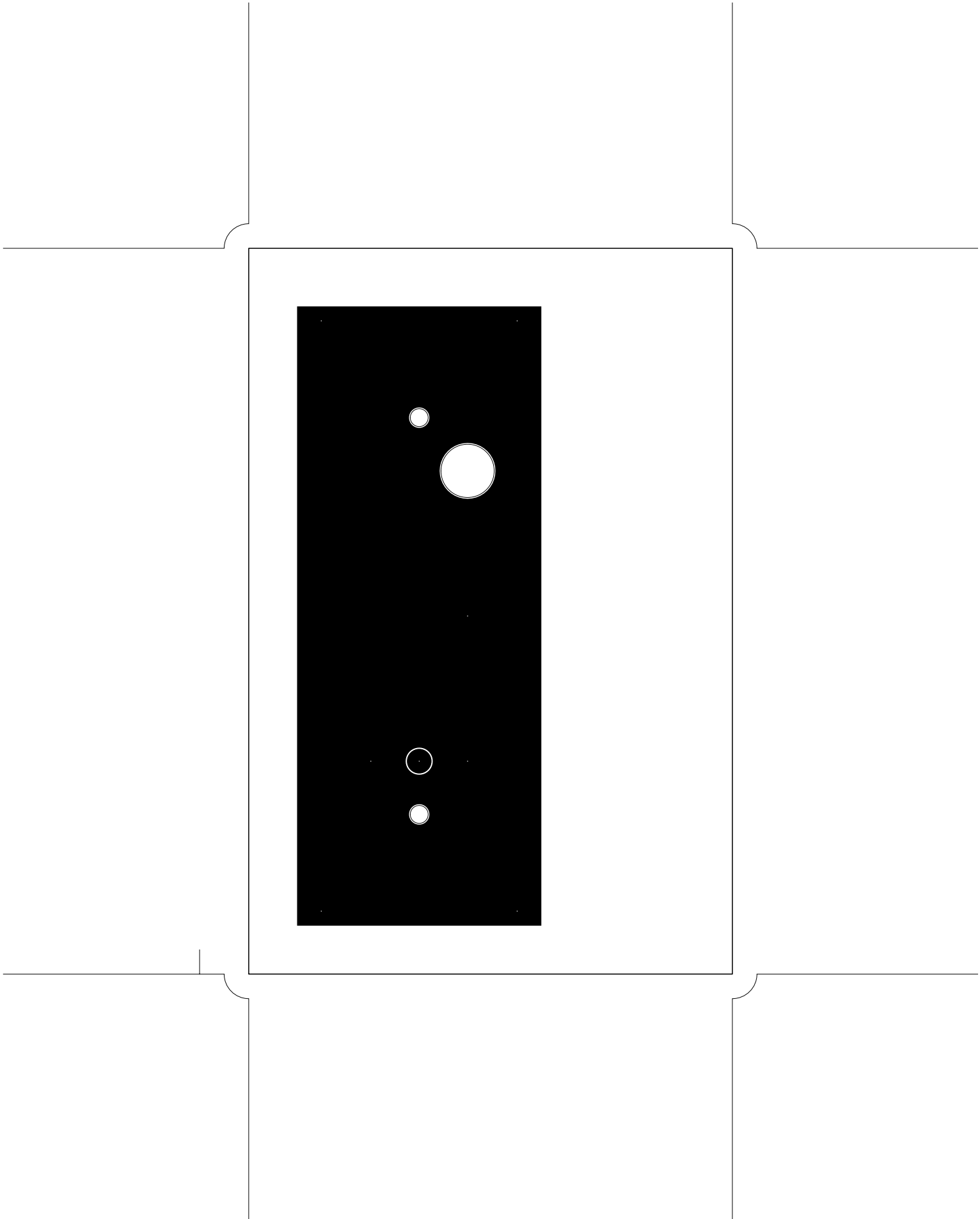


1. Table of Contents (This Page)

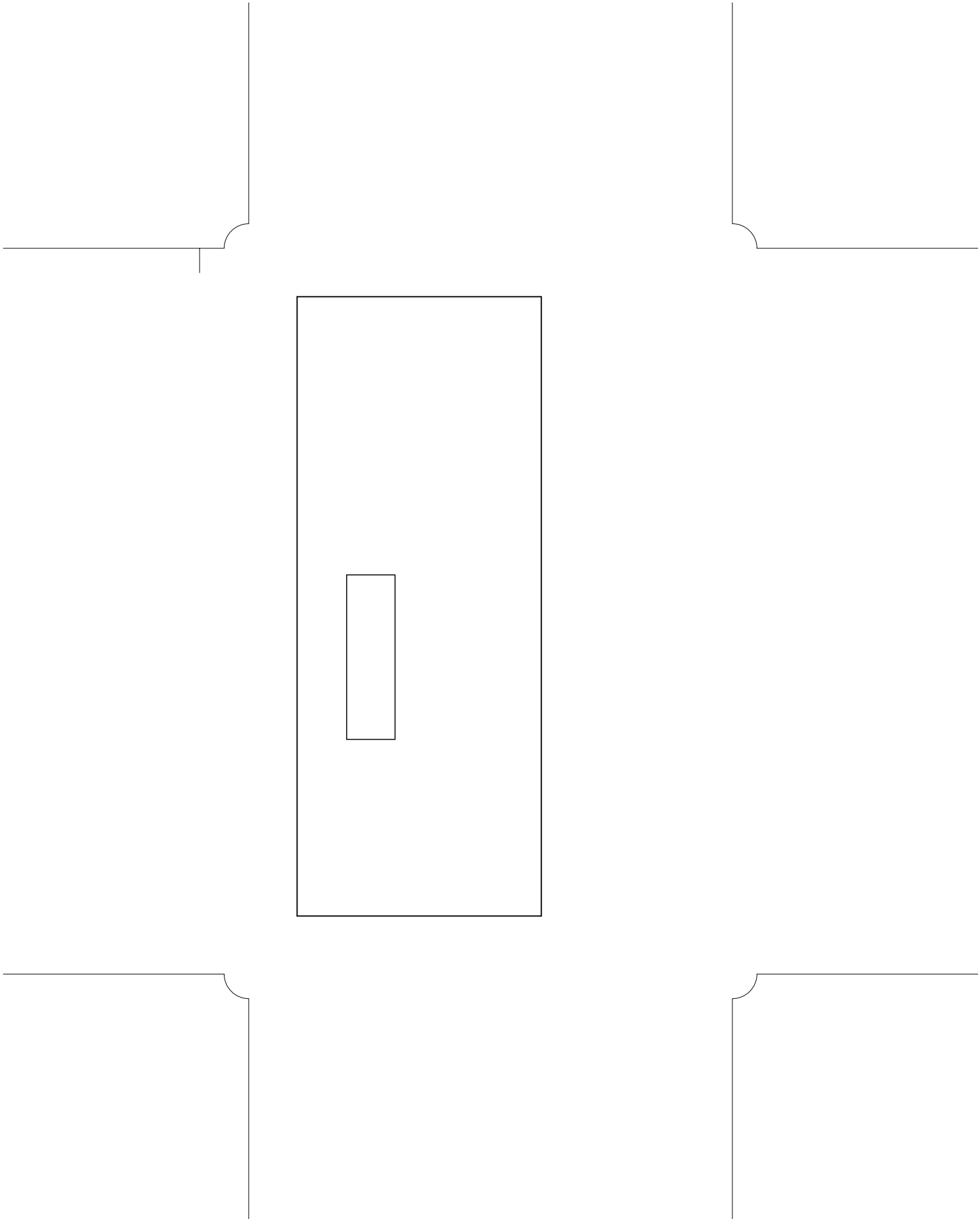
Created on Fri Aug 10 22:08:54 2018

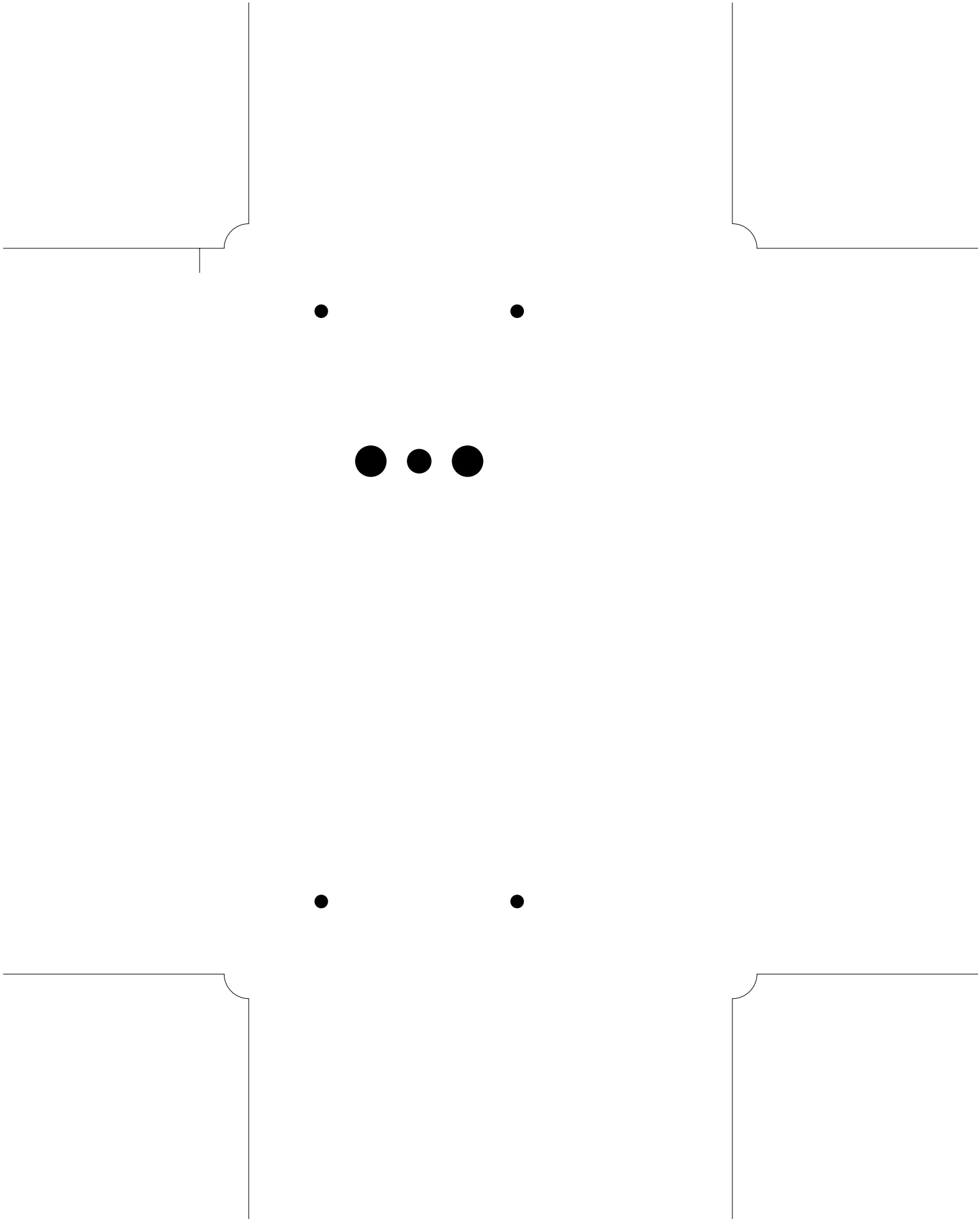
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. bottompaste
12. topassembly
13. bottomassembly
14. fab

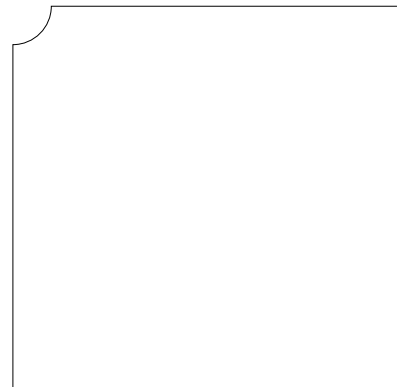
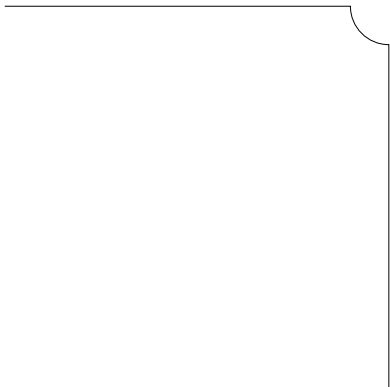
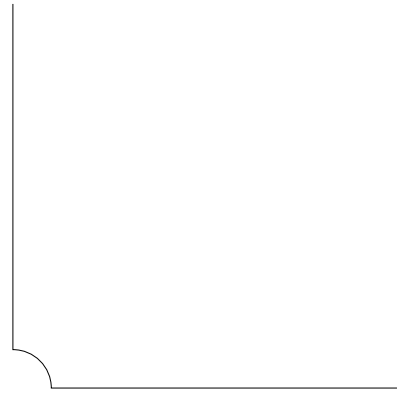
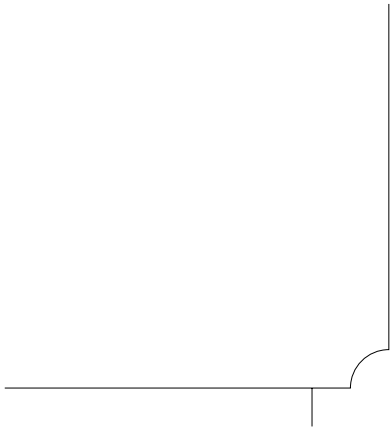


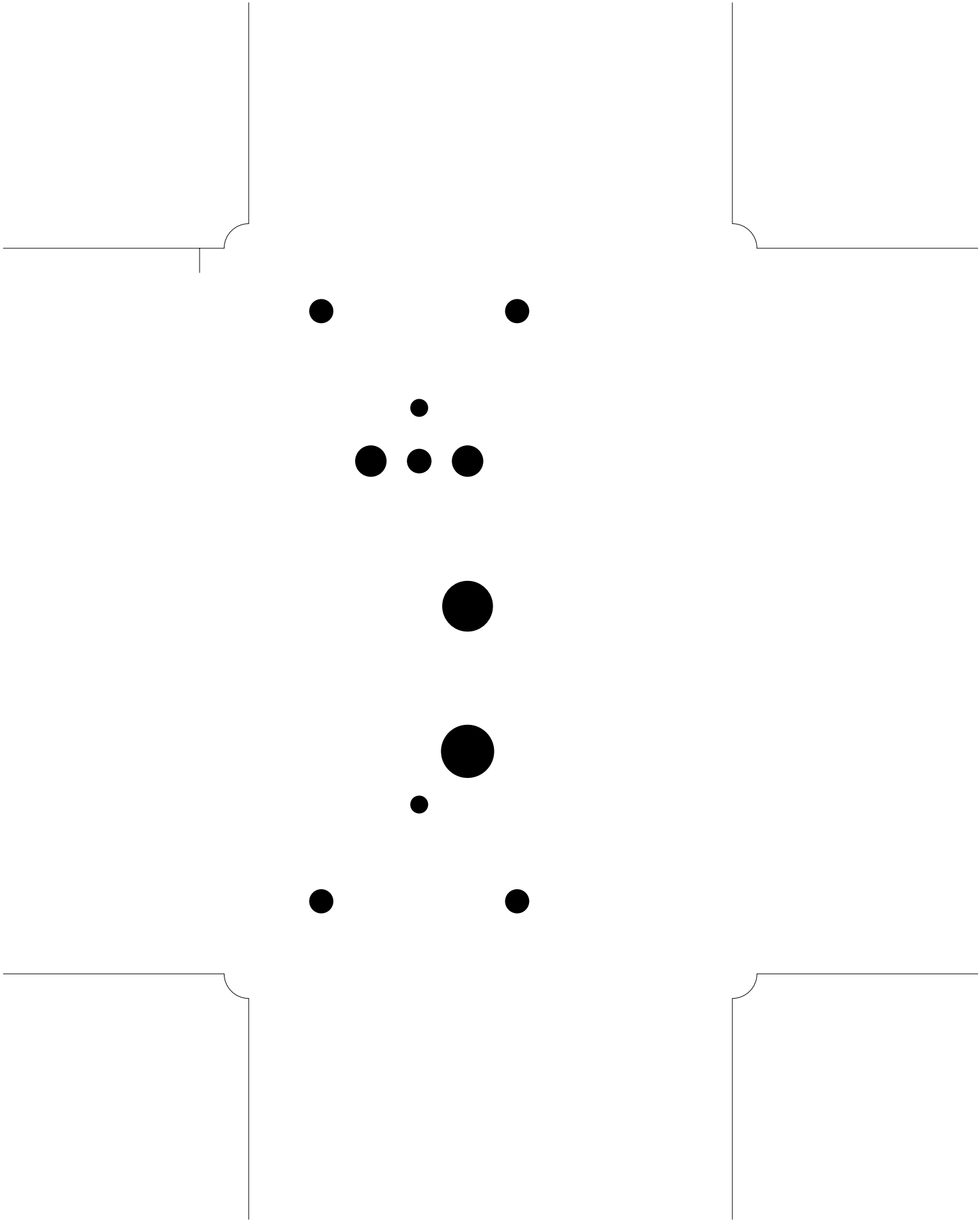


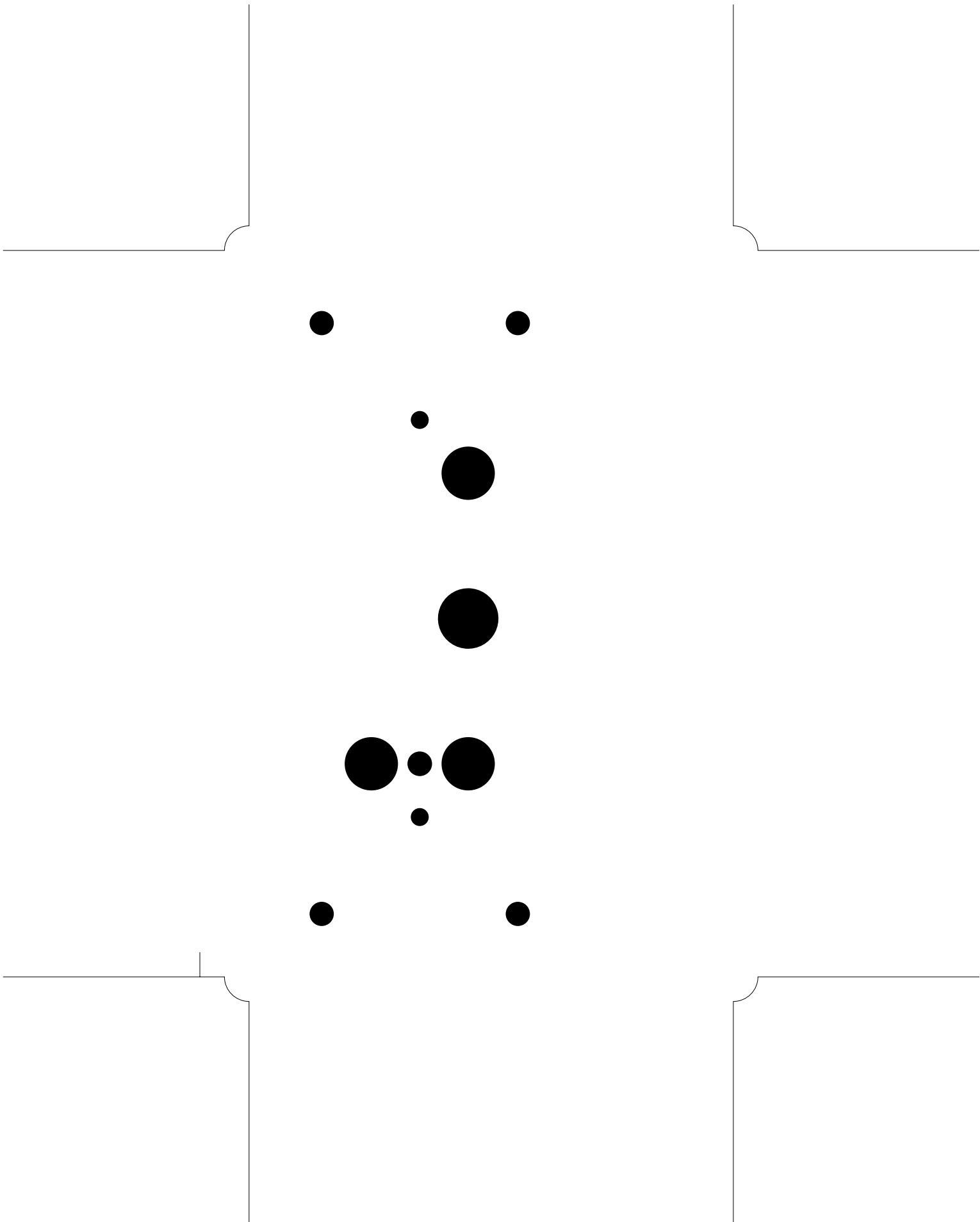
26.001.00.01.01.pcb, bottom (mirrored), scale = 1:1.000
26.001.00.01.01.pcb

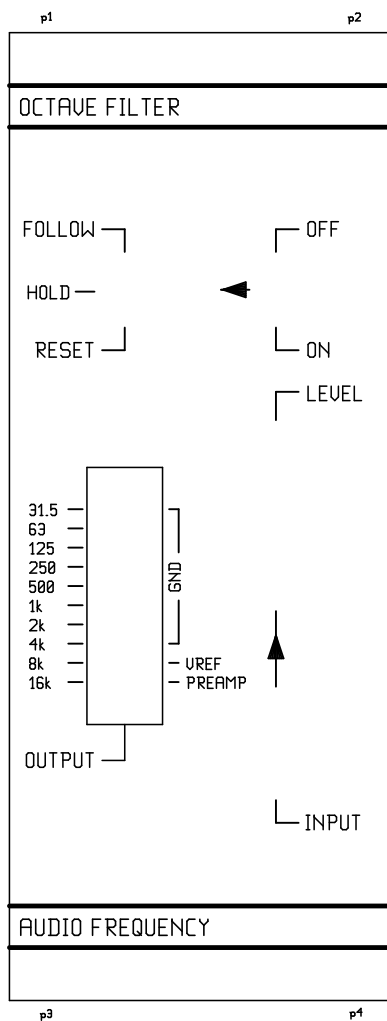


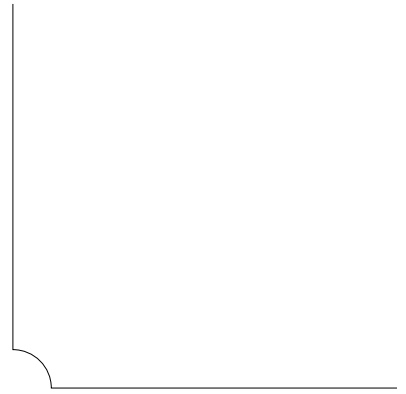
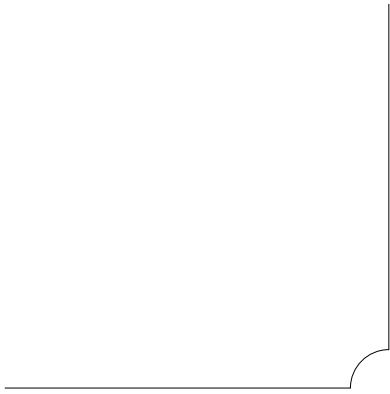










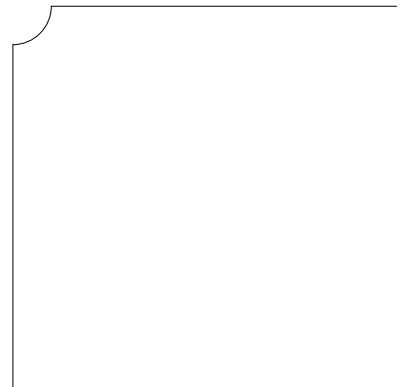
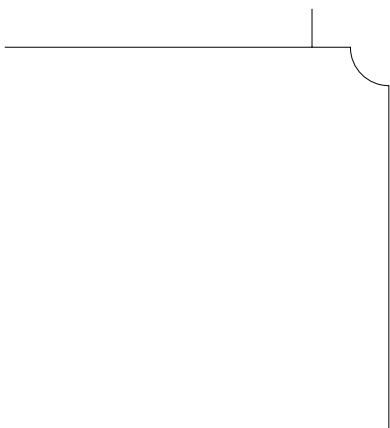


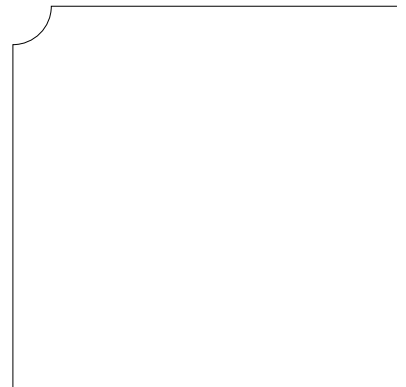
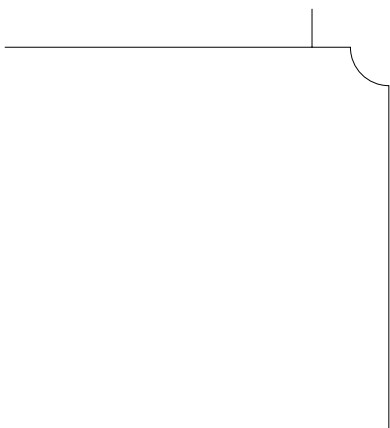
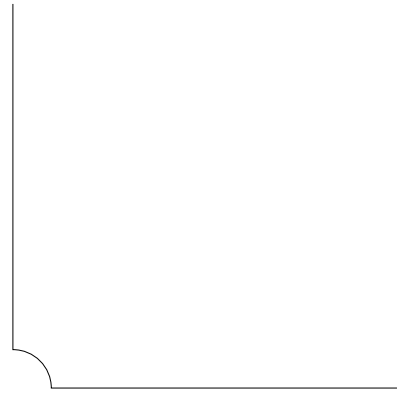
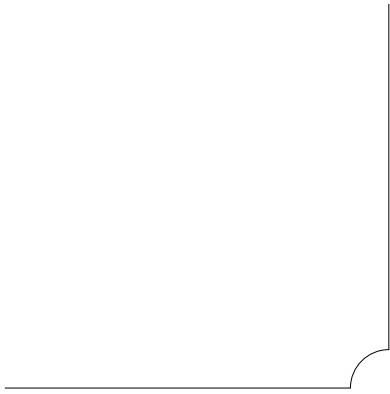
R101

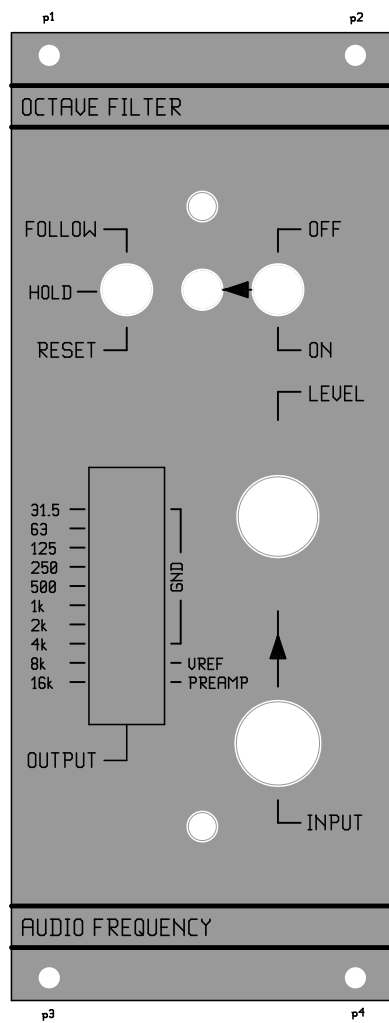
S101

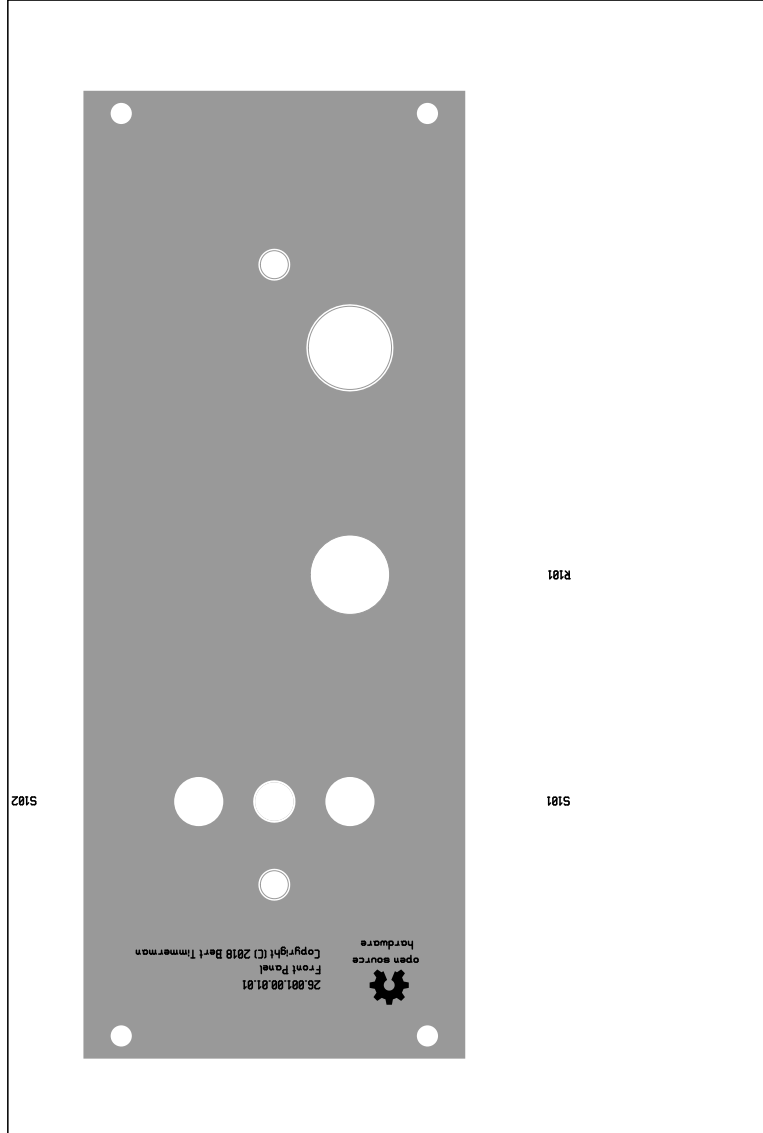
S102

26.001.00.01.01
Front Panel
Copyright (C) 2018 Bert Immerman
open source
hardware









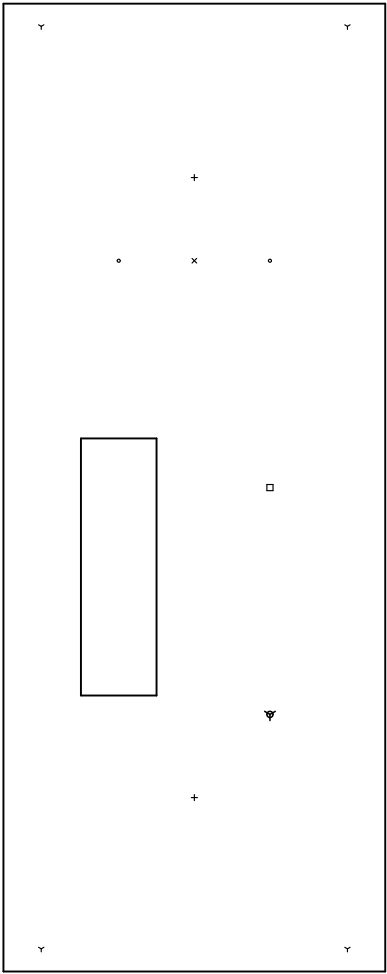
There are 6 different drill sizes used in this layout, 11 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.110	4	YES
+	0.146	2	NO
x	0.200	1	YES
•	0.256	2	YES
□	0.413	1	NO
▼	0.433	1	NO

Title: 26.001.00.01.01.pcb - Fabrication Drawing

Author: Bert Timmerman

Date: Fri Aug 10 20:00:54 2018 UTC



Board outline is the centerline of this path