
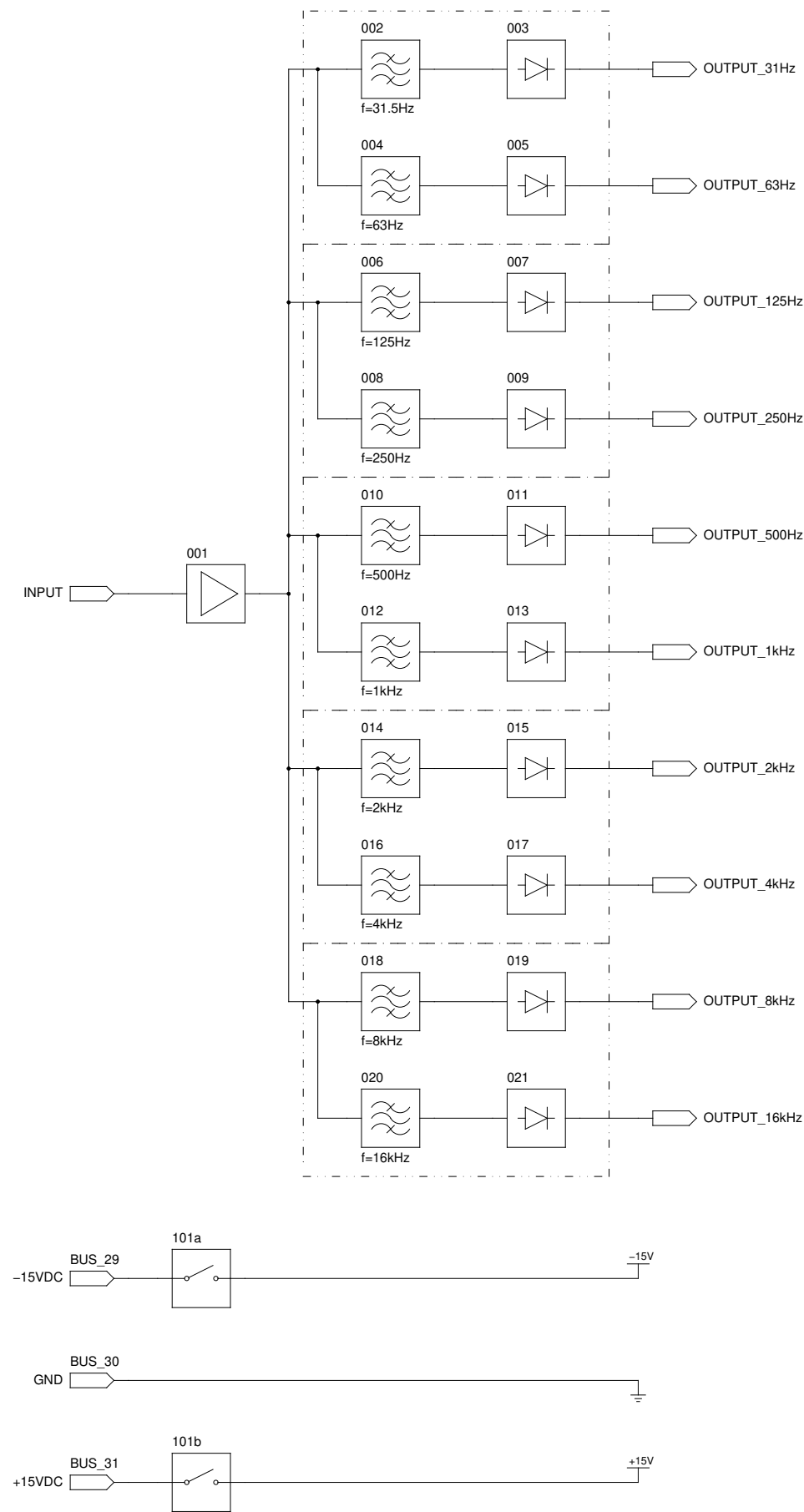
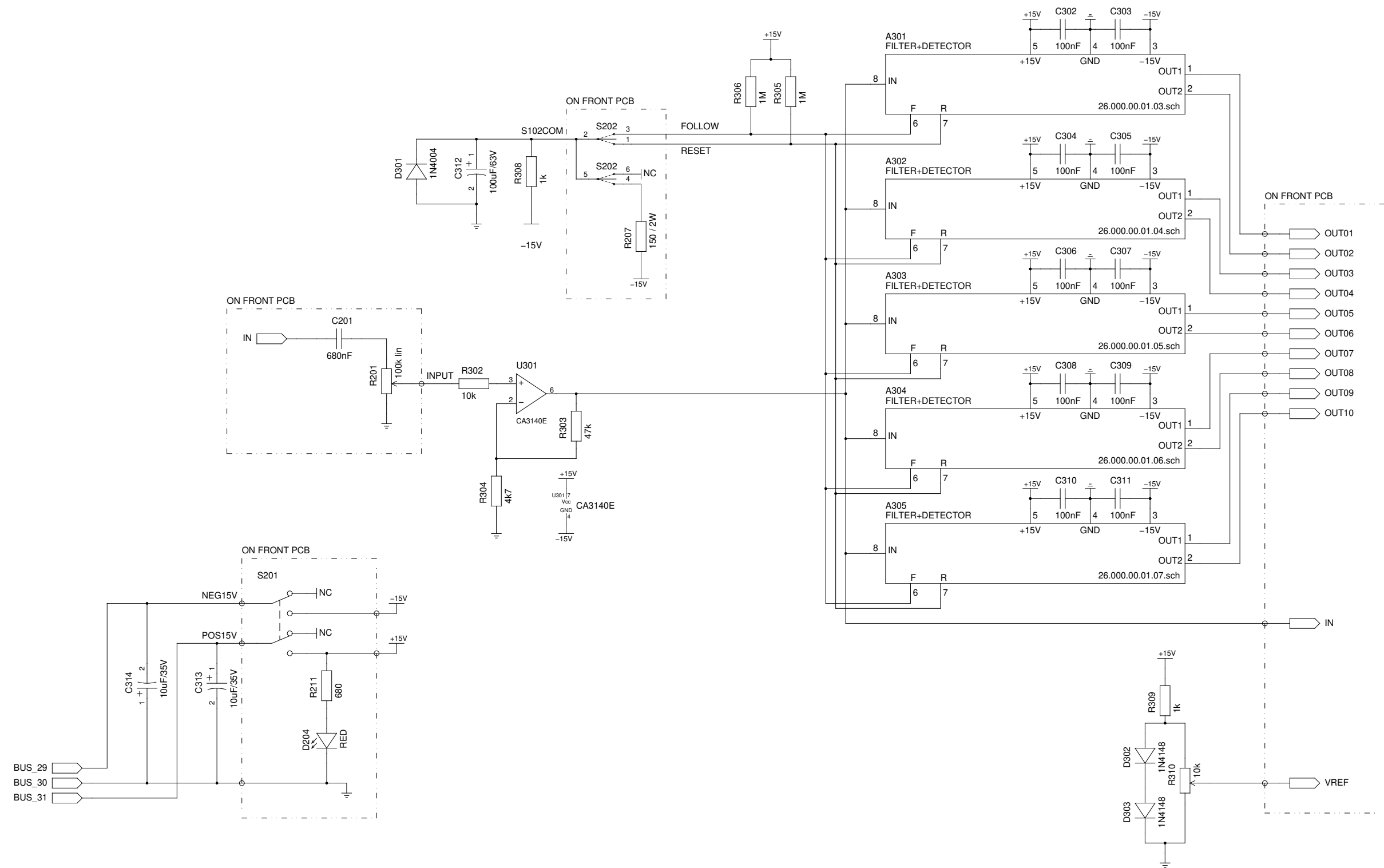
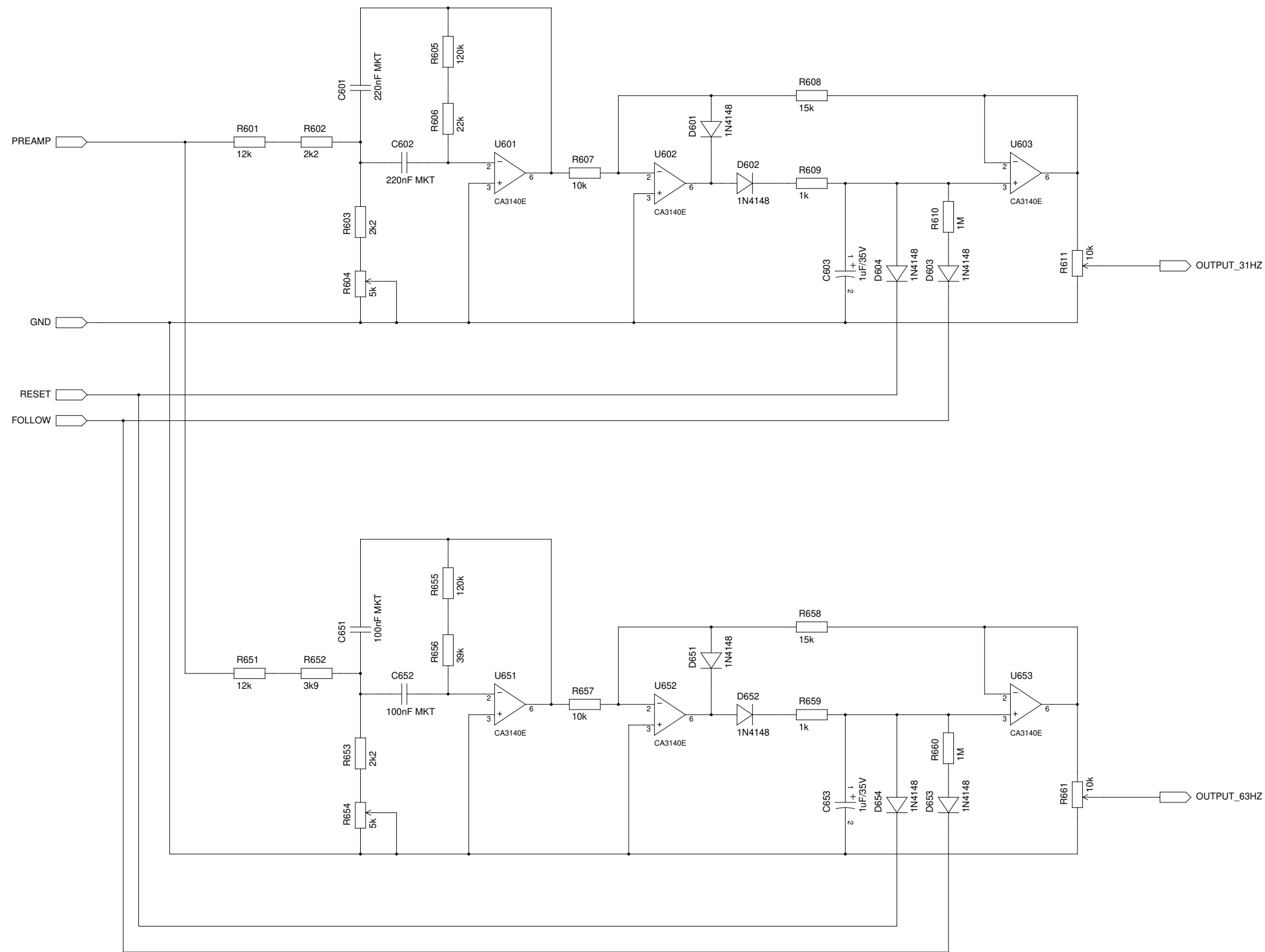


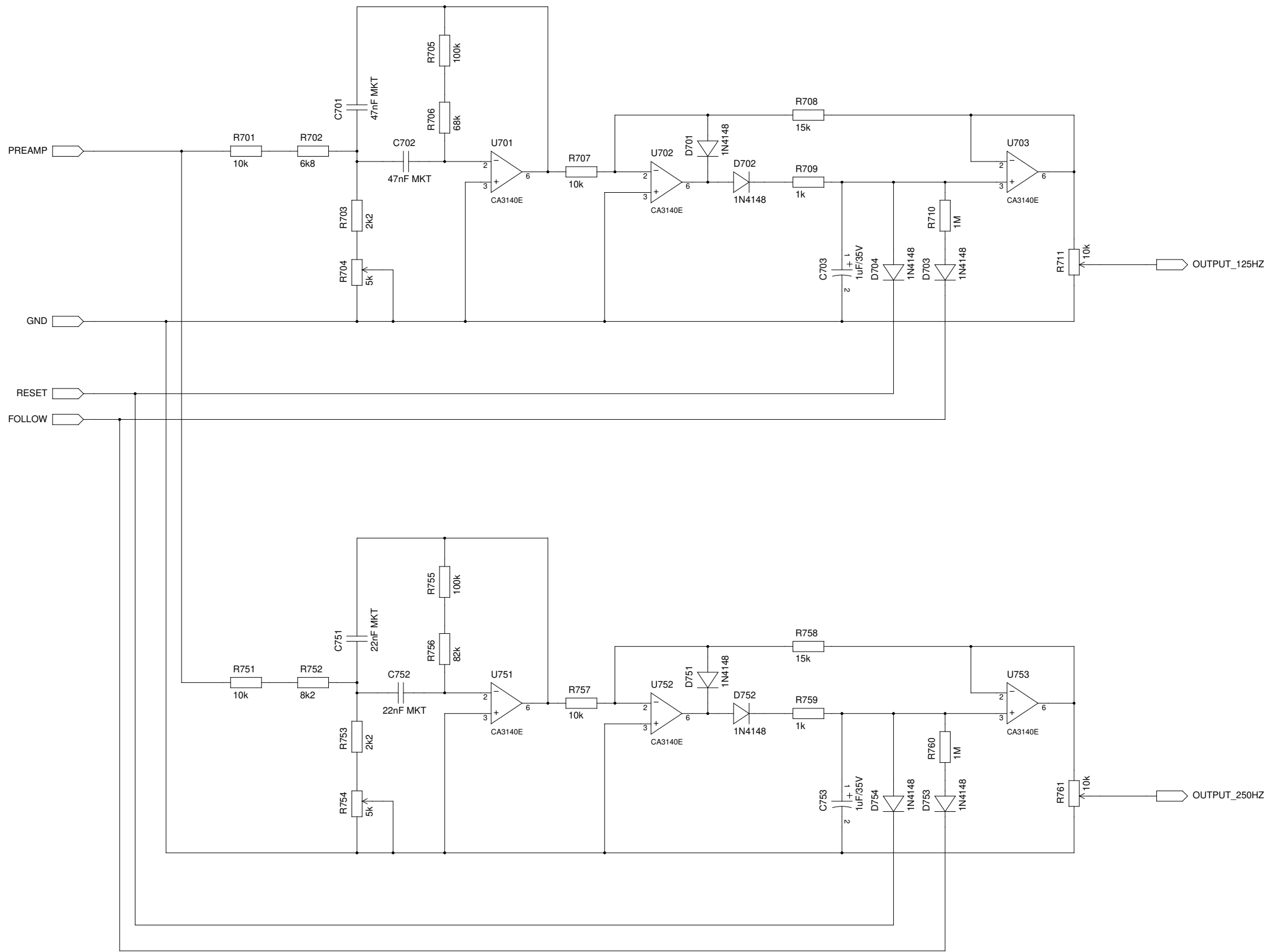
<p>This hardware is released under Creative Commons Share-alike 4.0 International (<a href="http://creativecommons.org/licenses/by-sa/4.0/">http://creativecommons.org/licenses/by-sa/4.0/</a>).</p> <p>Note: This is a human-readable summary of (and not a substitute for) the license (<a href="http://creativecommons.org/licenses/by-sa/4.0/legalcode">http://creativecommons.org/licenses/by-sa/4.0/legalcode</a>).</p> <p>You are free to:</p> <p>Share — copy and redistribute the material in any medium or format</p> <p>Adapt — remix, transform, and build upon the material for any purpose, even commercially.</p> <p>The licensor cannot revoke these freedoms as long as you follow the license terms.</p> <p>Under the following terms:</p> <p>Attribution — You must give appropriate credit, provide a link to the license, and indicate if changes were made. You may do so in any reasonable manner, but not in any way that suggests the licensor endorses you or your use.</p> <p>ShareAlike — If you remix, transform, or build upon the material, you must distribute your contributions under the same license as the original.</p> <p>No additional restrictions — You may not apply legal terms or technological measures that legally restrict others from doing anything the license permits.</p> <p>Notices:</p> <p>You do not have to comply with the license for elements of the material in the public domain or where your use is permitted by an applicable exception or limitation.</p> <p>No warranties are given. The license may not give you all of the permissions necessary for your intended use. For example, other rights such as publicity, privacy, or moral rights may limit how you use the material.</p>	DOC.NR.	STATUS	DESCRIPTION	
	26.000.00.01.01	FINAL	Overall - Block diagram	
	26.000.00.01.02	FINAL	Overall - Schematic	
	26.000.00.01.03	FINAL	31.5 Hz & 63 Hz module - Schematic	
	26.000.00.01.04	FINAL	125 Hz & 250 Hz module - Schematic	
	26.000.00.01.05	FINAL	500 Hz & 1 kHz module - Schematic	
	26.000.00.01.06	FINAL	2 kHz & 4 kHz module - Schematic	
	26.000.00.01.07	FINAL	8 kHz & 16 kHz module - Schematic	
	26.001.00.01.01	FINAL	Front panel - Schematic (DFM)	
	26.002.00.01.01	FINAL	Front pcb - Schematic (DFM)	
	26.003.00.01.01	FINAL	Main pcb - Schematic (DFM)	
	26.004.00.01.01	FINAL	Shielding pcb - Schematic (DFM)	
	26.005.00.01.01	FINAL	Strut pcb - Schematic (DFM)	
	26.006.00.01.01	VOID	31.5 Hz & 63 Hz module (PTH) - Schematic (DFM)	
	26.006.01.01.01	VOID	31.5 Hz & 63 Hz module (PTH+SMT) - Schematic (DFM)	
	26.006.02.01.01	FINAL	31.5 Hz & 63 Hz module (PTH+SMT) - Schematic (DFM)	
	26.007.01.01.01	FINAL	125 Hz & 250 Hz module (PTH+SMT) - Schematic (DFM)	
	26.008.01.01.01	FINAL	500 Hz & 1 kHz module (PTH+SMT) - Schematic (DFM)	
	26.009.01.01.01	TBD	2 kHz & 4 kHz module (PTH+SMT) - Schematic (DFM)	
	26.010.01.01.01	TBD	8 kHz & 16 kHz module (PTH+SMT) - Schematic (DFM)	
	26.999.00.01.01	VOID	CA3140M breakout pcb (PTH+SMT) - Schematic (DFM)	
	26.999.01.01.01	FINAL	CA3140M breakout pcb (PTH+SMT) - Schematic (DFM)	
<div></div>	OCTAVE_FILTER			
	TITLE Front Page			
	FILE: 26.000.00.00.01.sch		REVISION: 20240523	
	PAGE 01 OF 01		DRAWN BY: Bert Timmerman	
			A3	

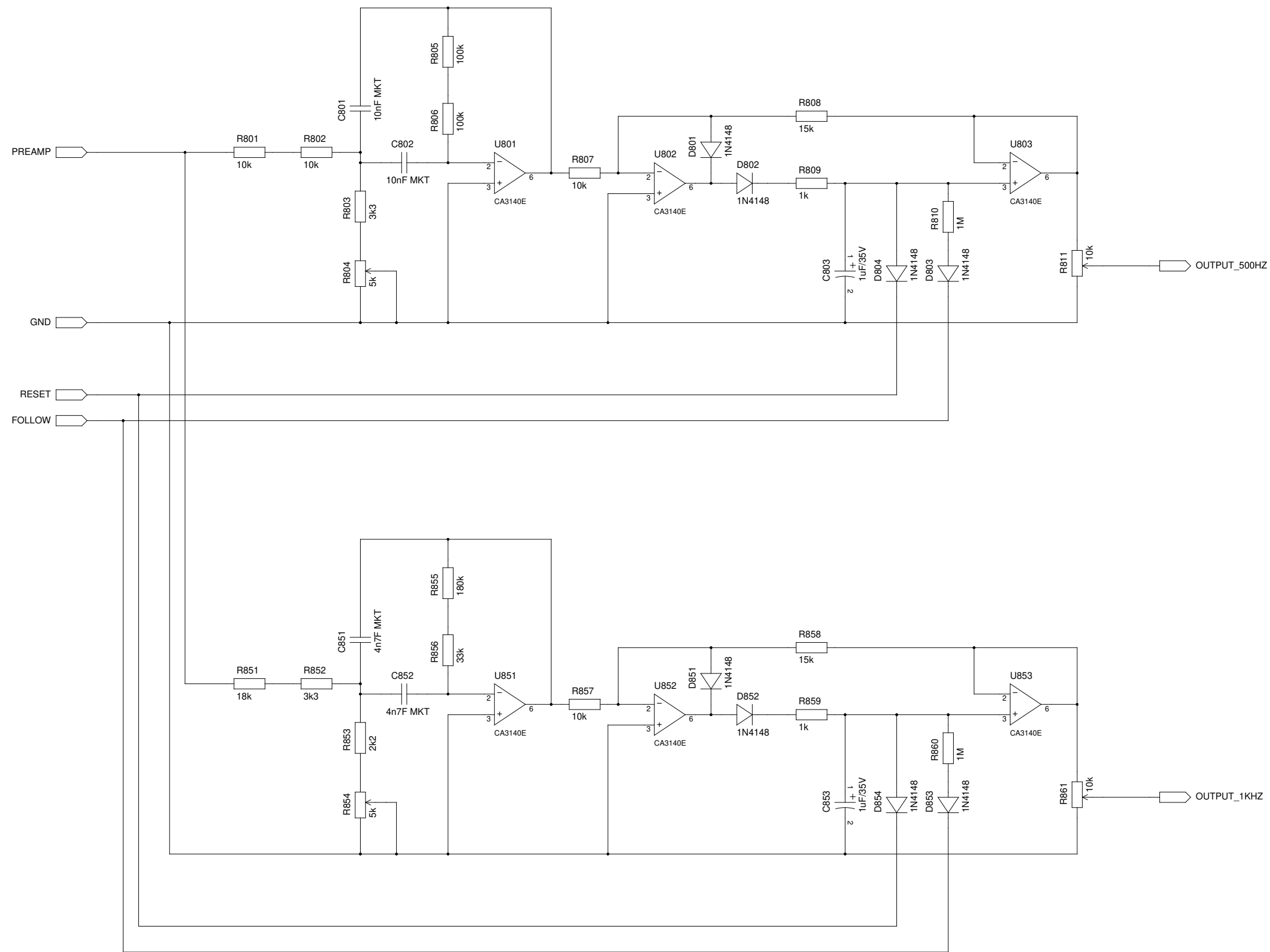


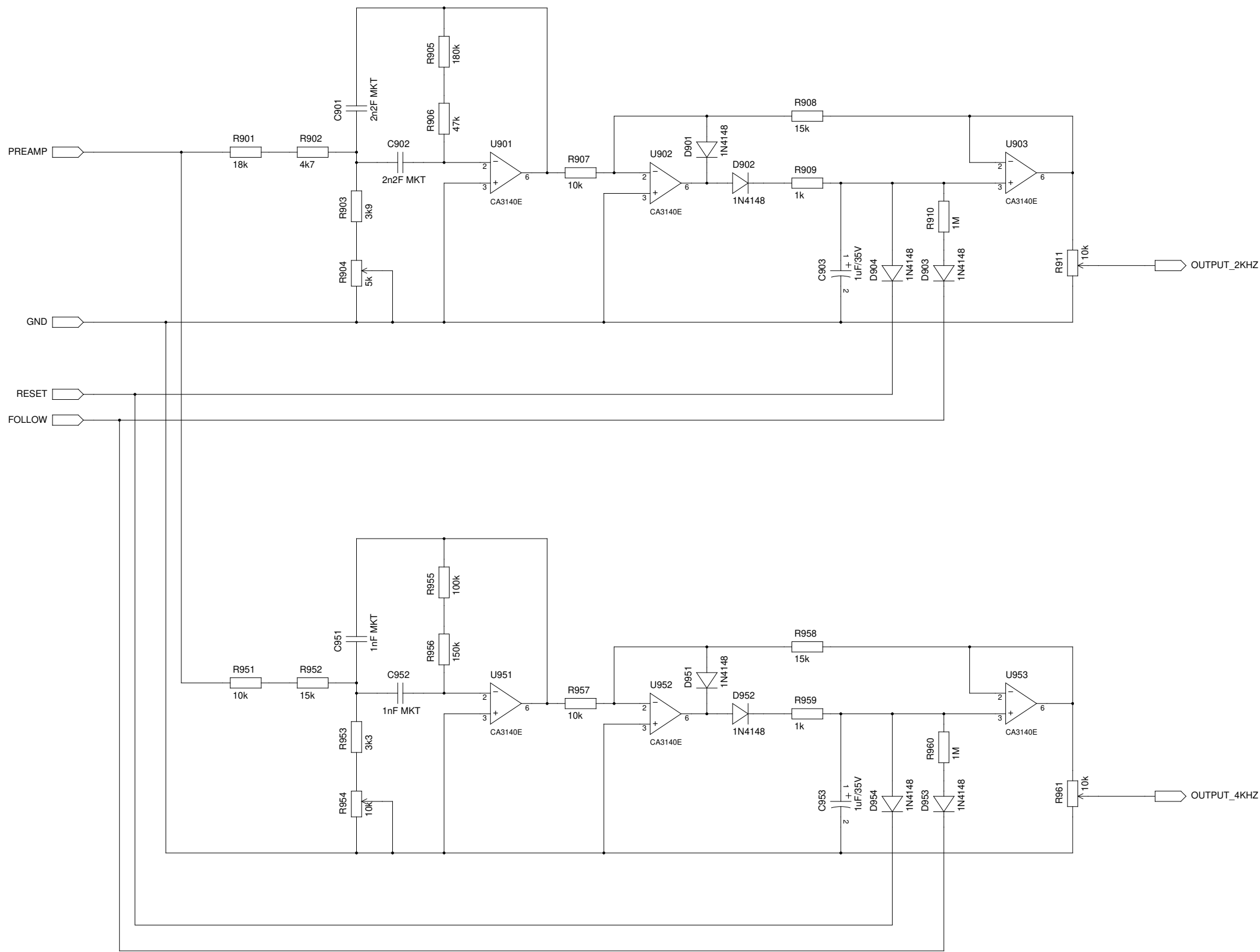
OCTAVE_FILTER		
Overall		
TITLE Block diagram		
FILE:	26.000.00.01.01.sch	REVISION: 20240522
PAGE	01 OF 07	DRAWN BY: Bert Timmerman
		A1

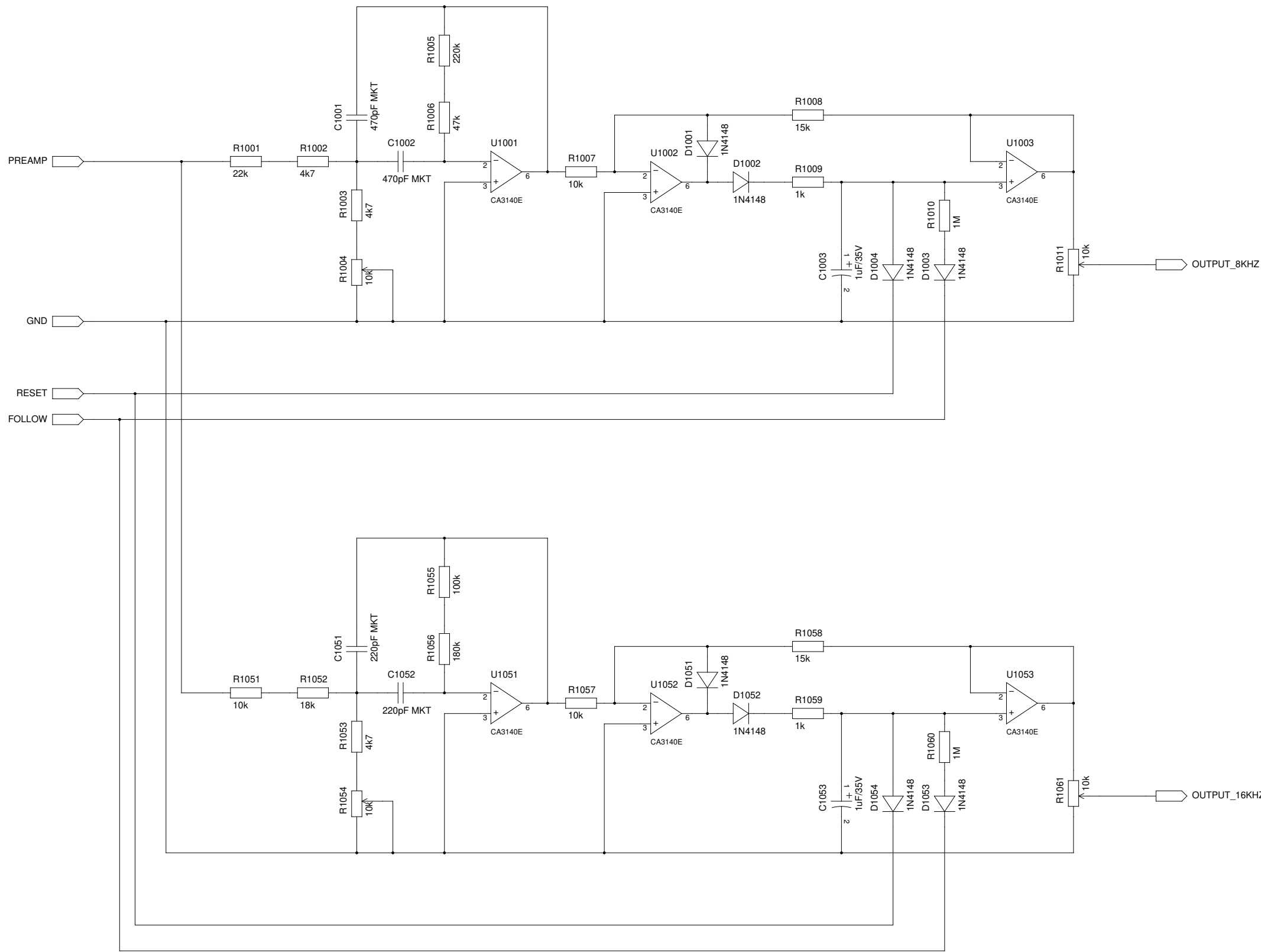












OCTAVE_FILTER 8 kHz & 16 kHz module			A1
TITLE Schematic			
FILE: 26.000.00.01.07.sch	REVISION: 20240522		
PAGE 07	OF 07	DRAWN BY: Bert Timmerman	



.TITLE OCTAVE FILTER – MAIN BOARD – INPUT STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 100)  
C1 1 2 680n  
R1 0 2 100k  
R2 2 6 10K  
R3 3 7 47k  
R4 7 0 4700  
XOP1 6 7 0 4 5 3 UA741

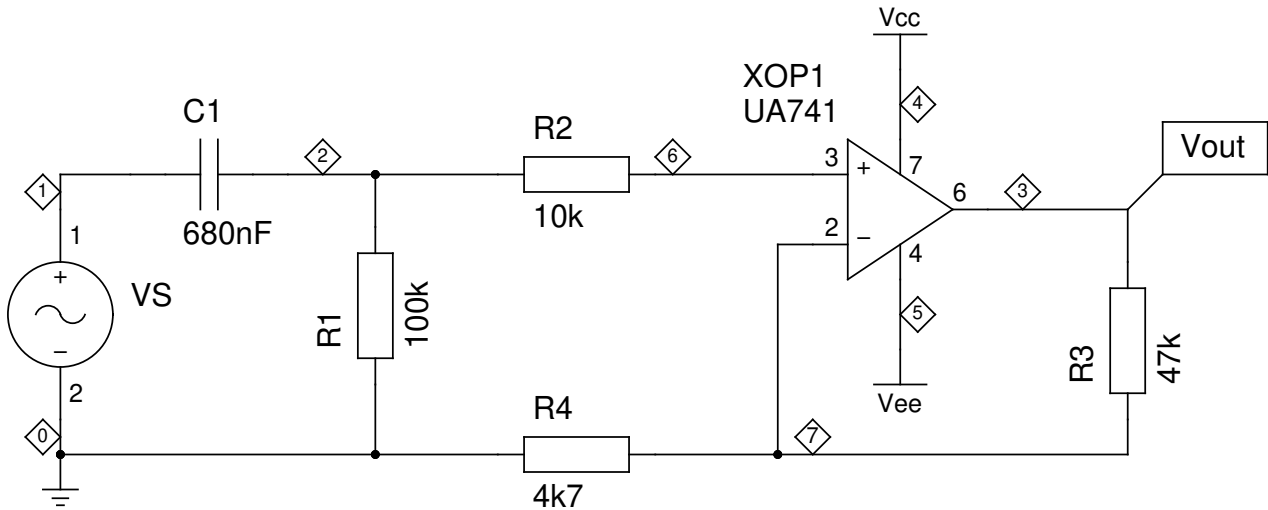
.PRINT OP Iter(0) V(3)

.PRINT AC VDB(3)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.01 10Meg

.END



Octave Filter – Main board – Input stage (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.01.sch

PAGE 01 OF 01

REVISION: 20220422

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A3

.TITLE OCTAVE FILTER – MAIN BOARD – INPUT STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

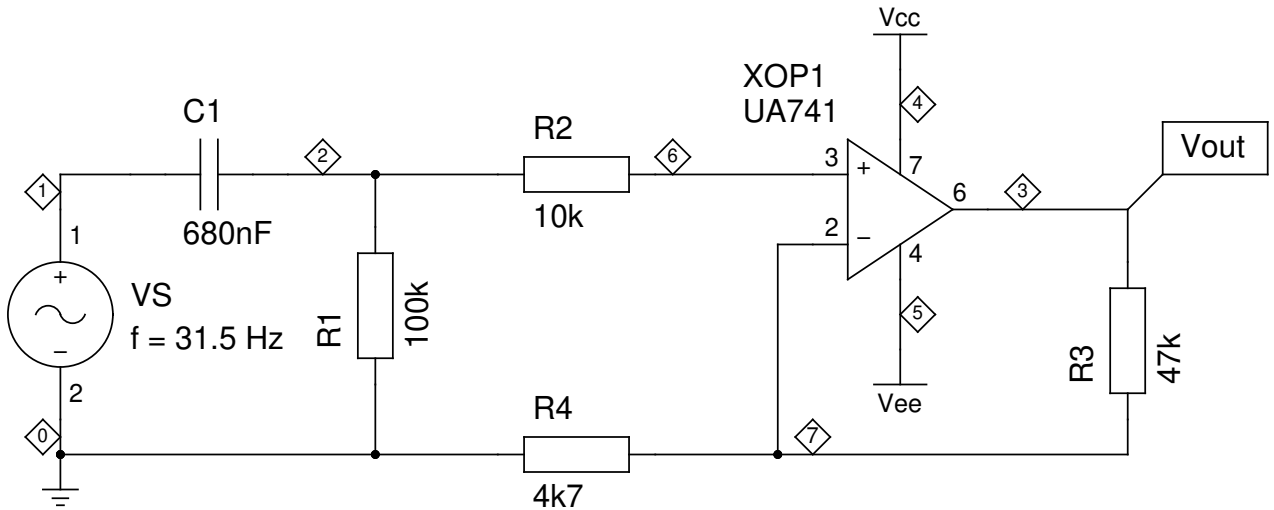
VS 1 0 AC 1 SIN(0 0.141 31.5)  
C1 1 2 680n  
R1 0 2 100k  
R2 2 6 10K  
R3 3 7 47k  
R4 7 0 4700  
XOP1 6 7 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.4 0.00001 TRACE ALL

.END



Octave Filter – Main board – Input stage (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.02.sch

PAGE 01 OF 01

REVISION: 20220422

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A3

.TITLE OCTAVE FILTER – MAIN BOARD – INPUT STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

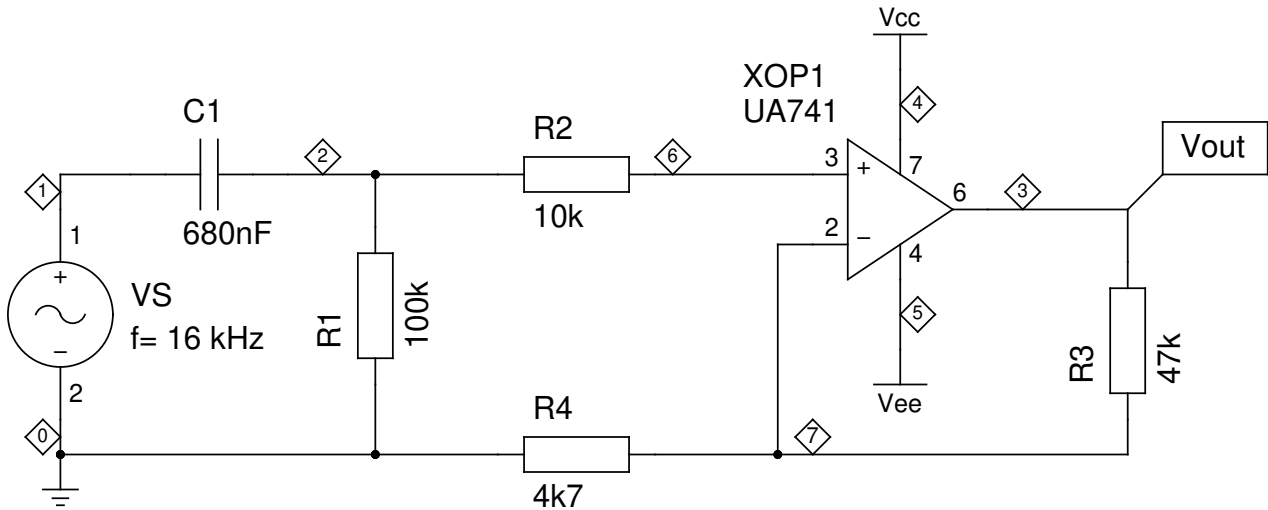
VS 1 0 AC 1 SIN(0 1 16k)  
C1 1 2 680n  
R1 0 2 100k  
R2 2 6 10K  
R3 3 7 47k  
R4 7 0 4700  
XOP1 6 7 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.01 0.000001 TRACE ALL

.END



Octave Filter – Main board – Input stage (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.03.sch

PAGE 01 OF 01

REVISION: 20220422

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A3

.TITLE OCTAVE FILTER – MAIN BOARD – INPUT STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

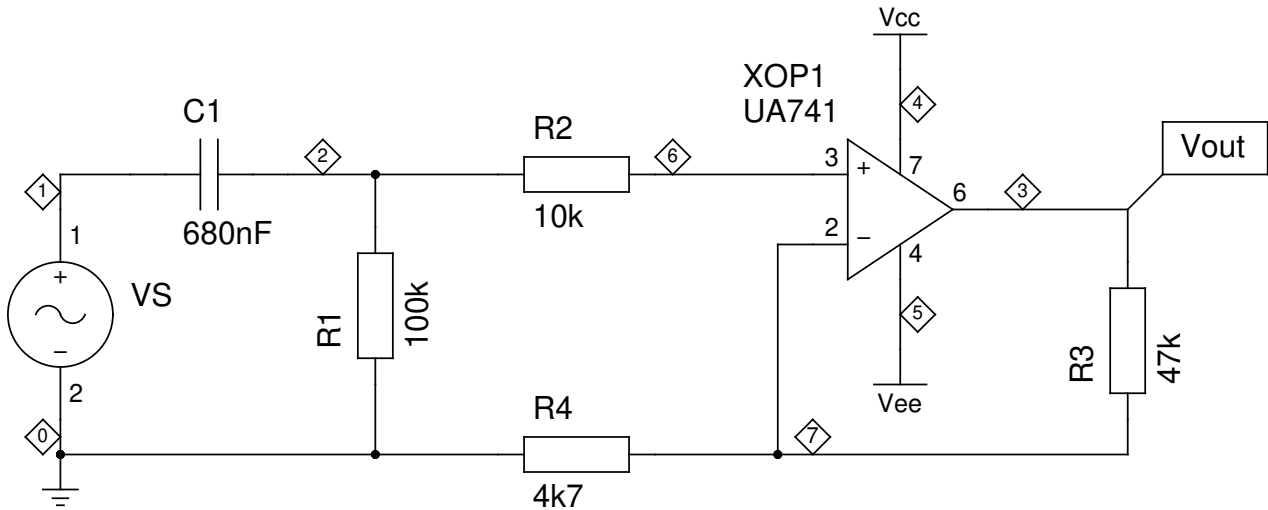
VS 1 0 AC 1 SIN(0 0.141 1k)  
C1 1 2 680n  
R1 0 2 100k  
R2 2 6 10K  
R3 3 7 47k  
R4 7 0 4700  
XOP1 6 7 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.02 0.000001 TRACE ALL

.END



Octave Filter – Main board – Input stage (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.04.sch

PAGE 01 OF 01

REVISION: 20220422

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A3

.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 100)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

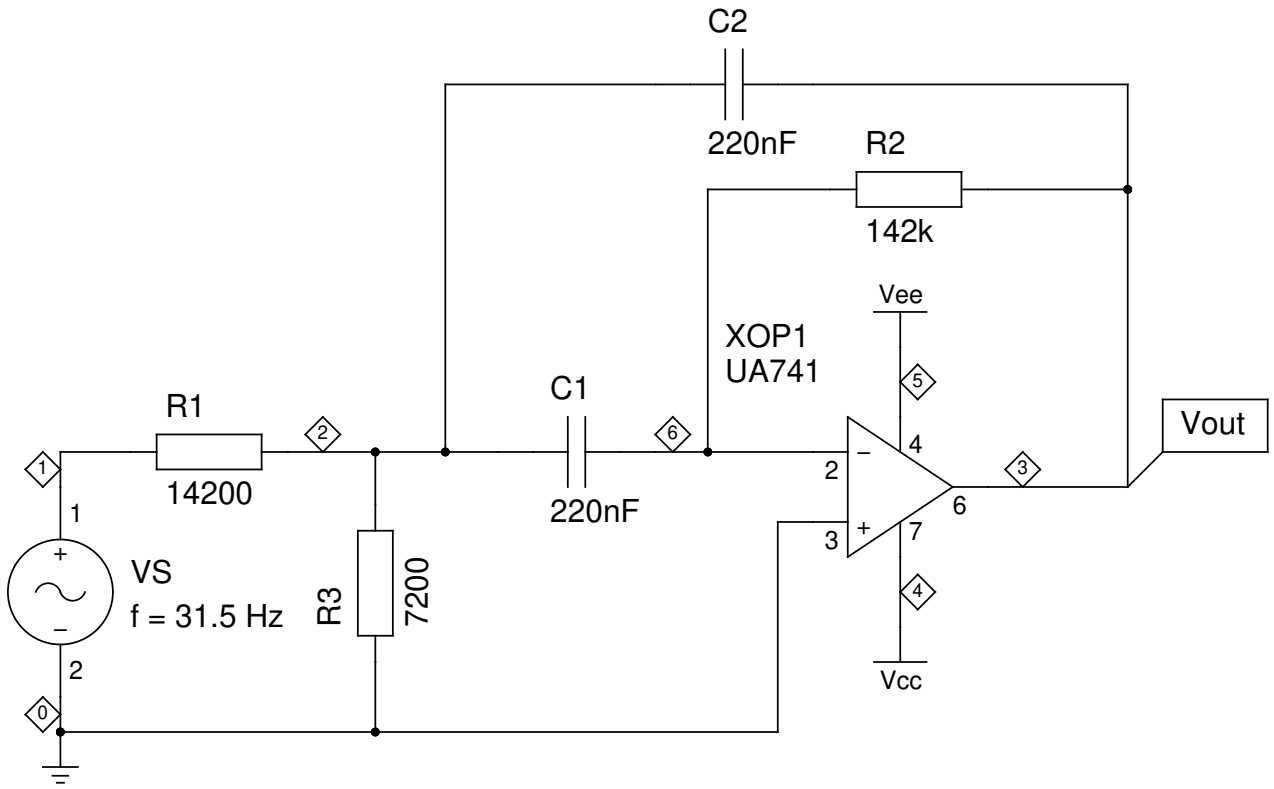
.PRINT OP Itr(0) V(3)

.PRINT AC VDB(3)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.1 100k

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.05.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

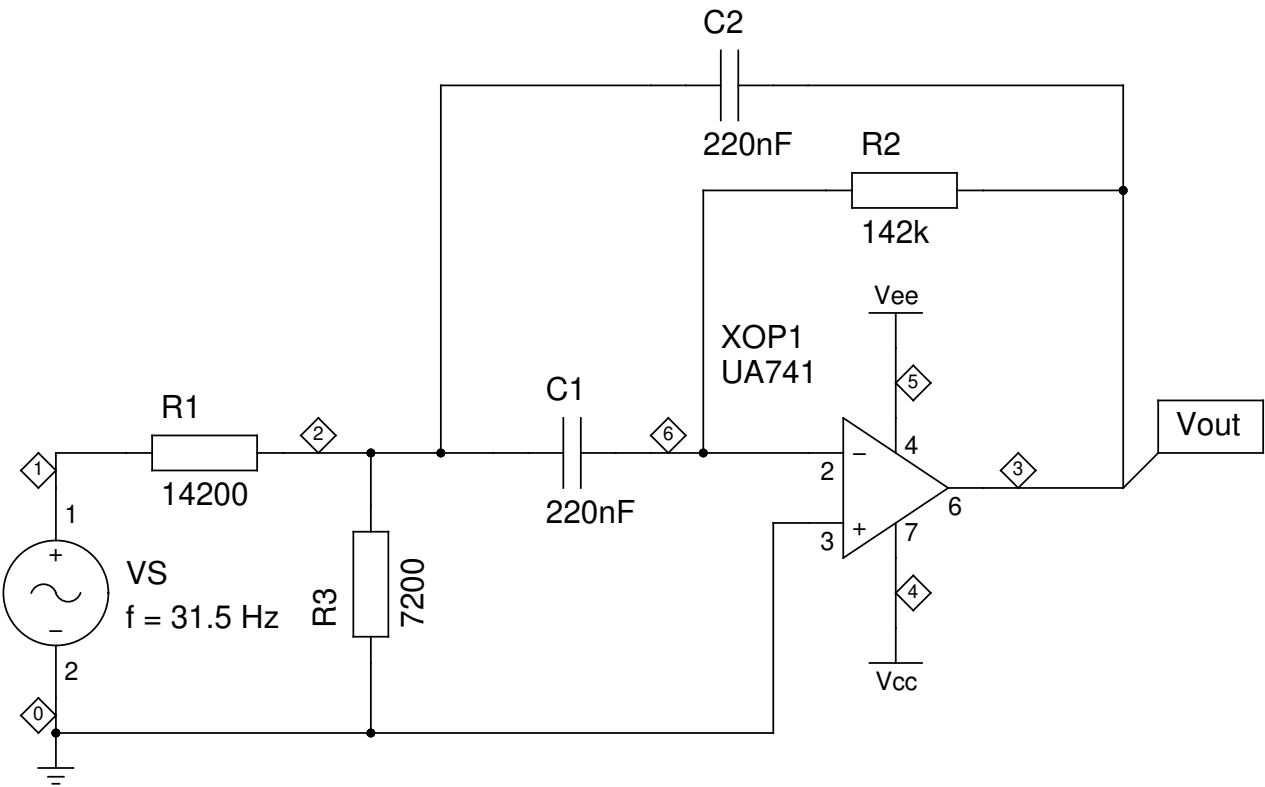
VS 1 0 AC 1 SIN(0 1.41 31.5)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.2 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.06.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

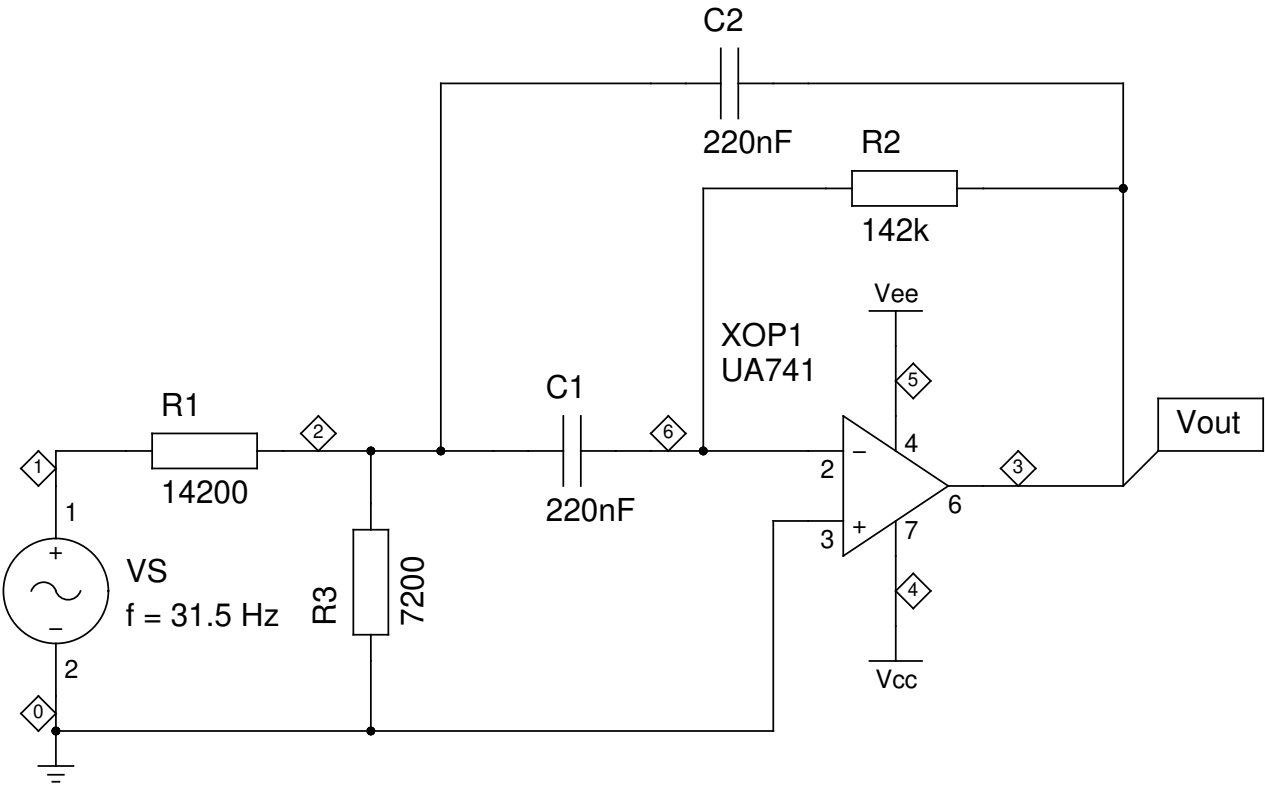
VS 1 0 AC 1 SIN(0 1.41 31.5)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.2 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.07.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

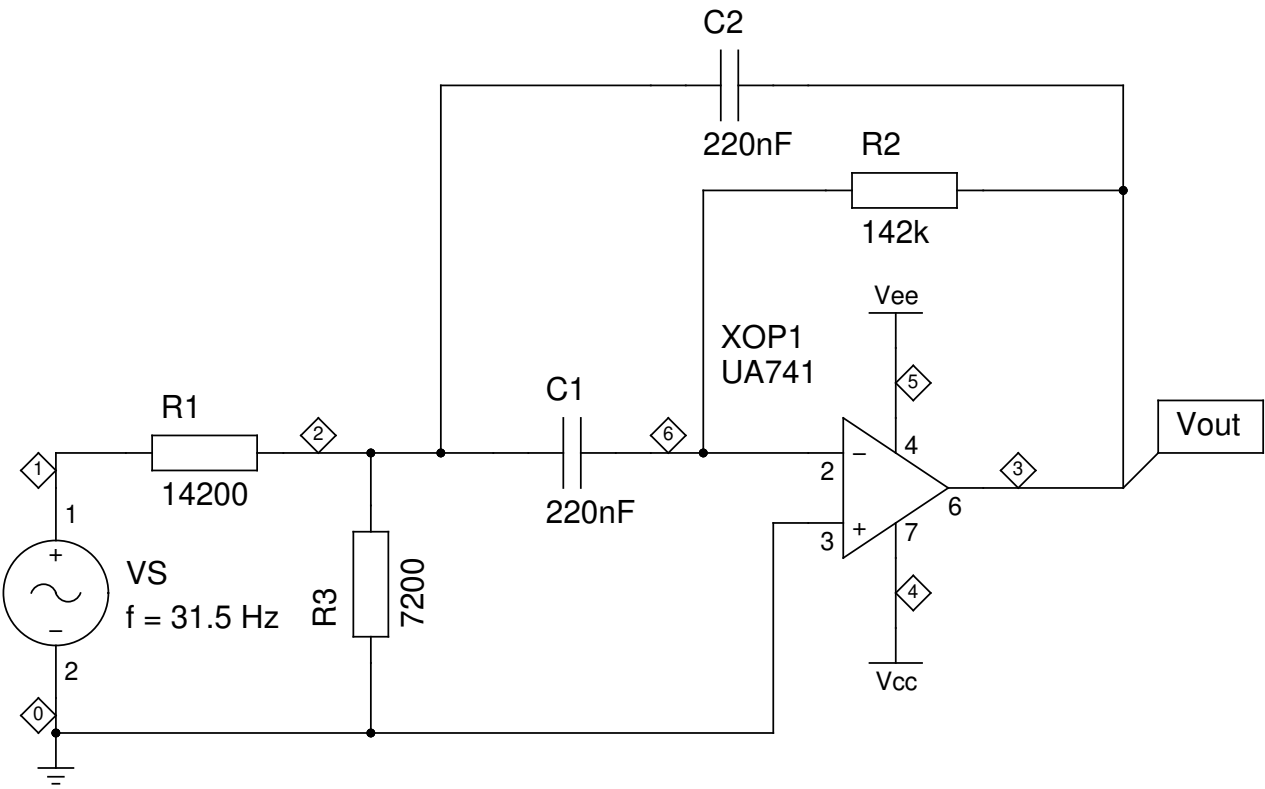
VS 1 0 AC 1 SIN(0 1.41 31.5)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.2 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.08.sch

REVISION: 20220422

PAGE 01

OF 01

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A3



.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

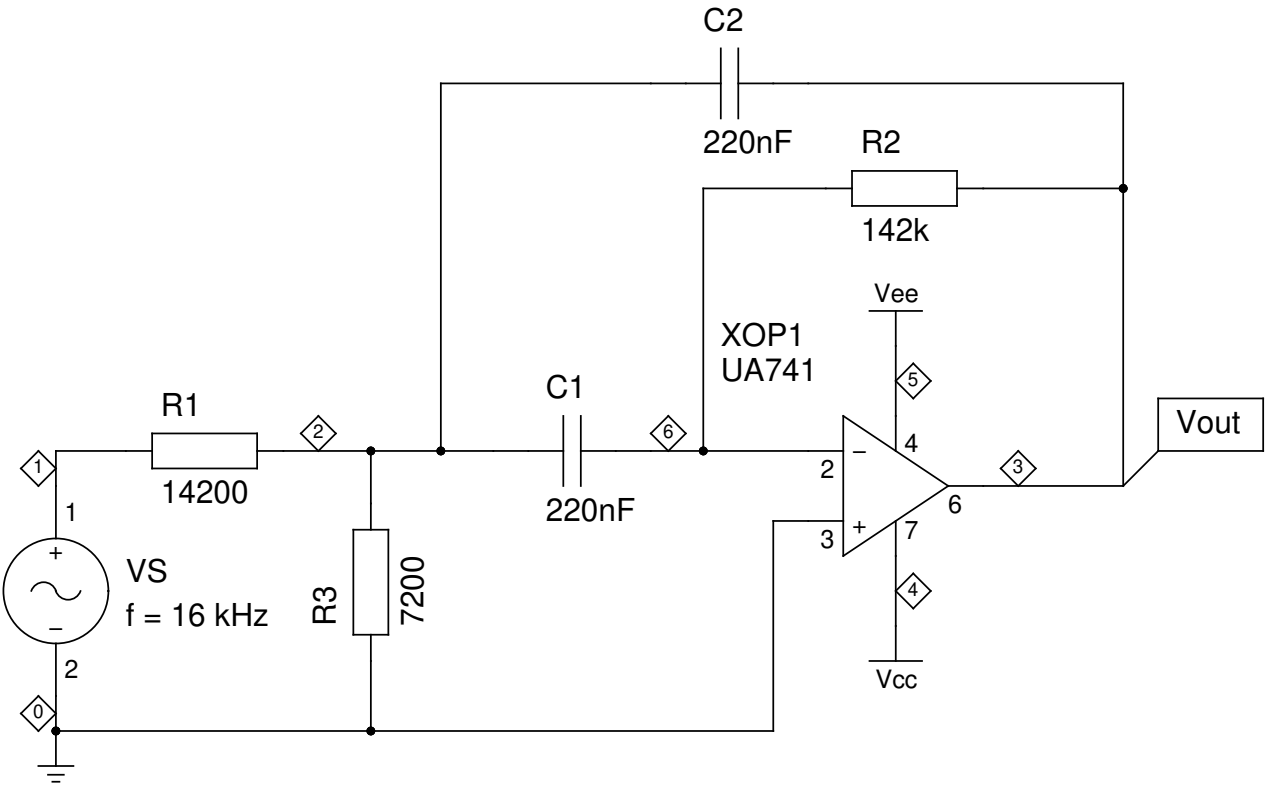
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.01 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.09.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

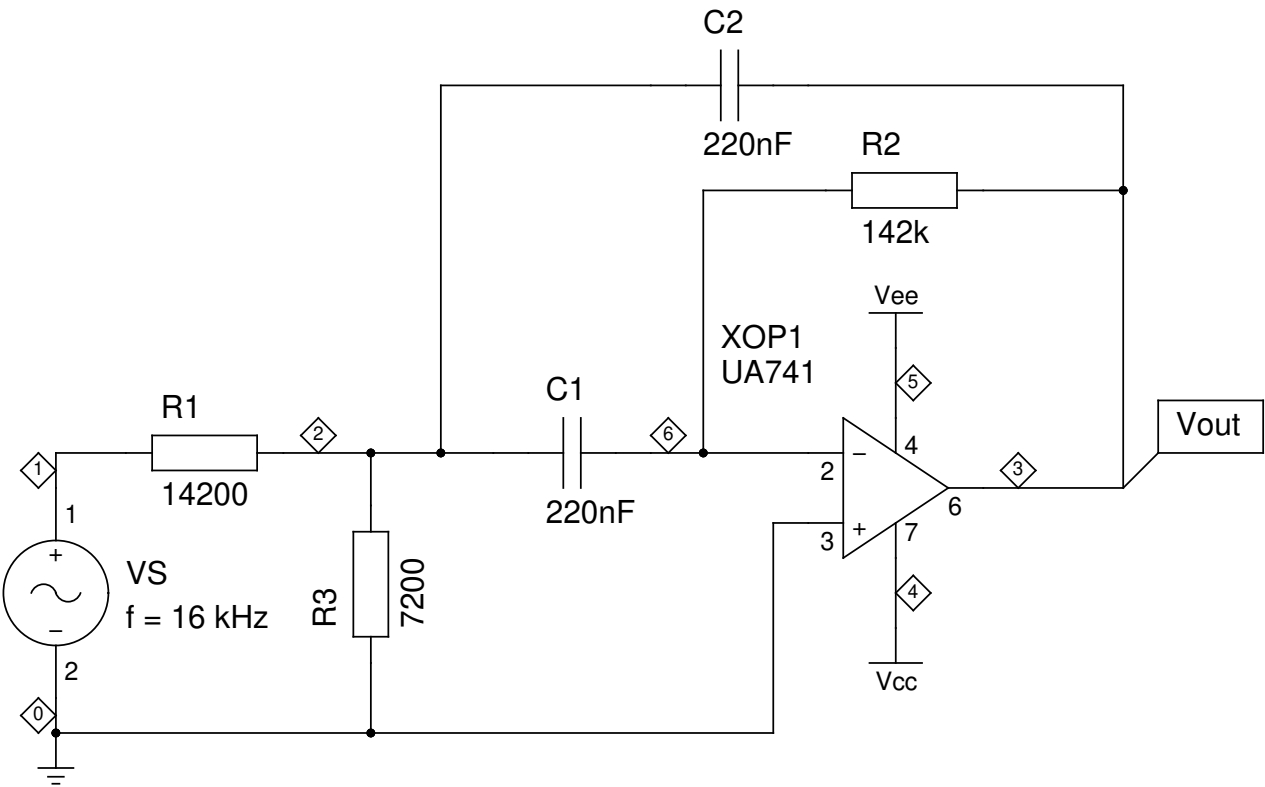
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.001 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.10.sch  
PAGE 01 OF 01

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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

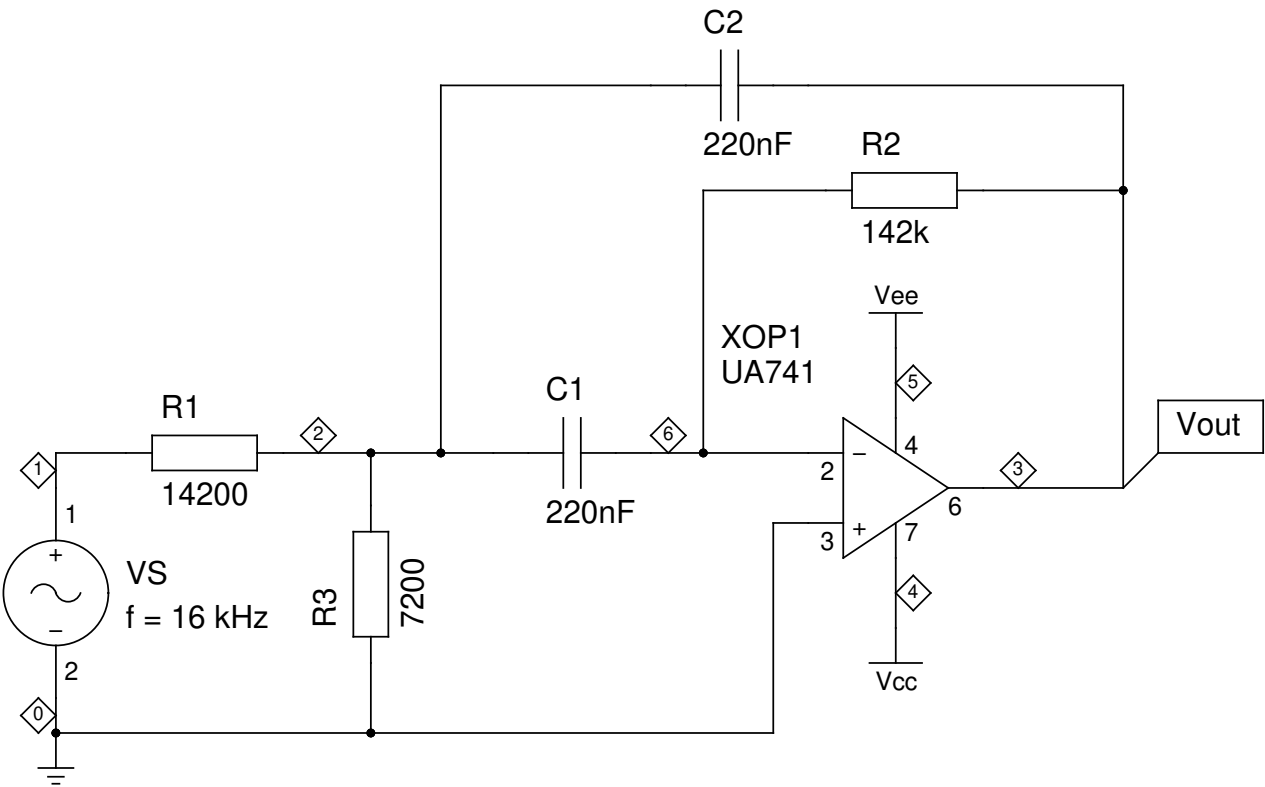
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 14200  
R2 3 6 142K  
R3 0 2 7200  
C1 2 6 220nF  
C2 3 2 220nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.001 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.11.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – DETECTOR STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 31.5)

C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

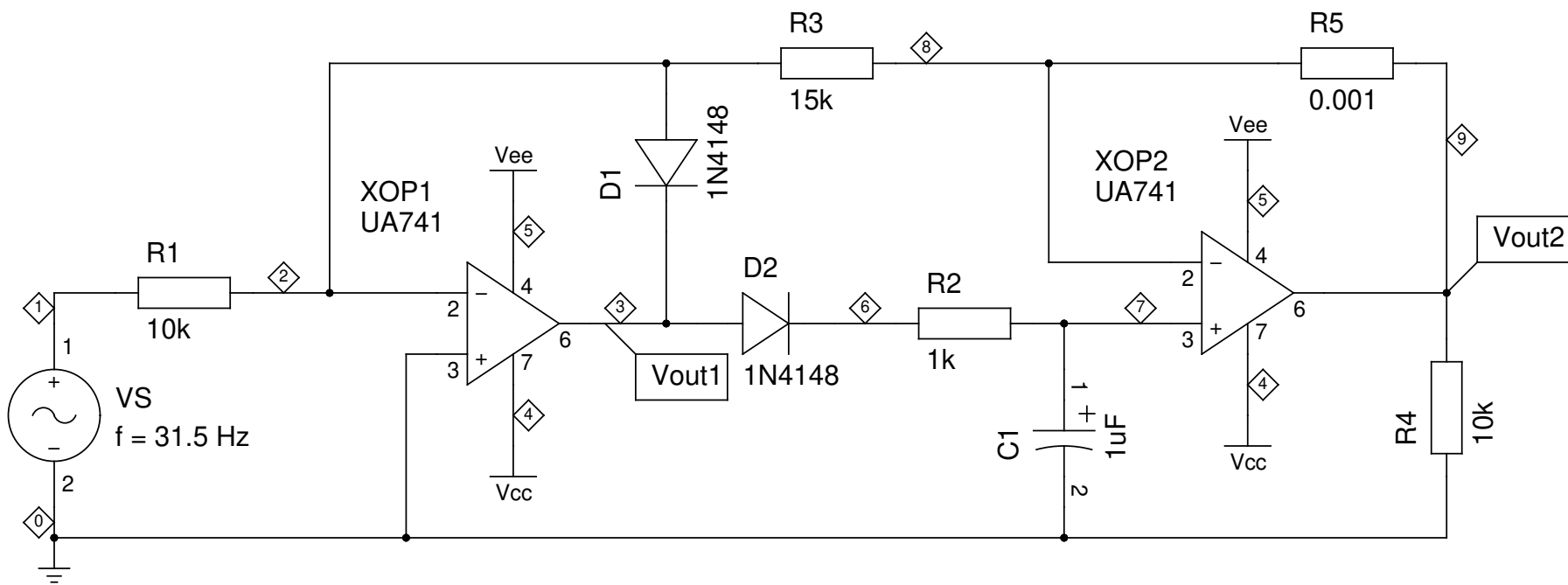
.PRINT OP Iter(0) V(3)

.PRINT AC VDB(3) VDB(9)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.1 100k

.END



Octave Filter – Second stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.12.sch  
PAGE 01 OF 01

REVISION: 20220422  
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A3

.TITLE OCTAVE FILTER – 31.5 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

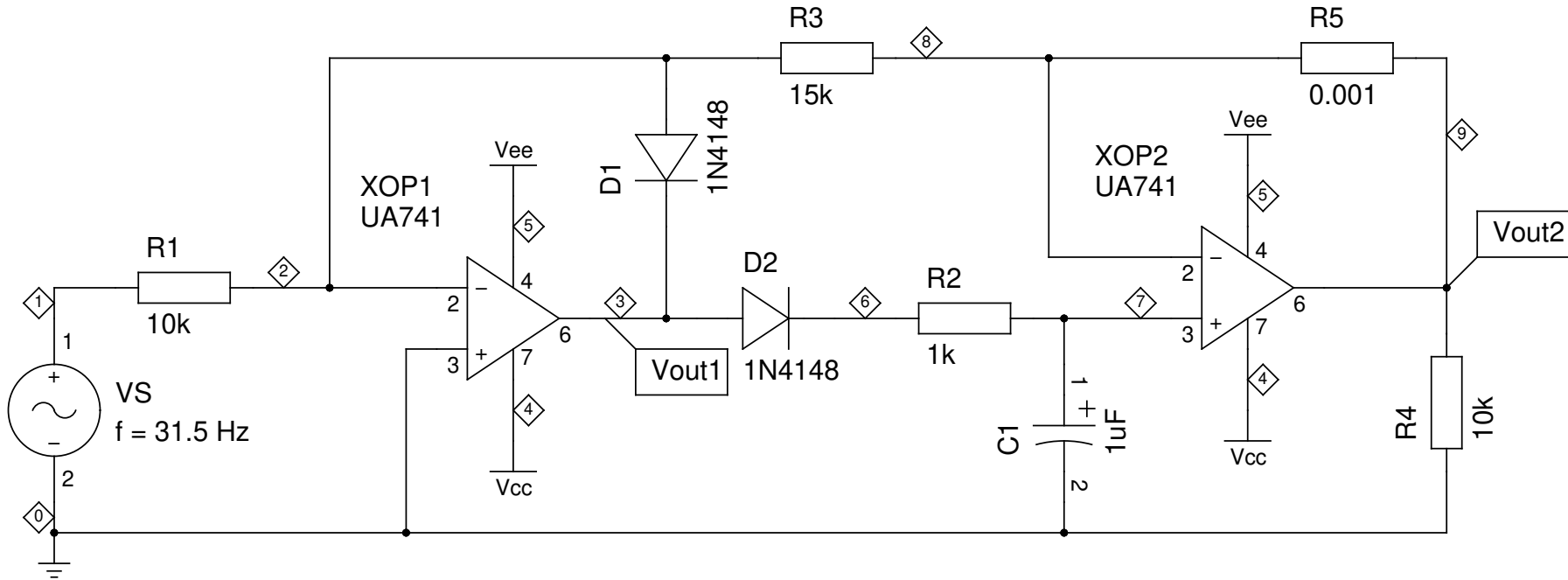
VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

VS 1 0 AC 1 SIN(0 1.41 31.5)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Iiter(0) V(3)

.PRINT TRAN V(1) V(3) V(9) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.4 0.00001 TRACE ALL  
.END



Octave Filter – Second stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.13.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 31.5 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

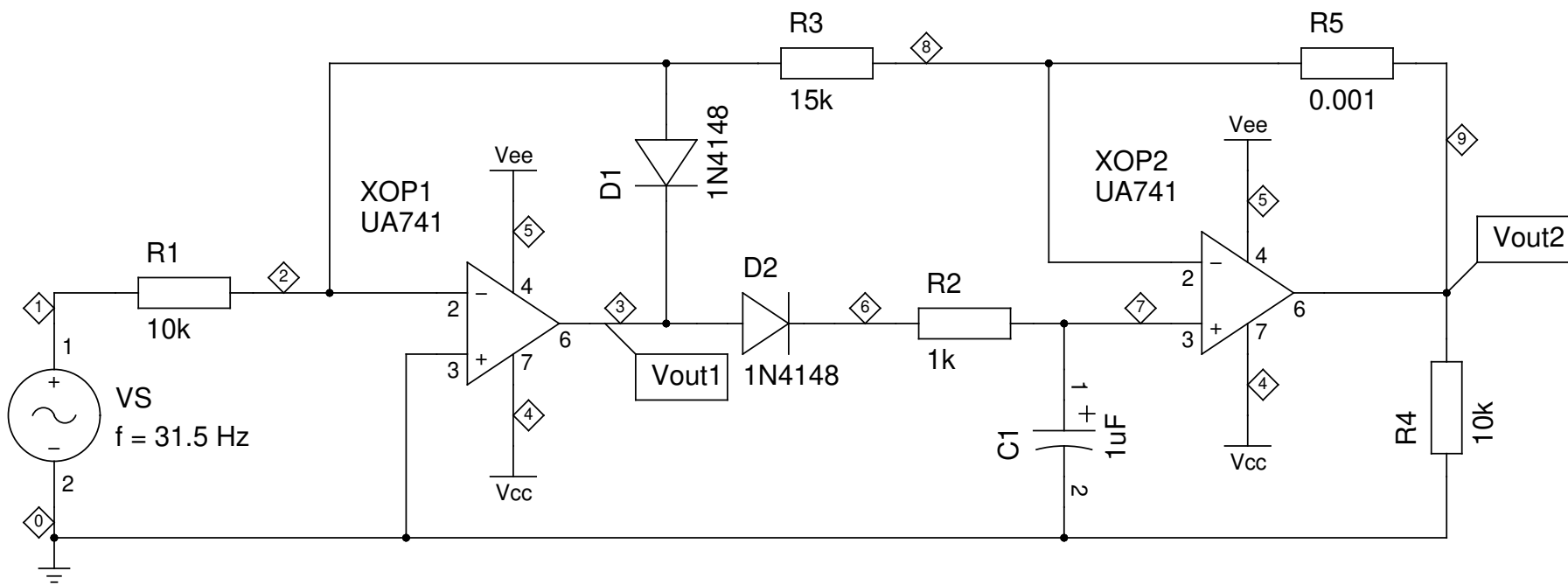
VS 1 0 AC 1 SIN(0 1.41 31.5)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3) I(R4) I(R5)

\* FROM TO STEP  
.TRAN 0 0.4 0.00001 TRACE ALL

.END



Octave Filter – Second stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.14.sch  
PAGE 01 OF 01

REVISION: 20220422  
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A3

.TITLE OCTAVE FILTER – 31.5 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

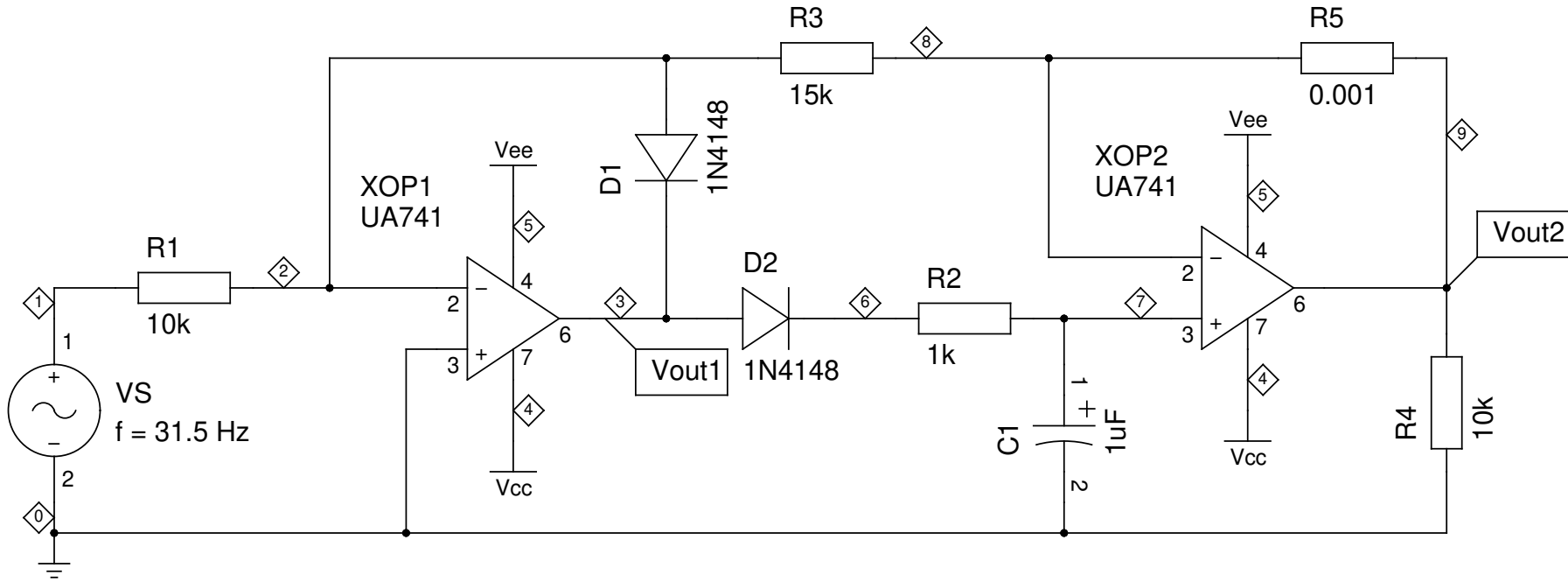
VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

VS 1 0 AC 1 SIN(0 1.41 31.5)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3) P(R4) P(R5) P(D1) P(D2)

\* FROM TO STEP  
.TRAN 0 0.4 0.00001 TRACE ALL  
.END



Octave Filter – Second stage of the 31.5 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.15.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 100)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

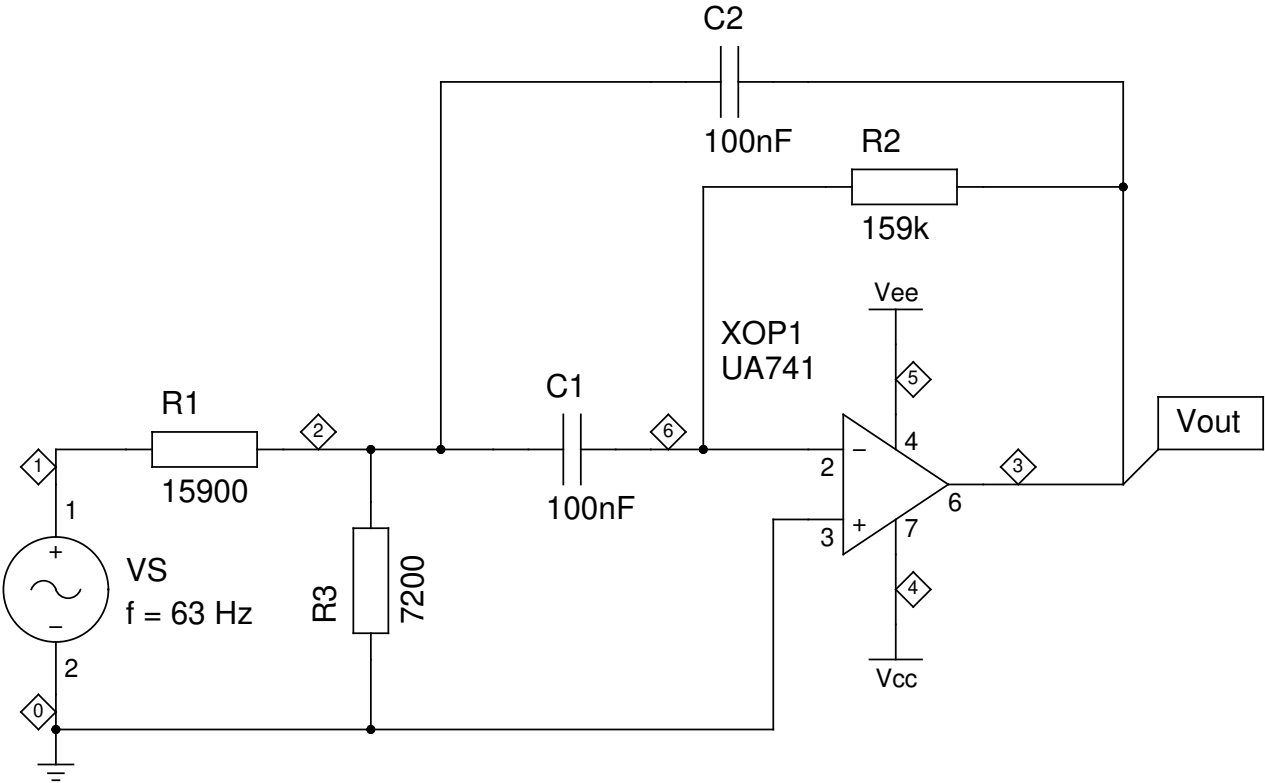
.PRINT OP Iter(0) V(3)

.PRINT AC VDB(3)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.1 100k

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.16.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

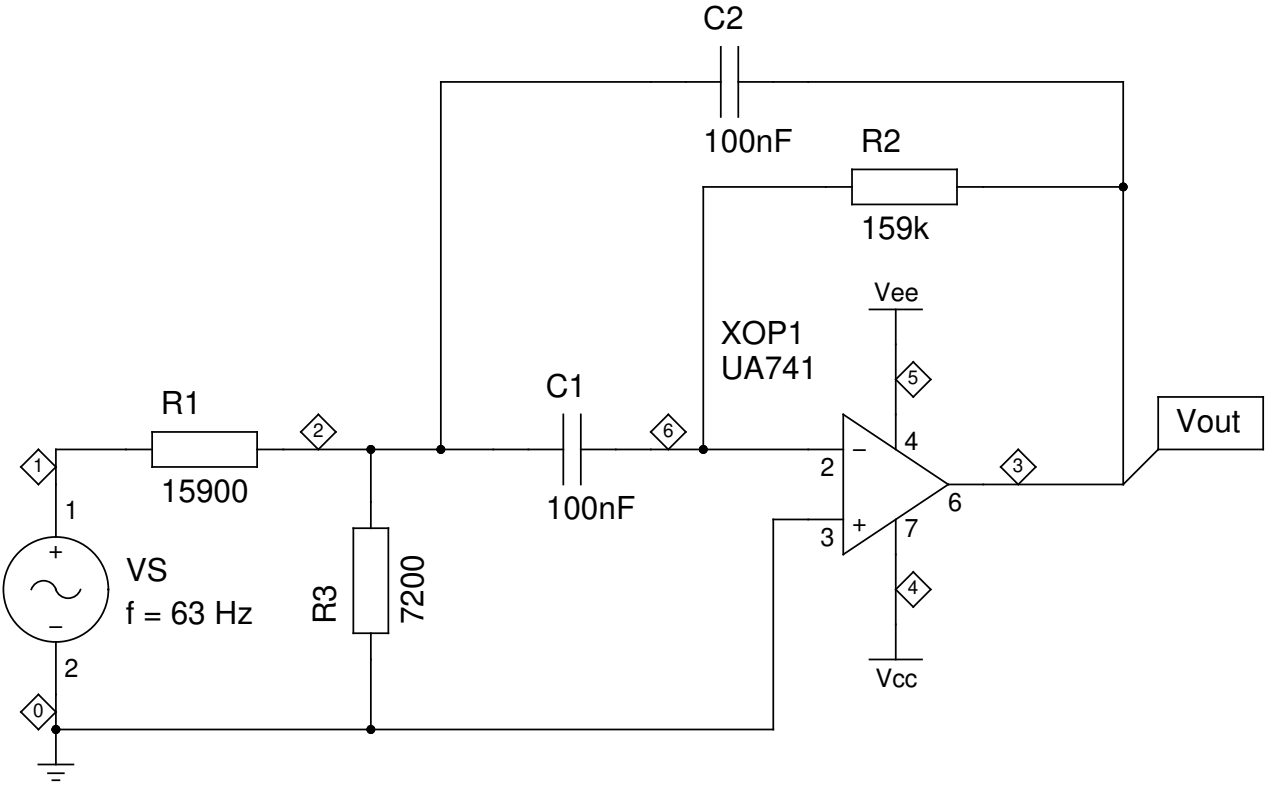
VS 1 0 AC 1 SIN(0 0.141 63)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.2 0.0001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.17.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

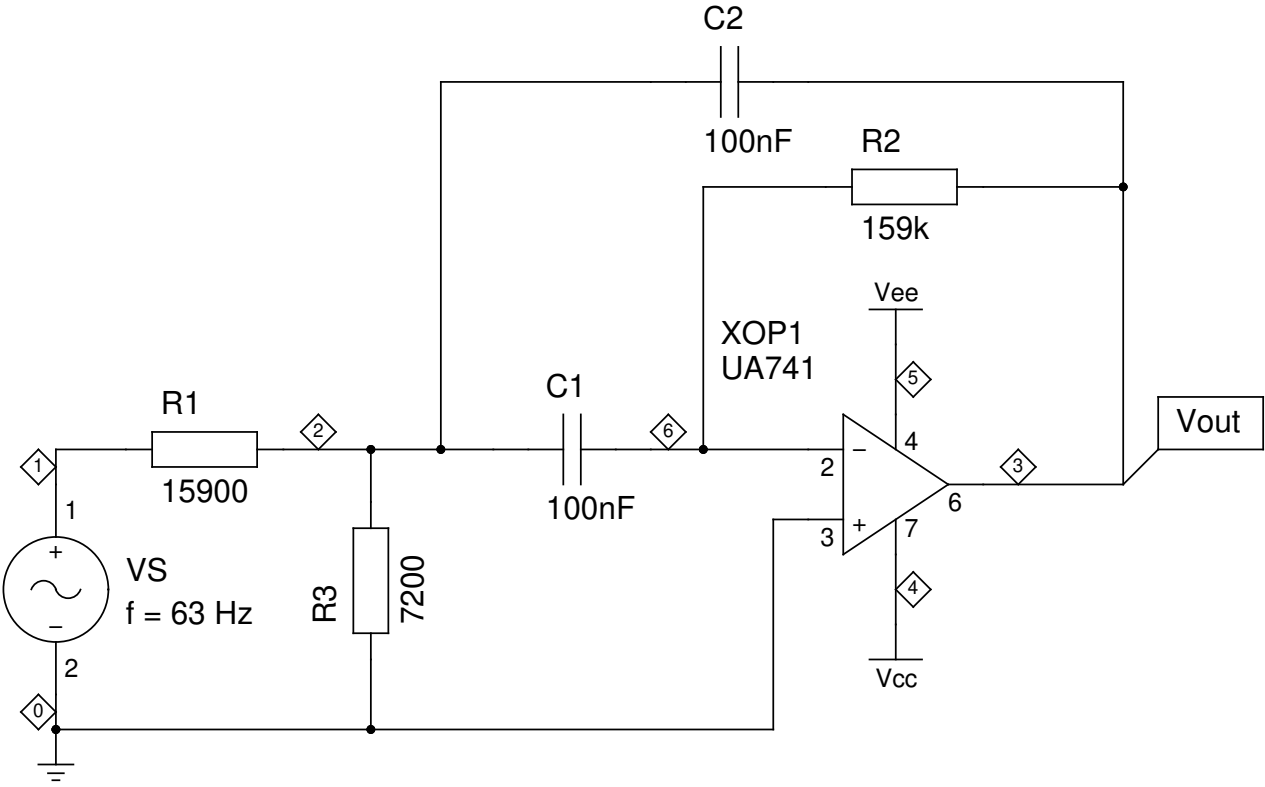
VS 1 0 AC 1 SIN(0 0.141 63)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.2 0.0001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.18.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

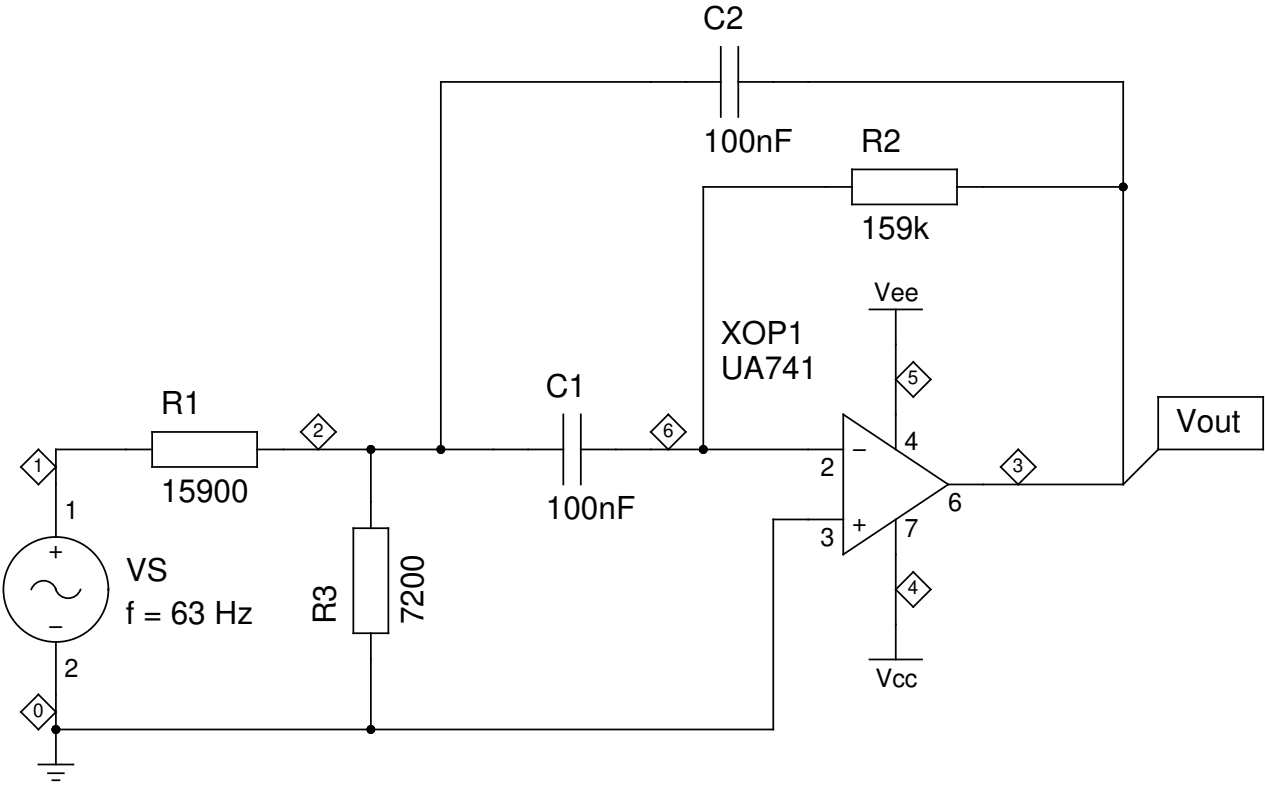
VS 1 0 AC 1 SIN(0 0.141 63)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.2 0.0001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.19.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

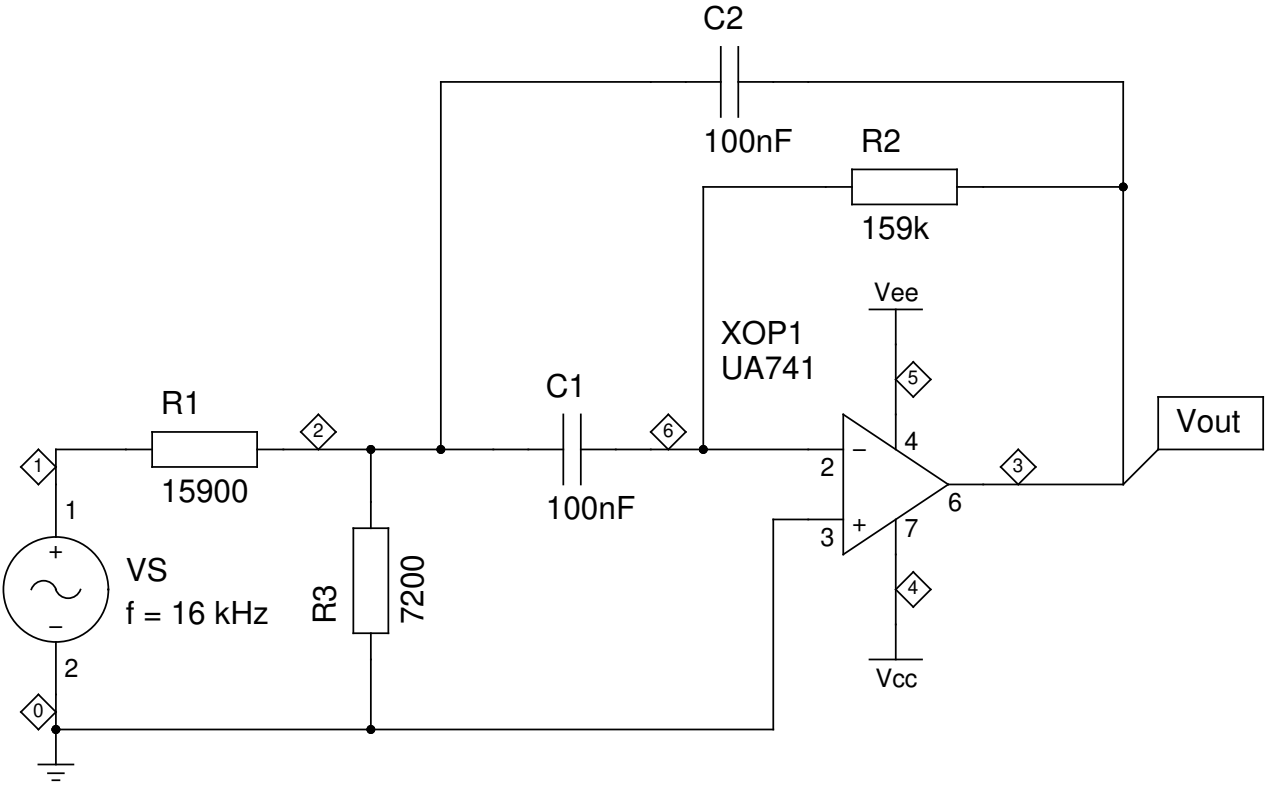
VS 1 0 AC 1 SIN(0 0.141 16k)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.01 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.20.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

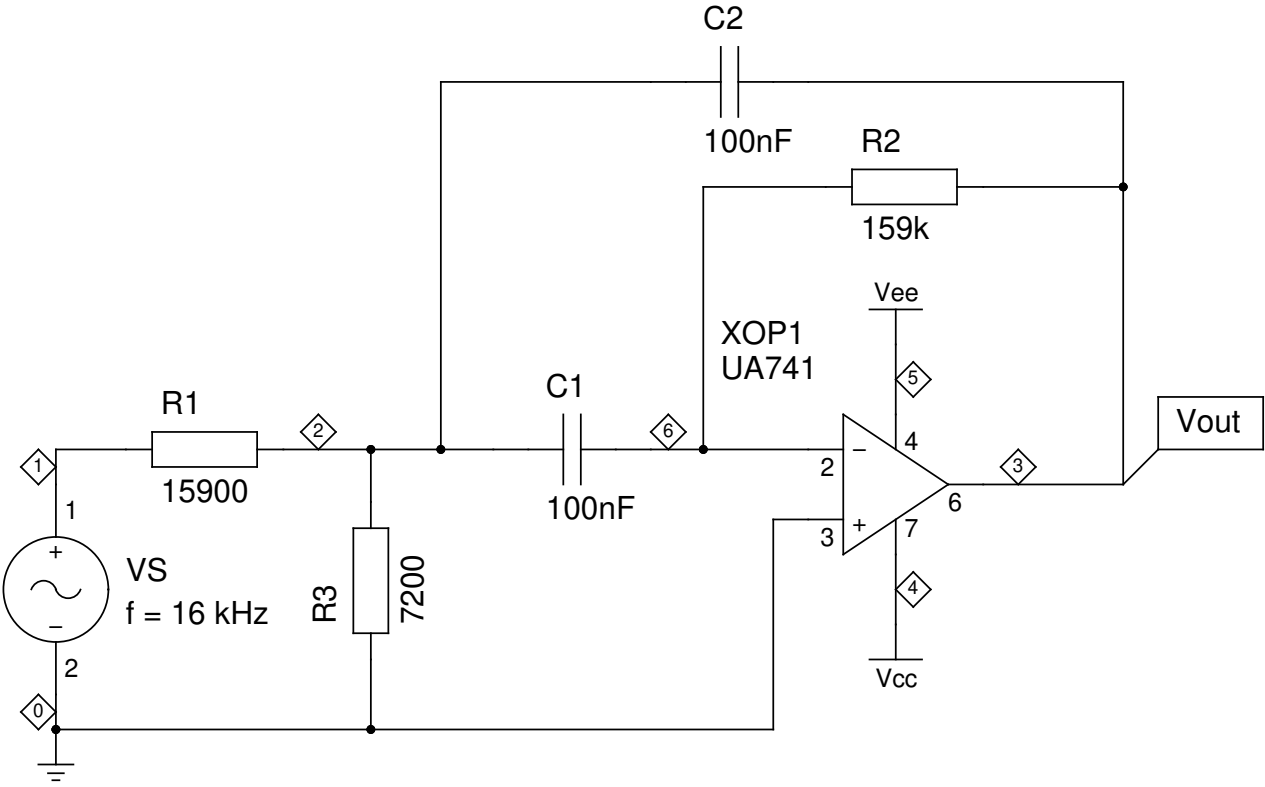
VS 1 0 AC 1 SIN(0 0.141 16k)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.01 0.0001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.21.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 63 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

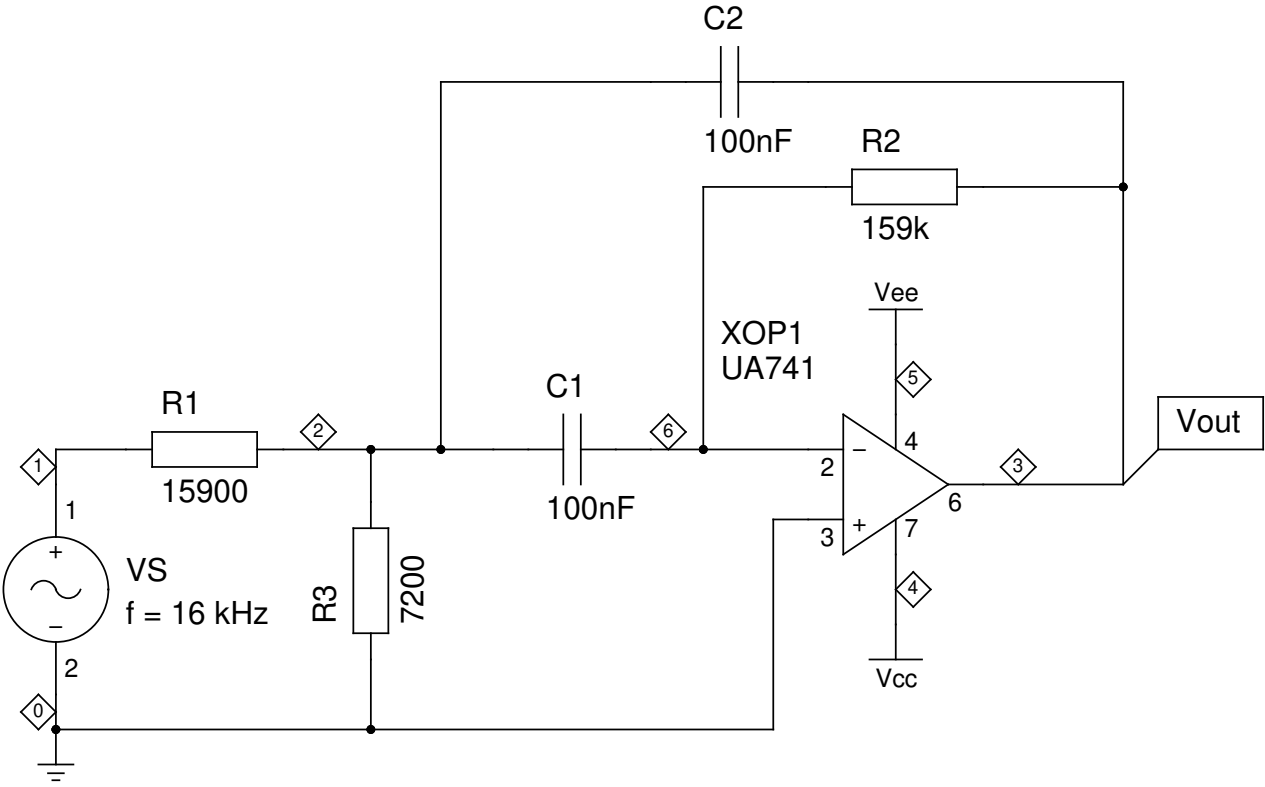
VS 1 0 AC 1 SIN(0 0.141 16k)  
R1 1 2 15900  
R2 3 6 159K  
R3 0 2 7200  
C1 2 6 100nF  
C2 3 2 100nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.01 0.0001 TRACE ALL

.END



Octave Filter – First stage of the 63 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.22.sch  
PAGE 01 OF 01

REVISION: 20220422  
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.TITLE OCTAVE FILTER – 63 HZ SECTION – DETECTOR STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 63)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

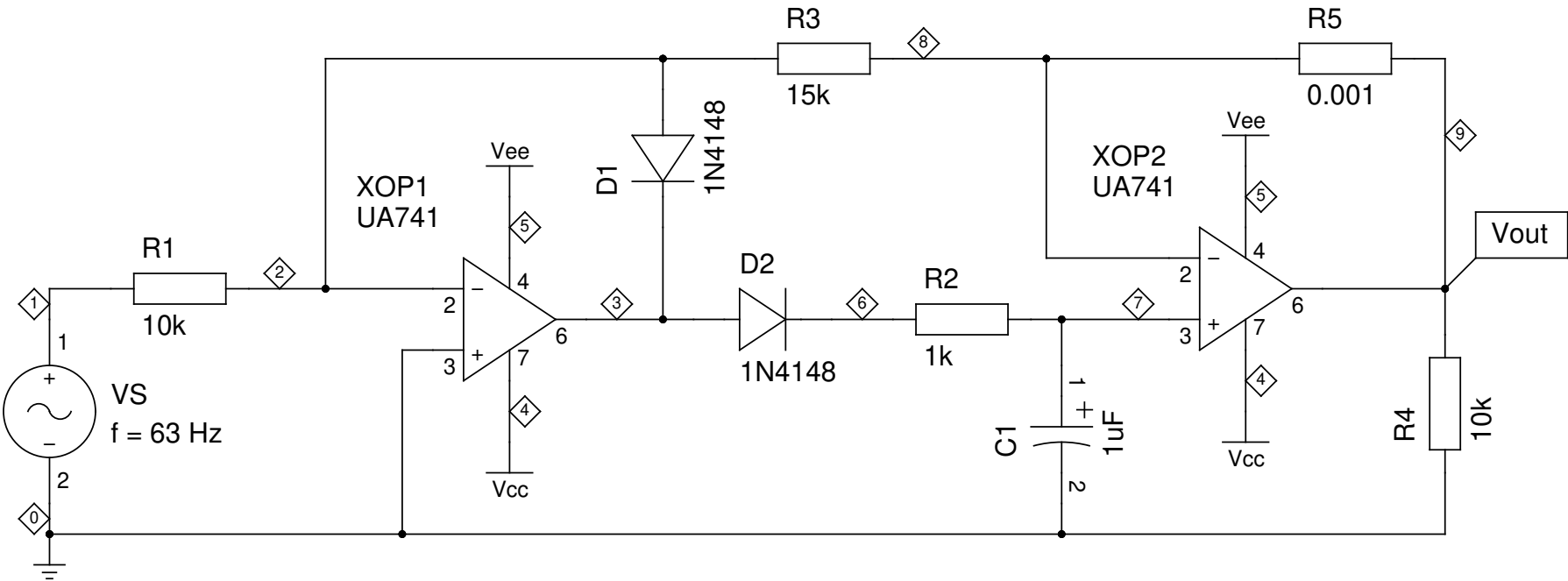
.PRINT OP Iiter(0) V(3)

.PRINT AC VDB(3) VDB(9)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.1 100k

.END



Octave Filter – Second stage of the 63 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.23.sch

PAGE 01 OF 01

REVISION: 20220422

DRAWN BY: Bert Timmerman

A3

.TITLE OCTAVE FILTER – 63 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

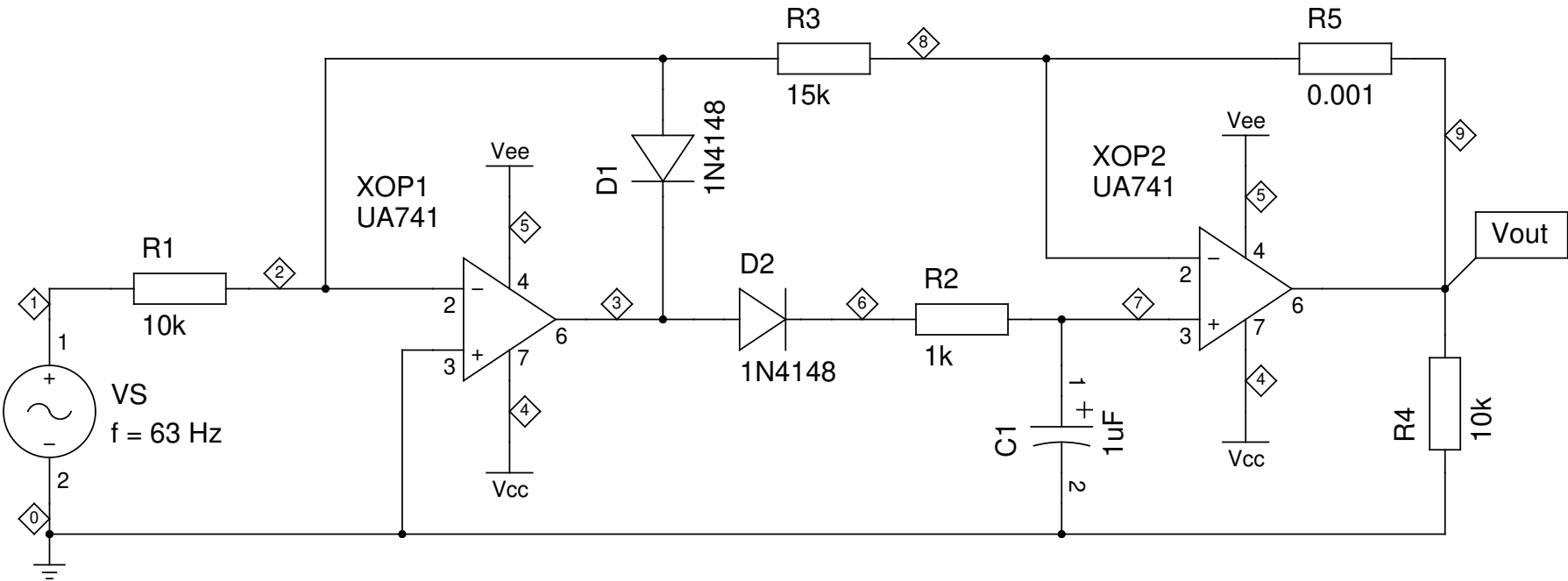
VS 1 0 AC 1 SIN(0 1.41 63)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Iter(0) V(3)

.PRINT TRAN V(1) V(3) V(9) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.4 0.00001

.END



Octave Filter – Second stage of the 63 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.24.sch

PAGE 01 OF 01

REVISION: 20220422

DRAWN BY: Bert Timmerman

A3



.TITLE OCTAVE FILTER – 63 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

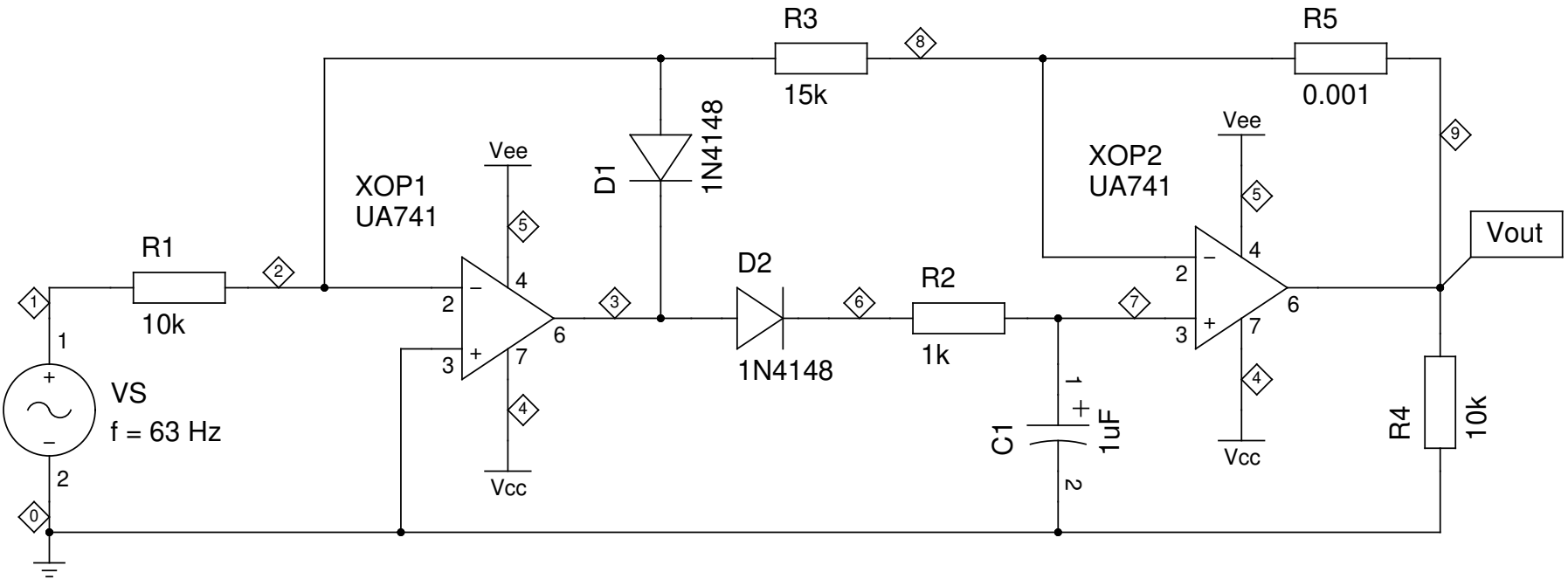
VS 1 0 AC 1 SIN(0 1.41 63)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3) I(R4) I(R5) I(D1) I(D2)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001

.END



Octave Filter – Second stage of the 63 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.25.sch

PAGE 01 OF 01

REVISION: 20220422

DRAWN BY: Bert Timmerman

A3

.TITLE OCTAVE FILTER – 63 HZ SECTION – DETECTOR STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

.MODEL 1N4148 D IS=2e-14

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

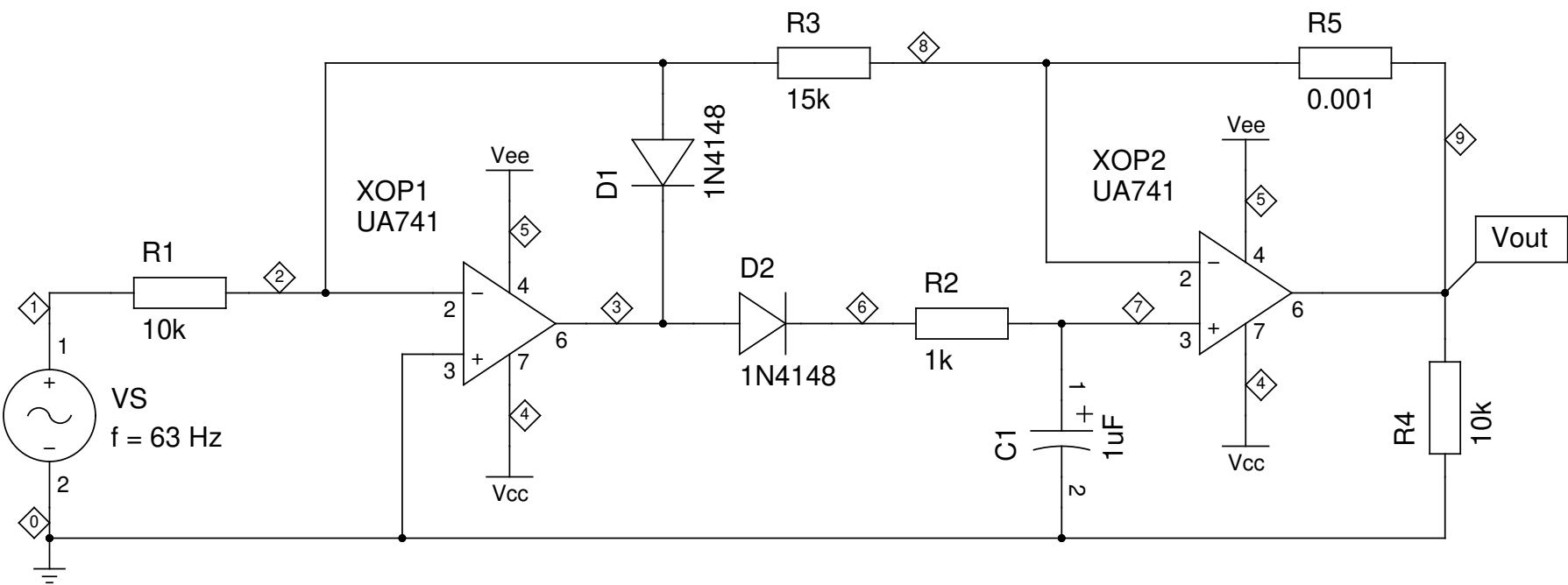
VS 1 0 AC 1 SIN(0 1.41 63)  
C1 0 7 1uF  
D1 2 3 1N4148  
D2 3 6 1N4148  
R1 1 2 10000  
R2 6 7 1000  
R3 8 2 15000  
R4 0 9 10000  
R5 8 9 .001  
XOP1 0 2 0 4 5 3 UA741  
XOP2 7 8 0 4 5 9 UA741

.PRINT OP Iiter(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3) P(R4) P(R5) P(D1) P(D2)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001

.END



Octave Filter – Second stage of the 63 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.26.sch

PAGE 01 OF 01

REVISION: 20220422

DRAWN BY: Bert Timmerman

A3

.TITLE OCTAVE FILTER – 125 HZ MODULE – FIRST STAGE – FREQUENCY RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 15  
VEE 5 0 -15

VS 1 0 AC 1 SIN(0 0.1 100)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

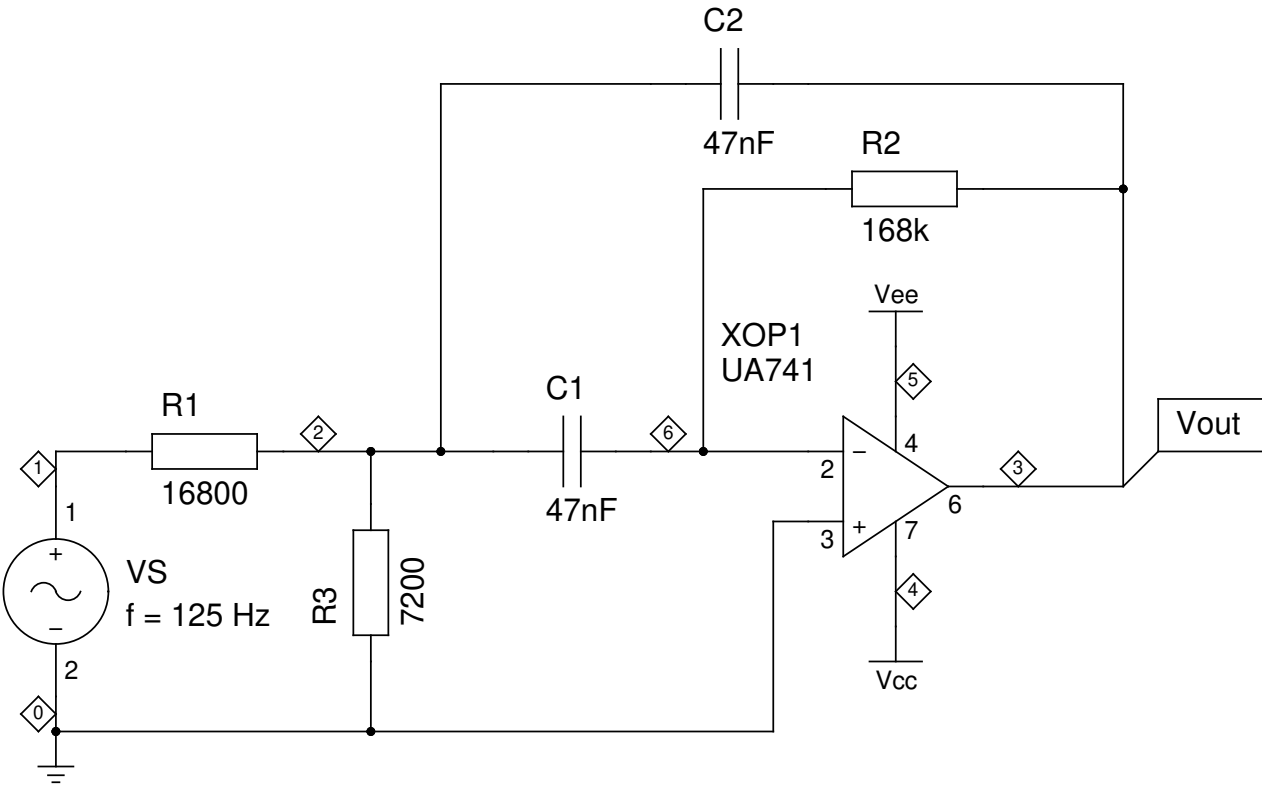
.PRINT OP Iter(0) V(3)

.PRINT AC VDB(3)

\* FROM TO STEP  
.TRAN 0.00001 0.2 0.0001

\* #STEPS/DECADE FROM TO  
.AC DEC 20 0.1 100k

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.27.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

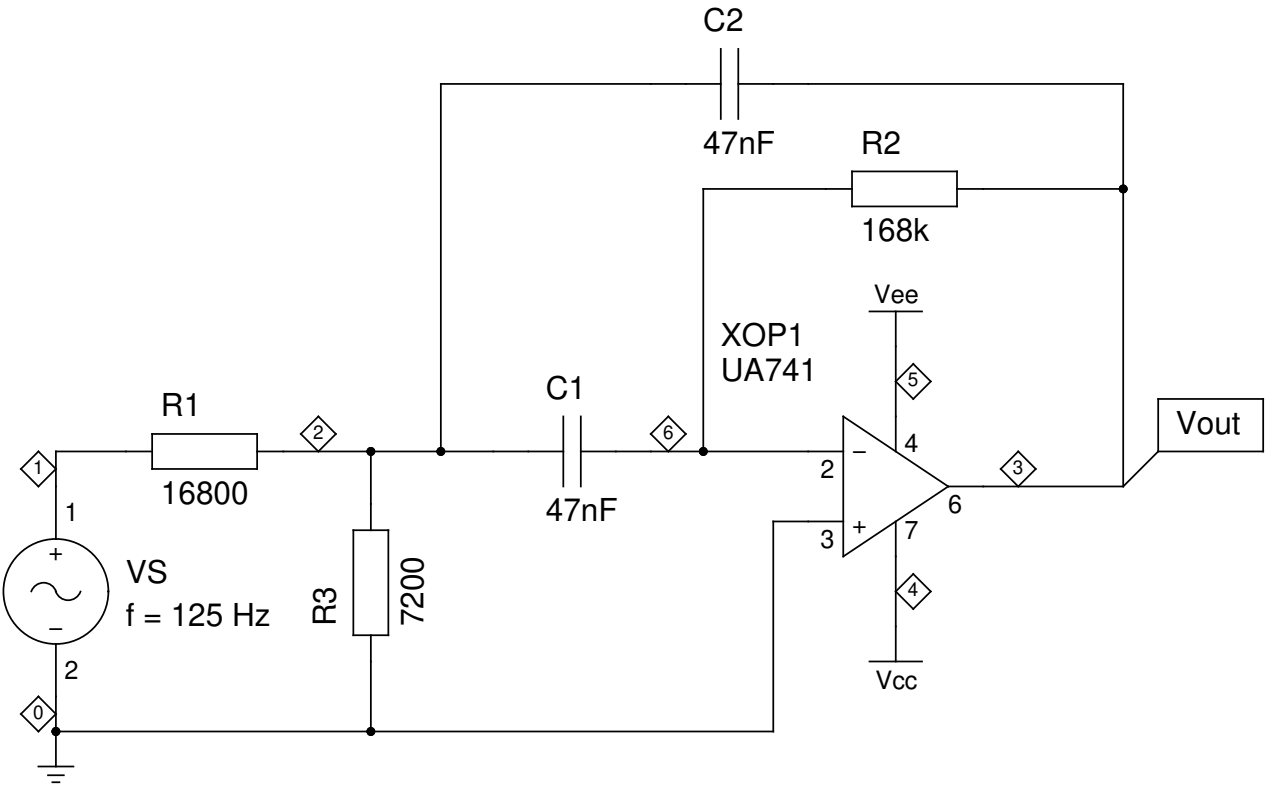
VS 1 0 AC 1 SIN(0 1.41 125)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.28.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

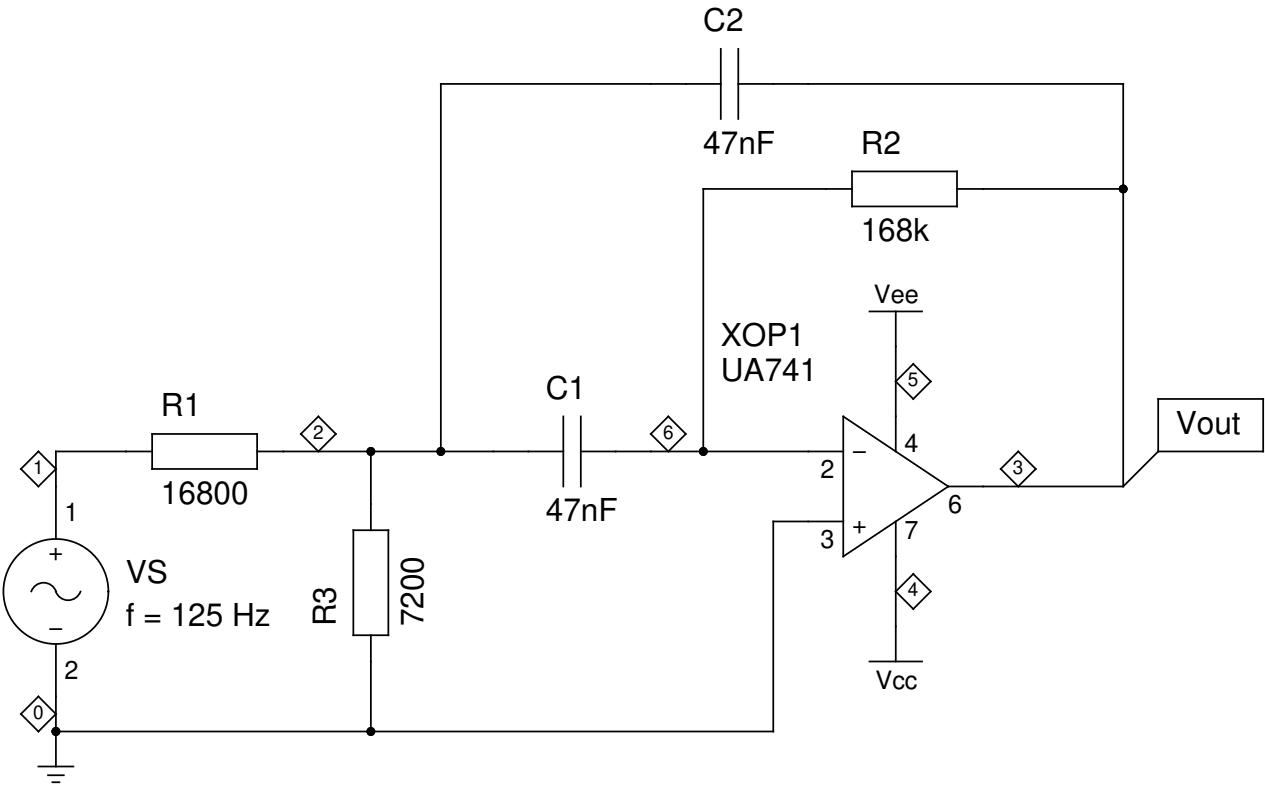
VS 1 0 AC 1 SIN(0 1.41 125)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic

TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.29.sch

PAGE 01 OF 01

REVISION: 20220422

DRAWN BY: Bert Timmerman

A3

.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

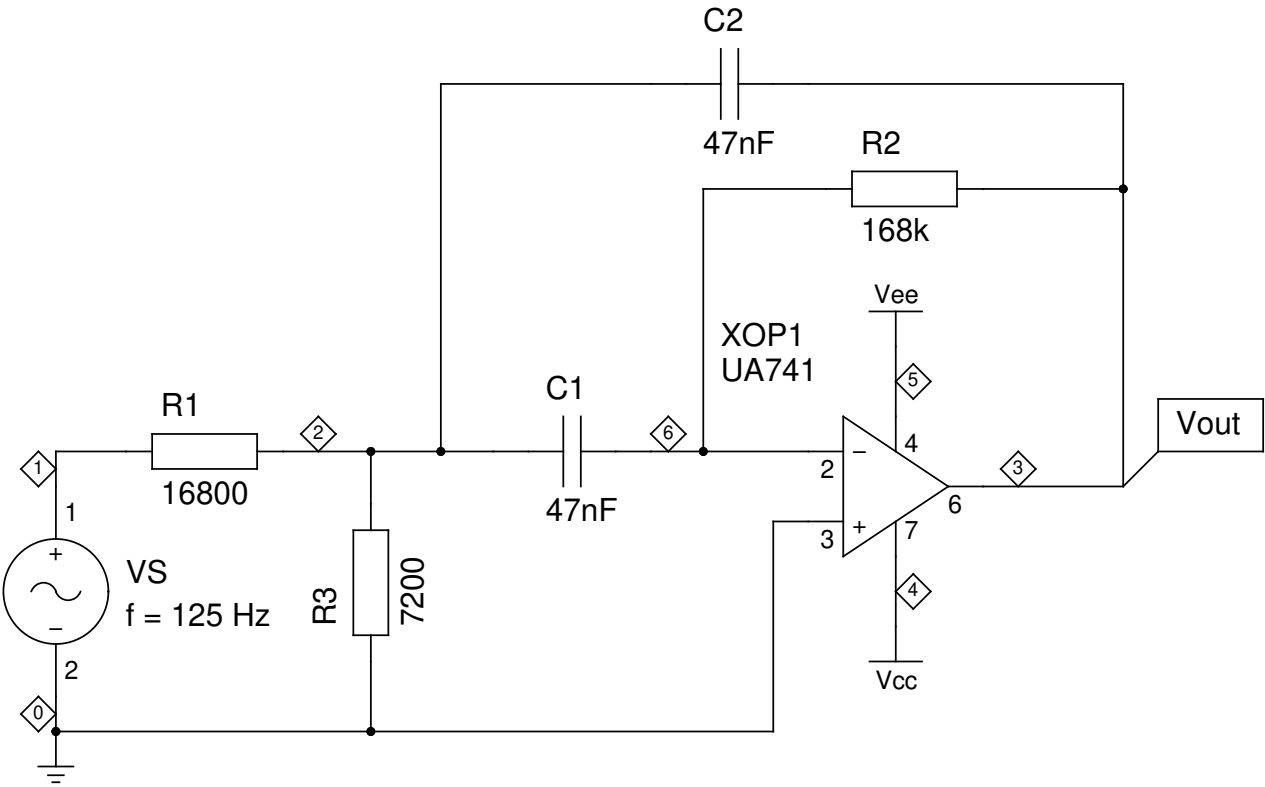
VS 1 0 AC 1 SIN(0 1.41 125)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.30.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

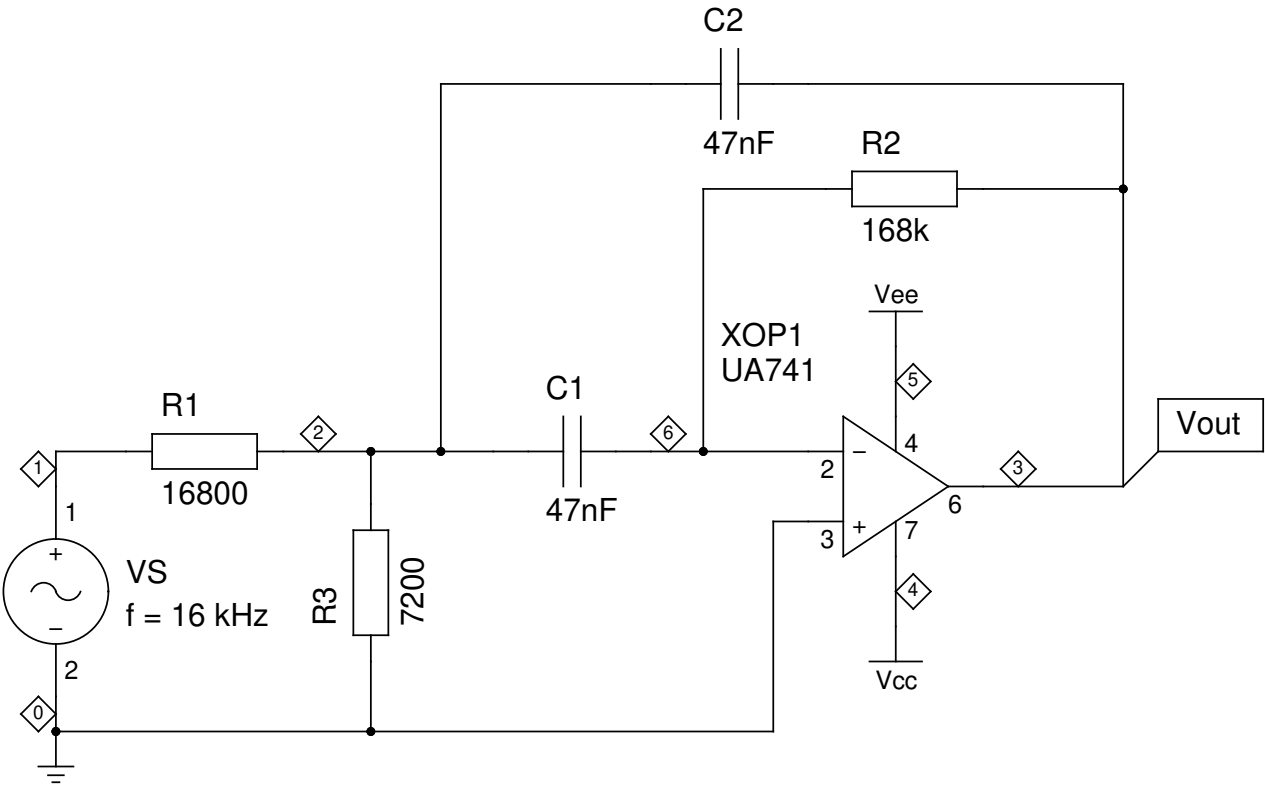
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Iiter(0) V(3)

.PRINT TRAN V(1) V(3) V(4) V(5)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.31.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman

.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

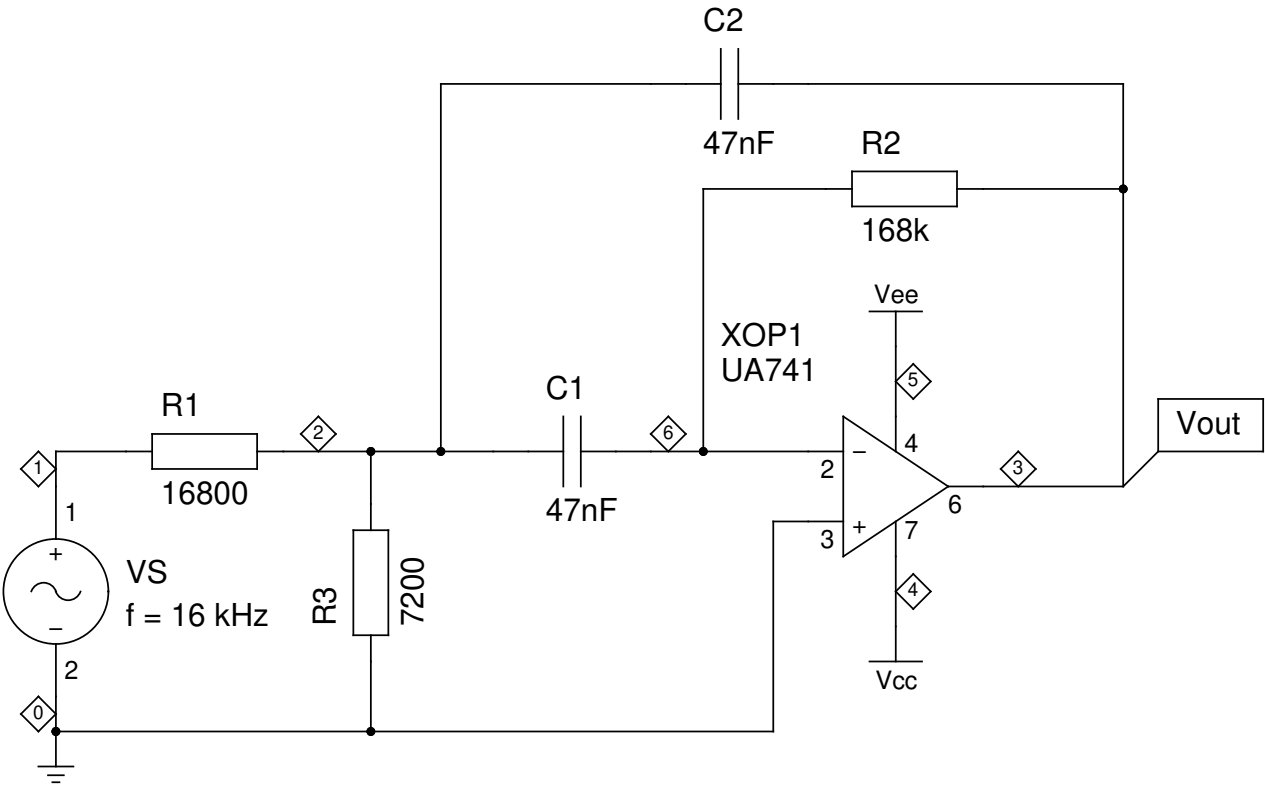
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN I(R1) I(R2) I(R3)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END



Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

FILE: 26.000.00.02.32.sch  
PAGE 01 OF 01

REVISION: 20220422  
DRAWN BY: Bert Timmerman



.TITLE OCTAVE FILTER – 125 HZ SECTION – BPF STAGE – TRANSIENT RESPONSE

.INCLUDE UA741.subckt

VCC 4 0 pulse(iv=0 pv=15 rise=.01)  
VEE 5 0 pulse(iv=0 pv=-15 rise=.01)

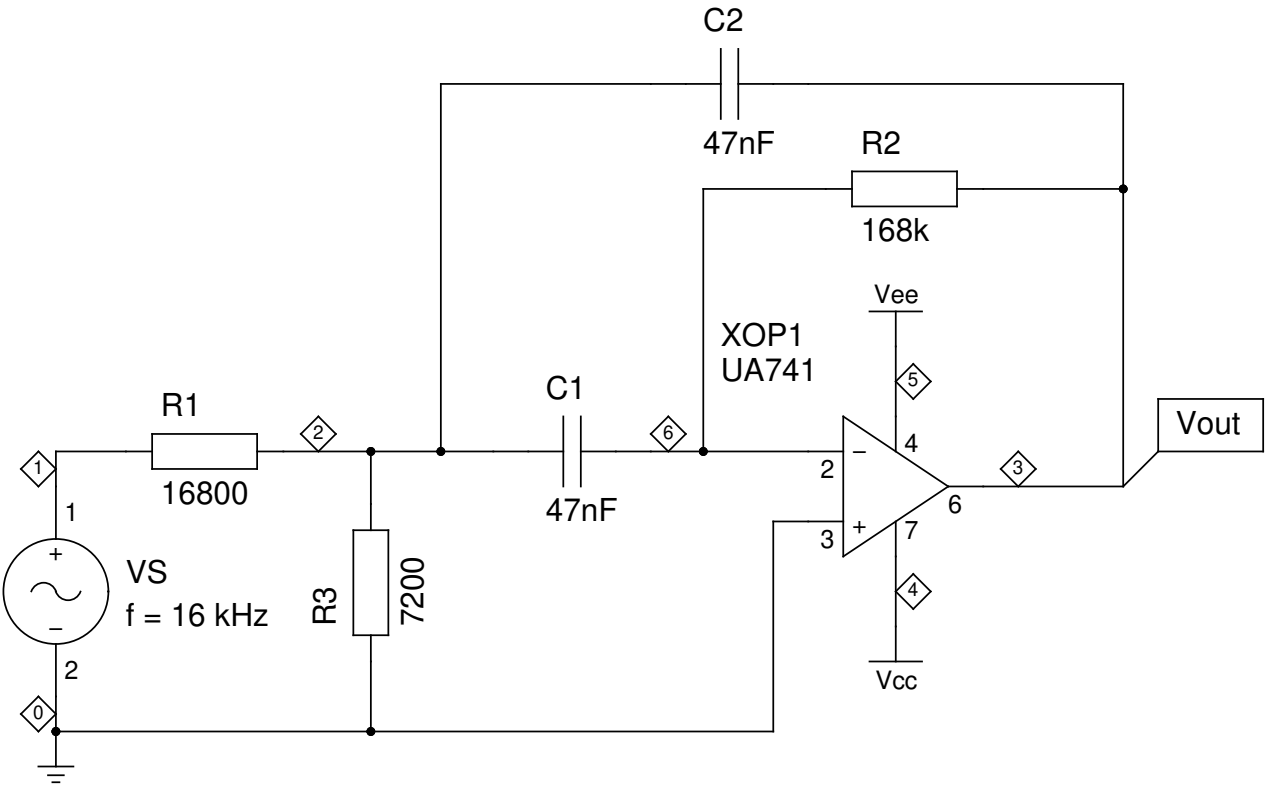
VS 1 0 AC 1 SIN(0 1.41 16k)  
R1 1 2 16800  
R2 3 6 168K  
R3 0 2 7200  
C1 2 6 47nF  
C2 3 2 47nF  
XOP1 0 6 0 4 5 3 UA741

.PRINT OP Itr(0) V(3)

.PRINT TRAN P(R1) P(R2) P(R3)

\* FROM TO STEP  
.TRAN 0 0.1 0.00001 TRACE ALL

.END

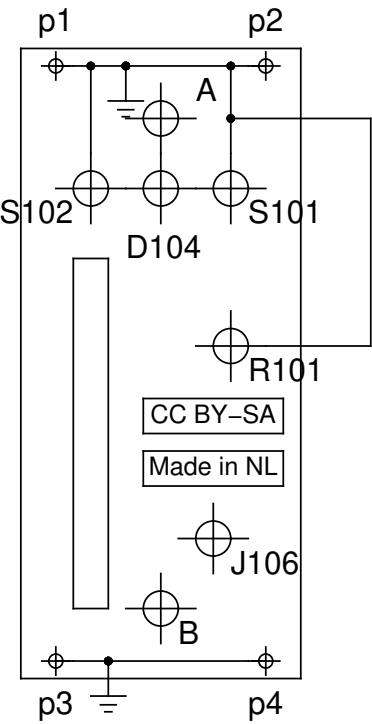


Octave Filter – First stage of the 125 Hz module (for simulation)  
schematic  
TITLE OCTAVE\_FILTER

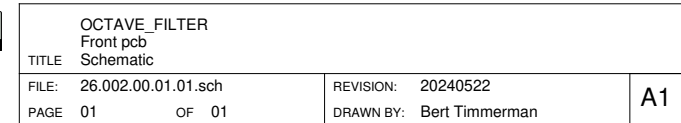
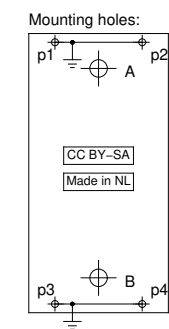
FILE: 26.000.00.02.33.sch  
PAGE 01 OF 01

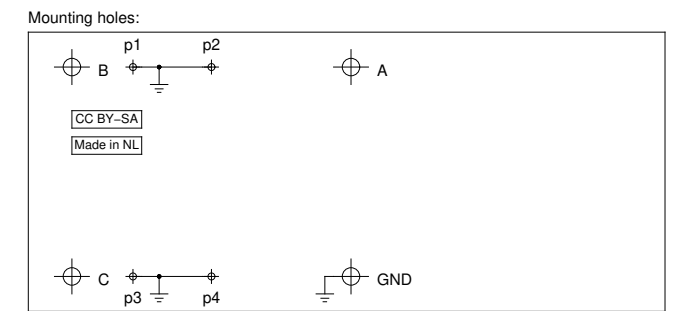
REVISION: 20220422  
DRAWN BY: Bert Timmerman

Mounting holes:

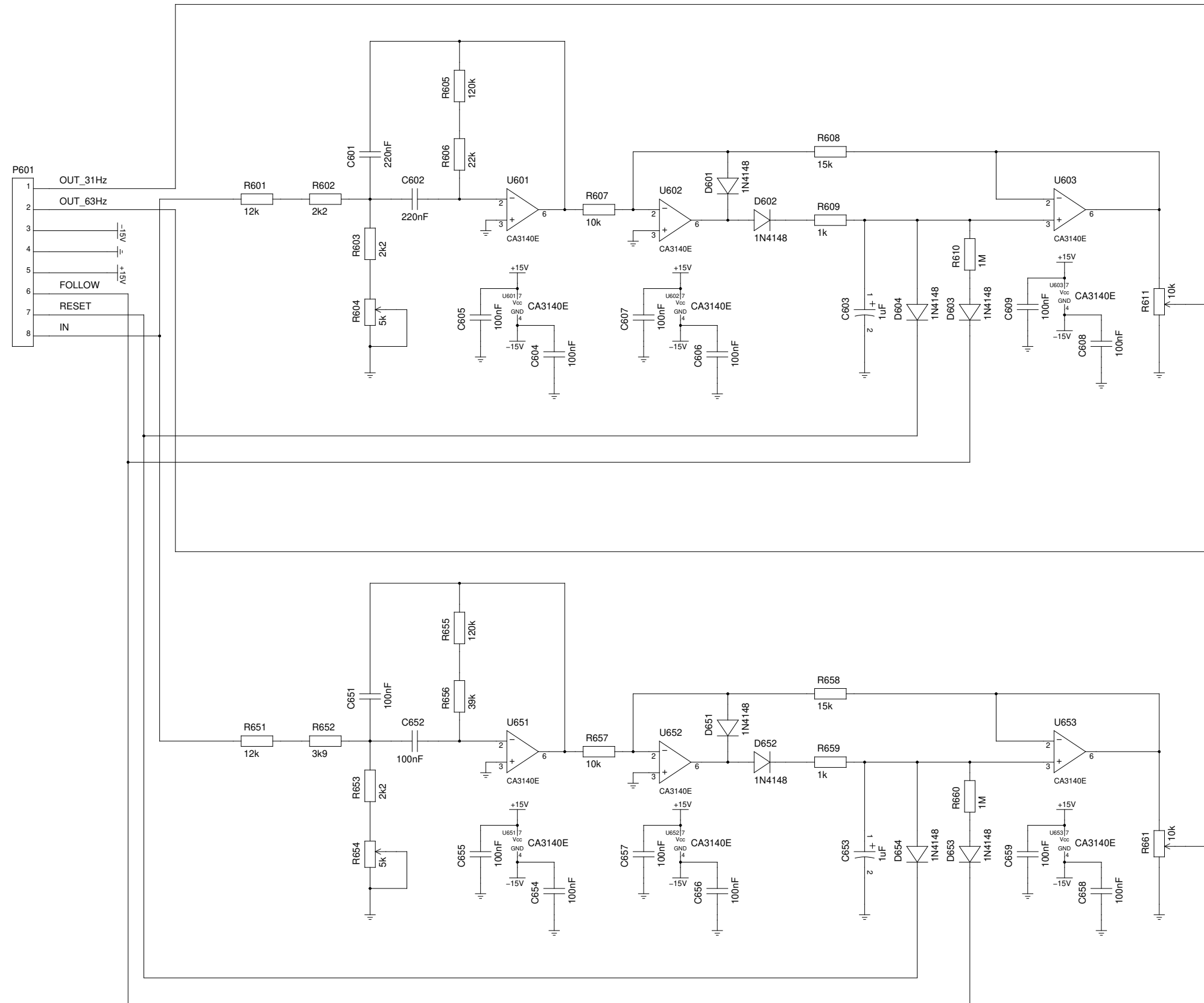


OCTAVE_FILTER Front panel			A3
TITLE Schematic (DFM)			
FILE: 26.001.00.01.01.sch	REVISION: 20240522		
PAGE 01	OF 01	DRAWN BY: Bert Timmerman	

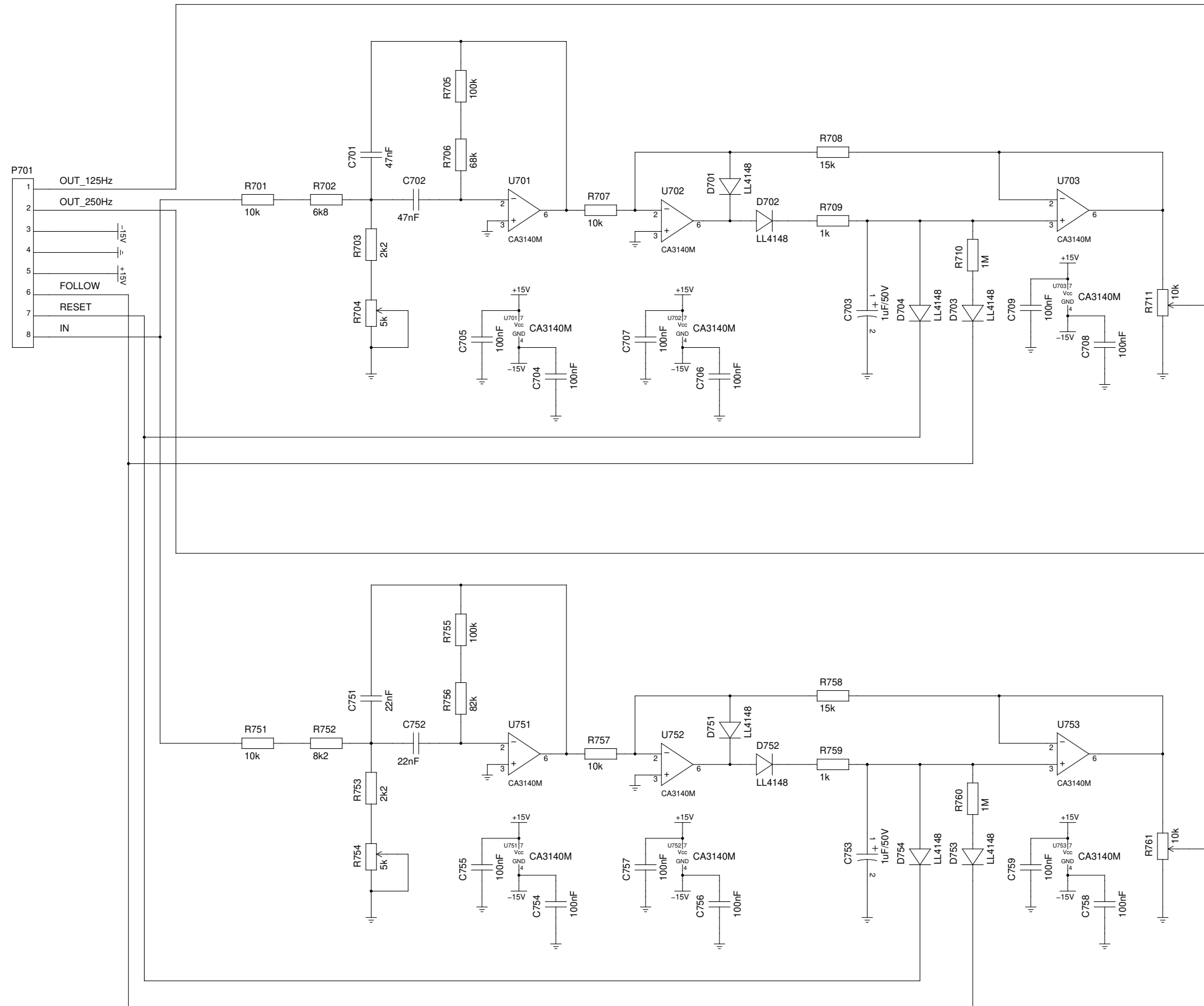




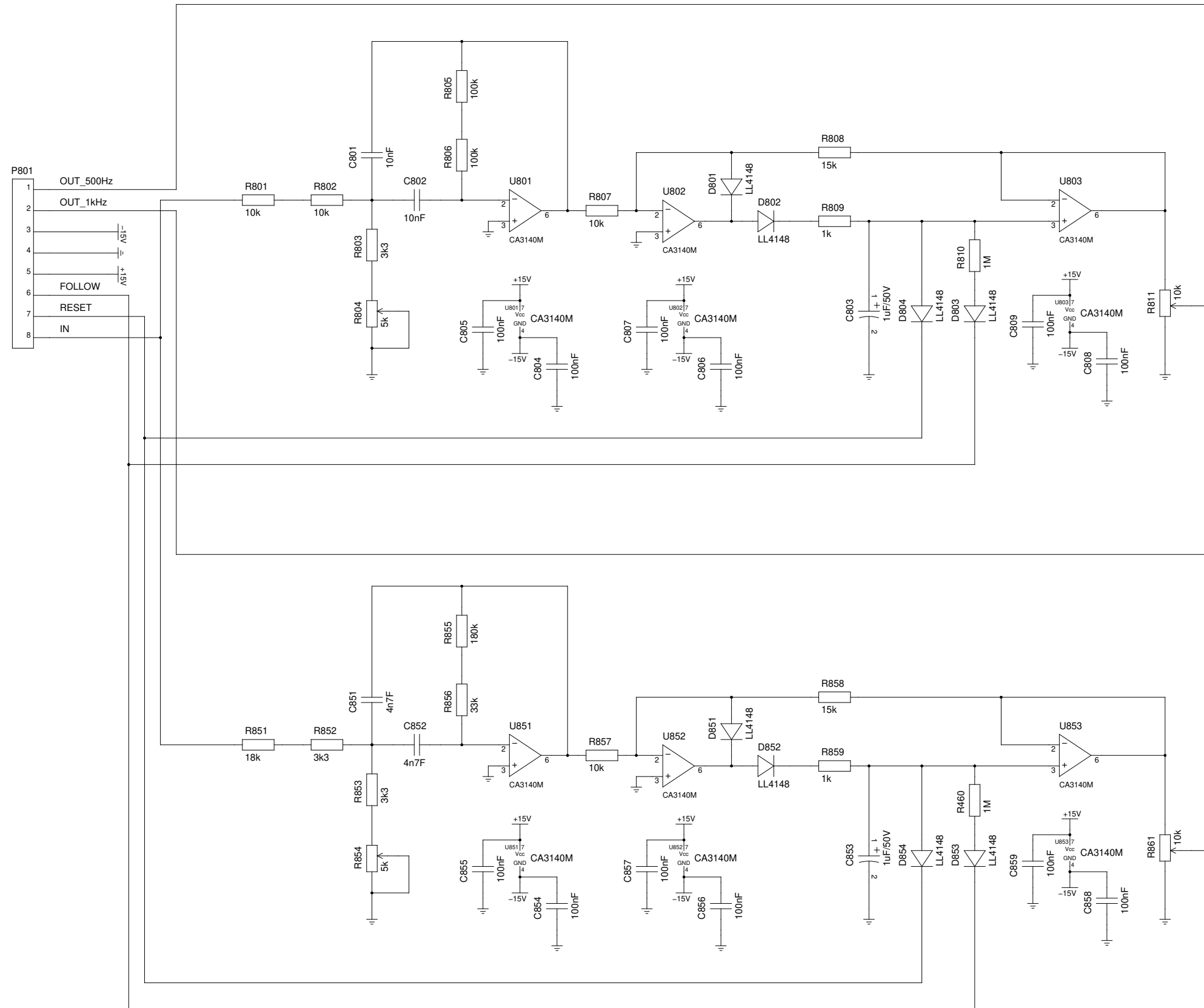


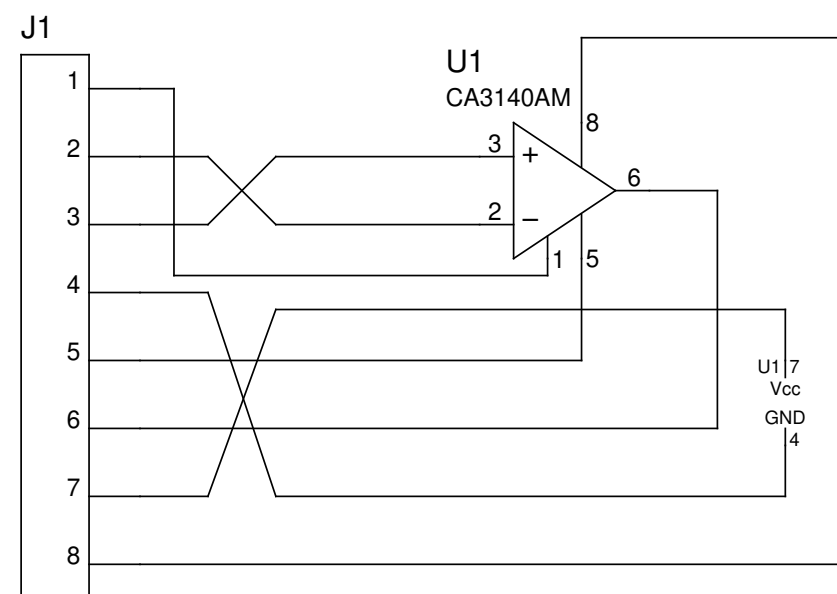












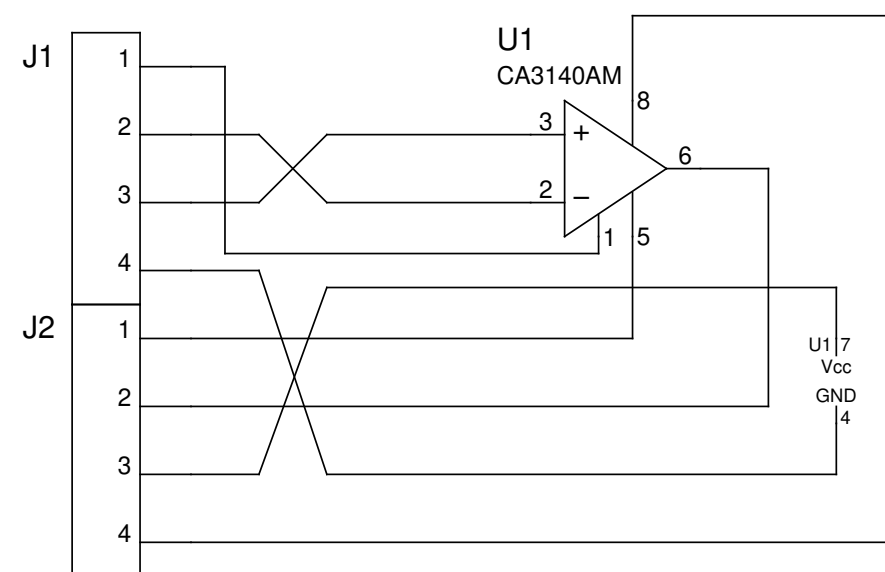
OCTAVE\_FILTER  
CA3140M breakout pcb (PTH+SMT)  
TITLE Schematic (DFM)

FILE: 26.999.00.01.01.sch

REVISION: 20240523

PAGE 01 OF 01

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OCTAVE_FILTER CA3140M breakout pcb (PTH+SMT) TITLE Schematic (DFM)		
FILE: 26.999.01.01.01.sch	REVISION: 20240523	
PAGE 01	OF 01	DRAWN BY: Bert Timmerman