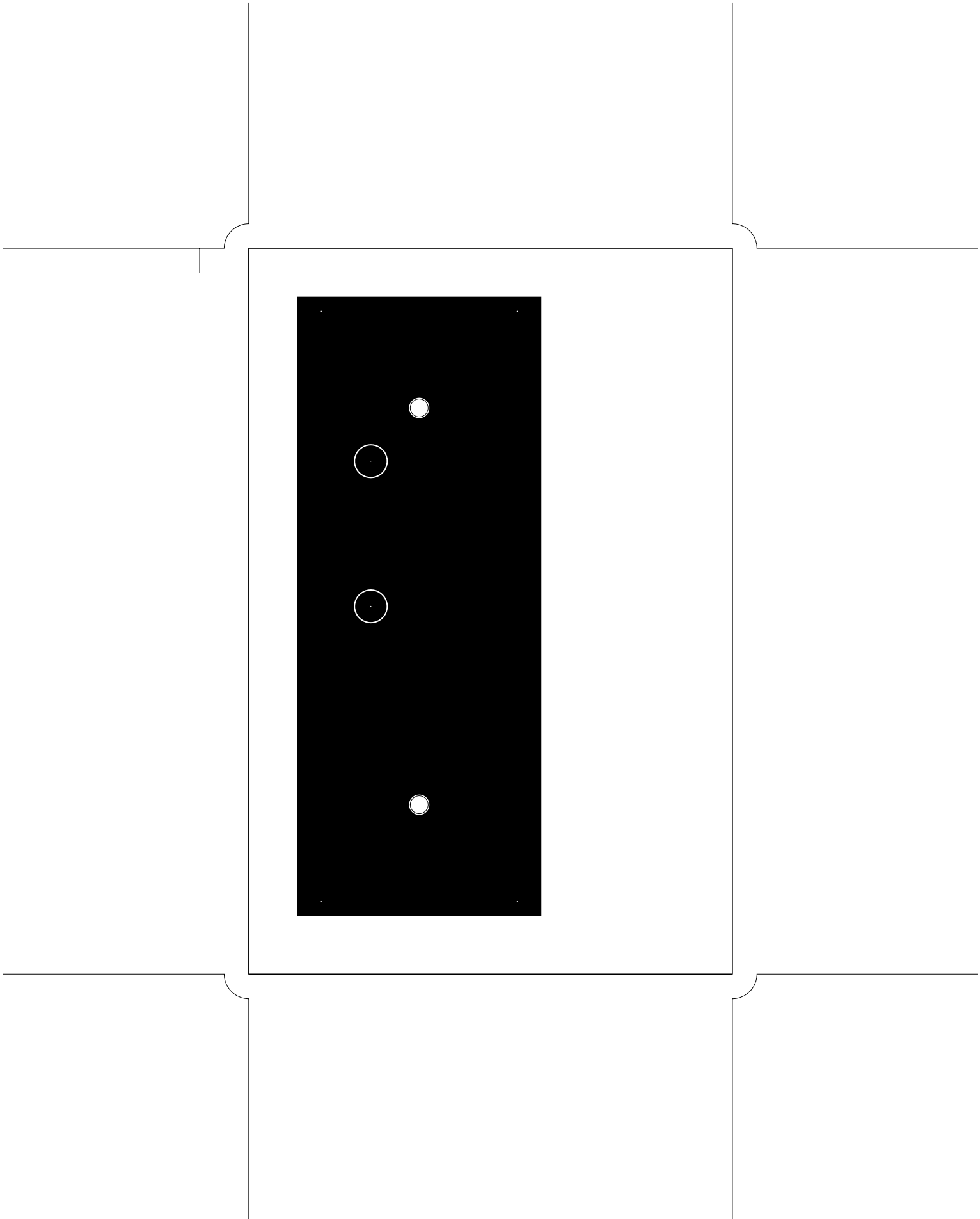
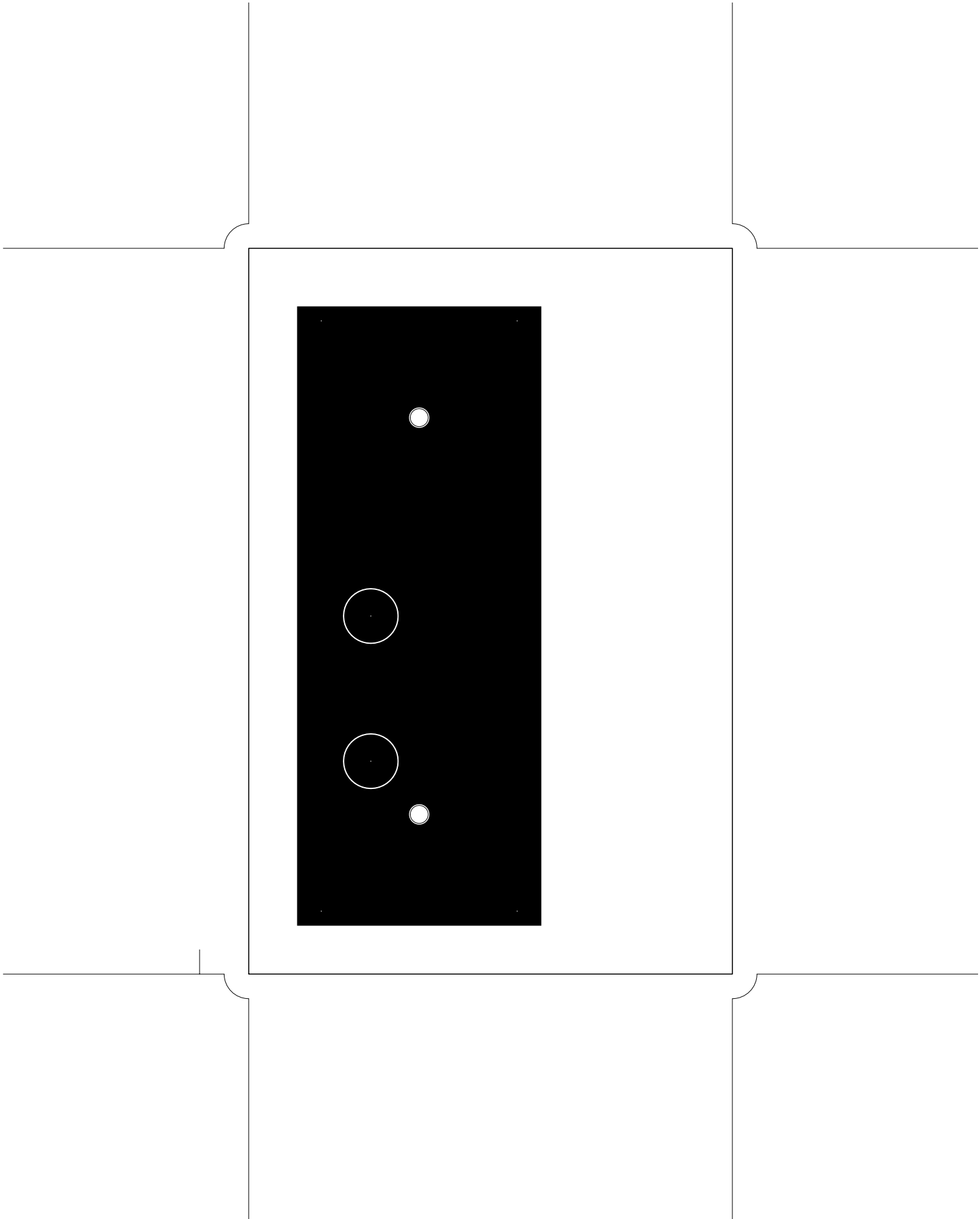


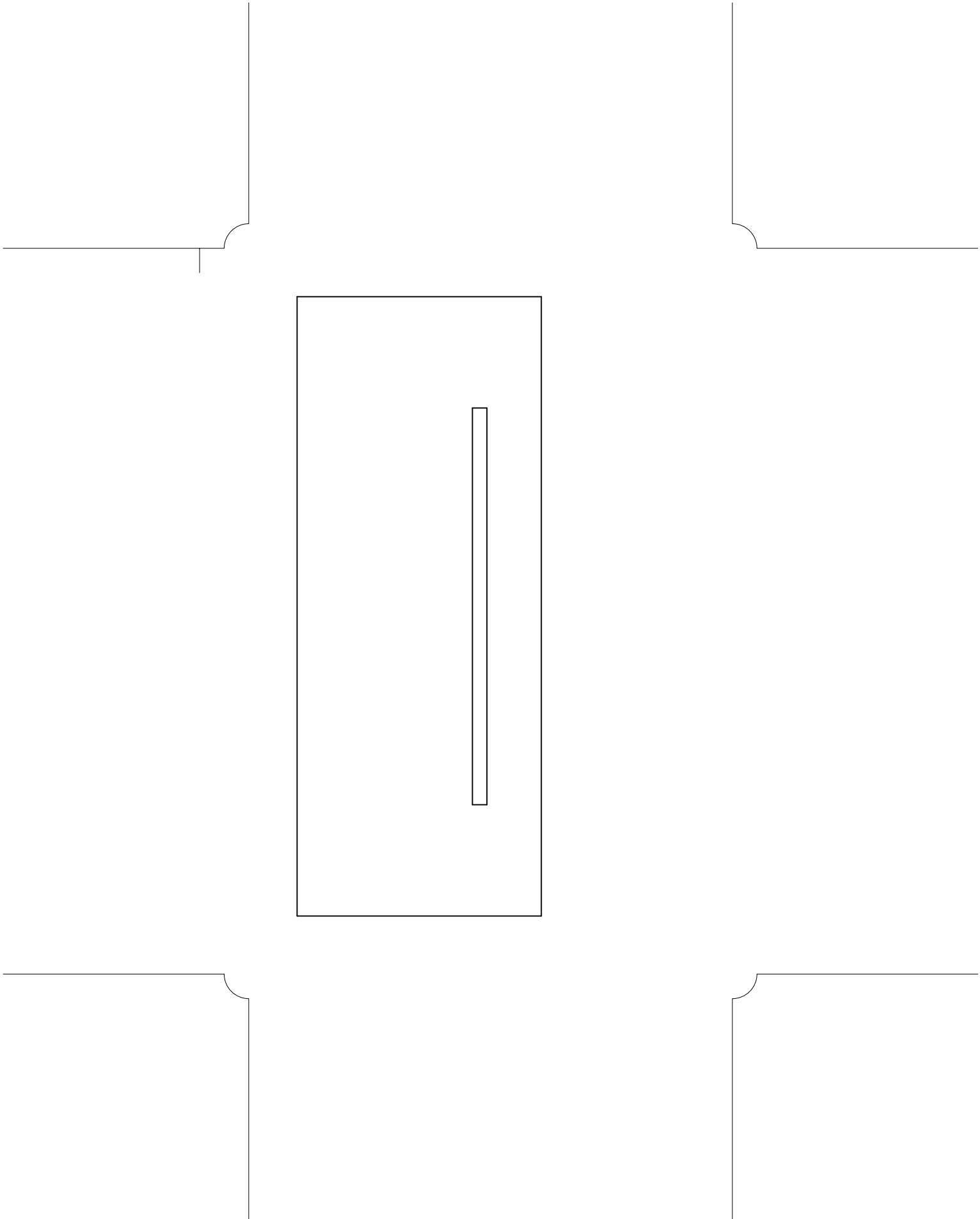
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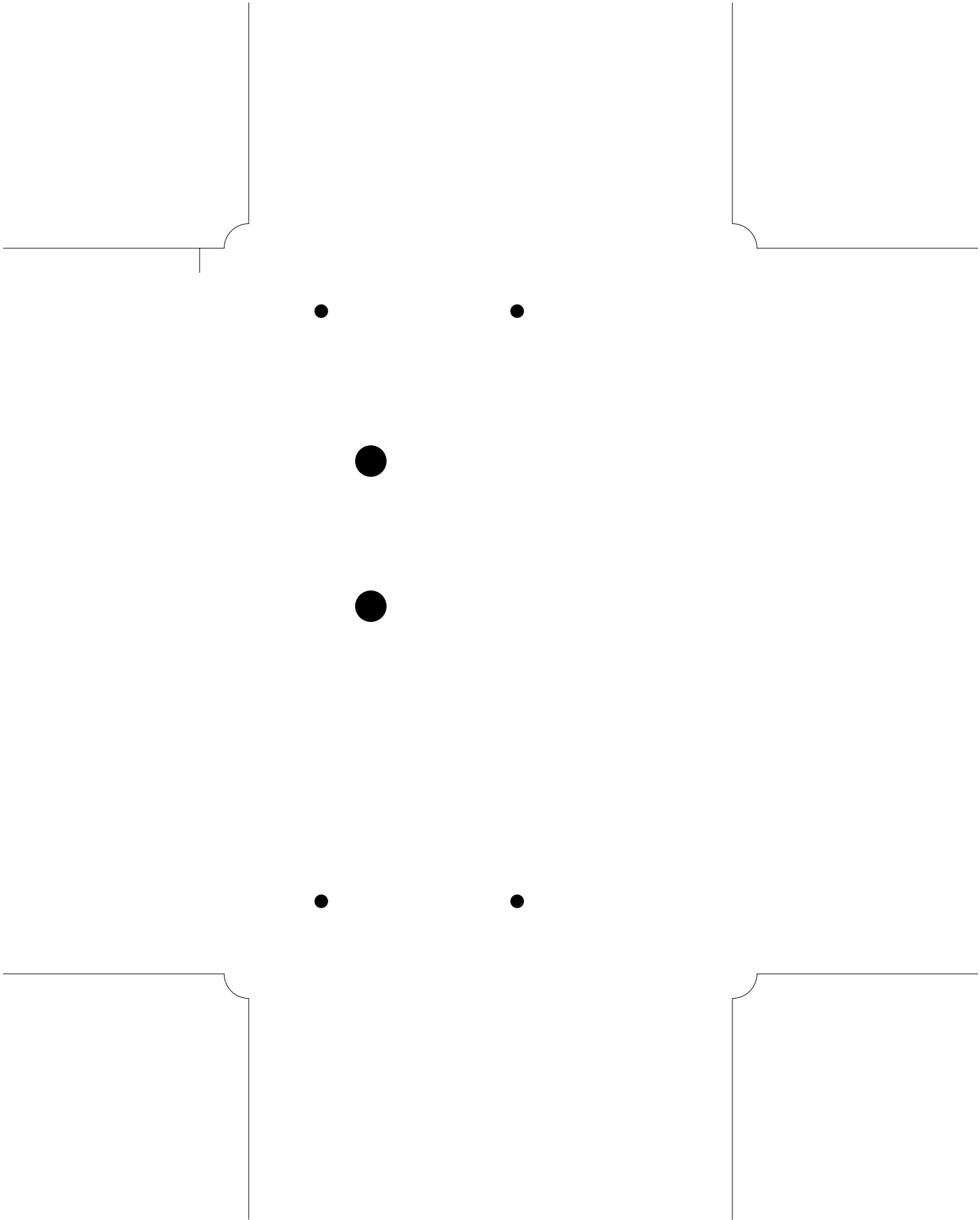
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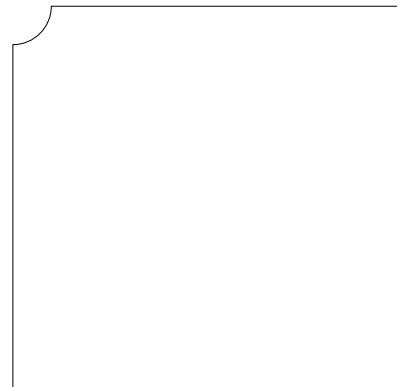
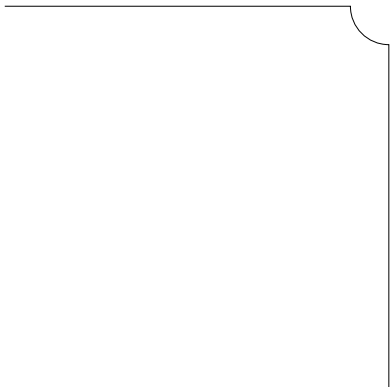
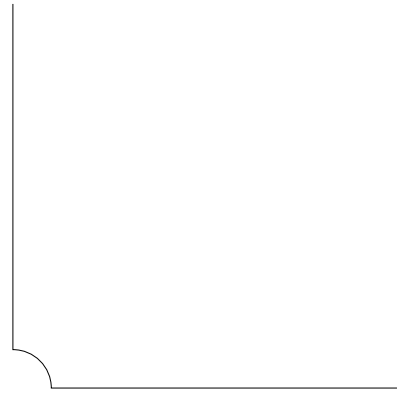
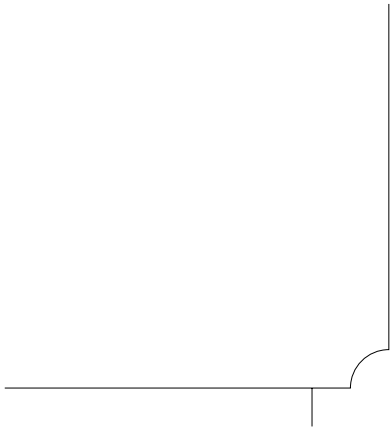
2. top
3. bottom
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. bottompaste
12. topassembly
13. bottomassembly
14. fab

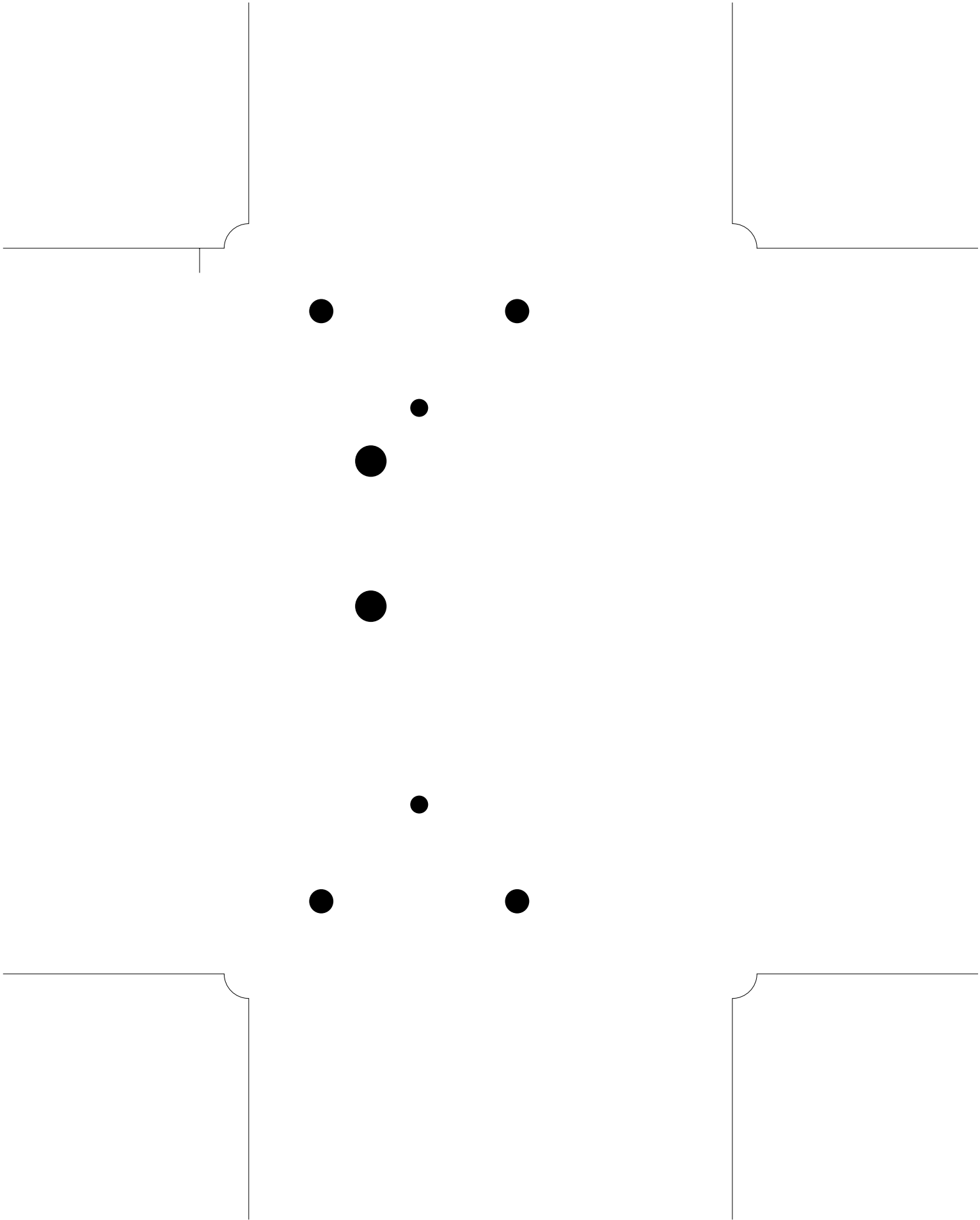


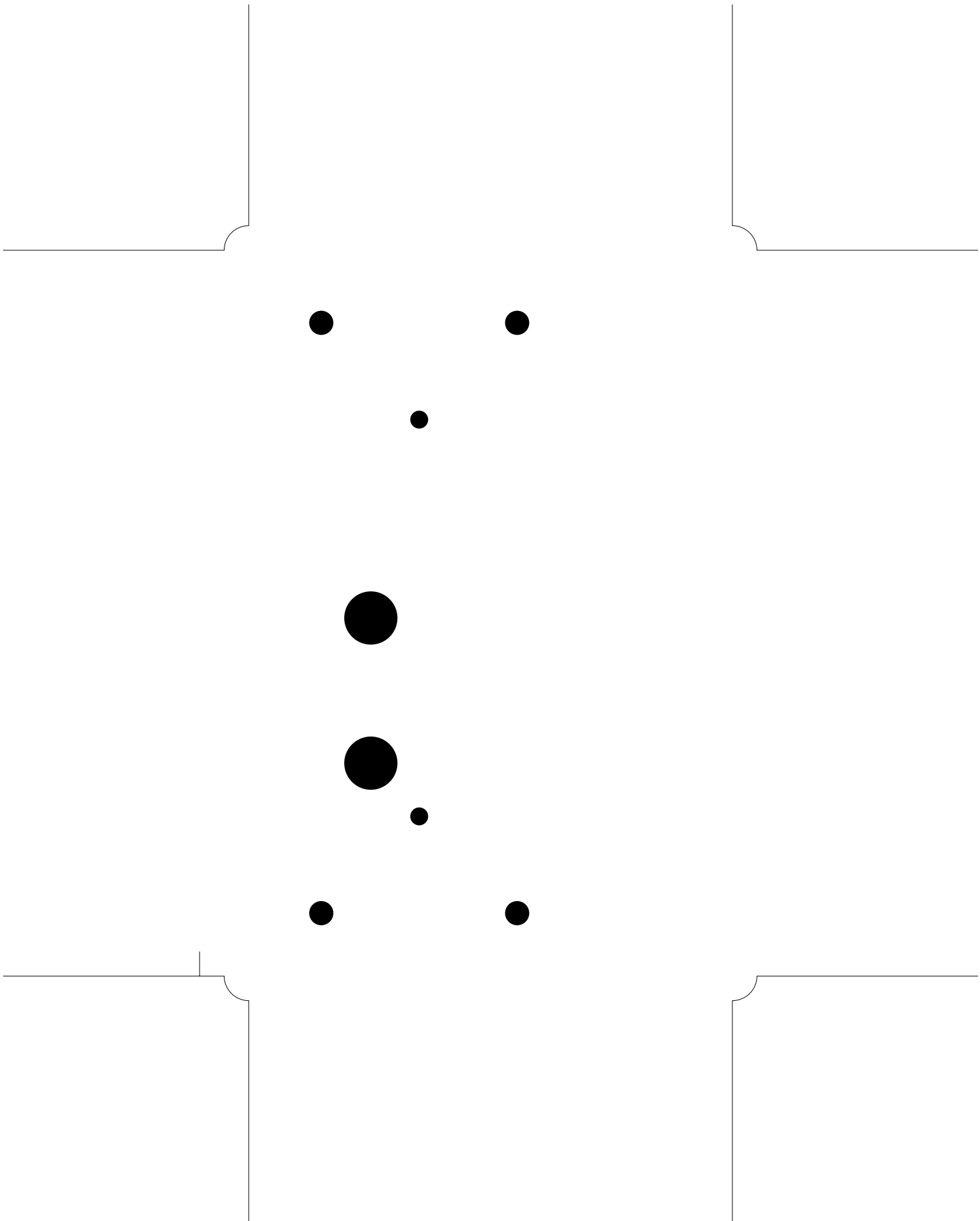


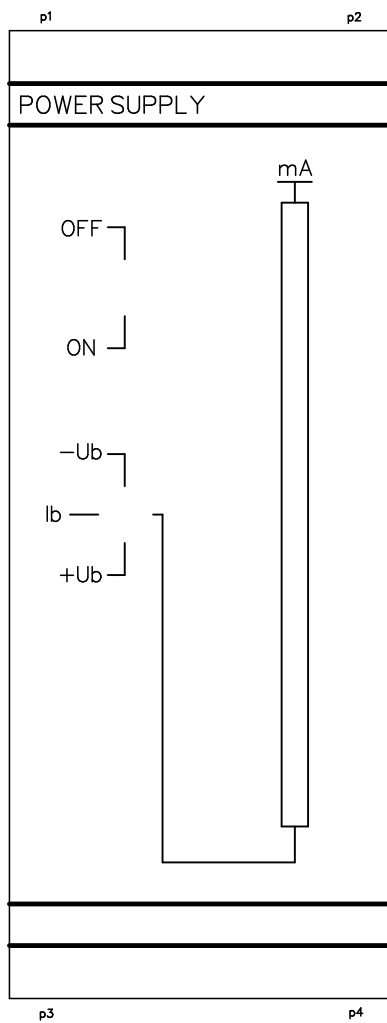


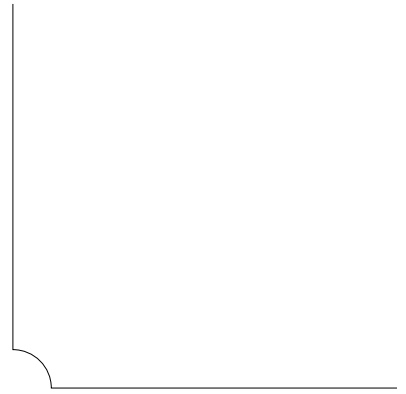
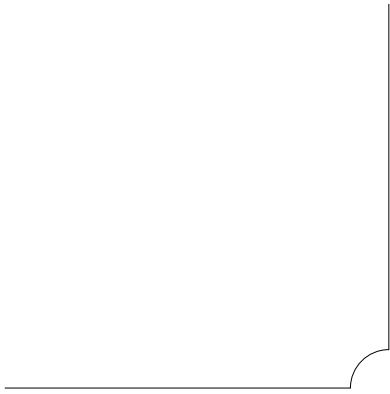








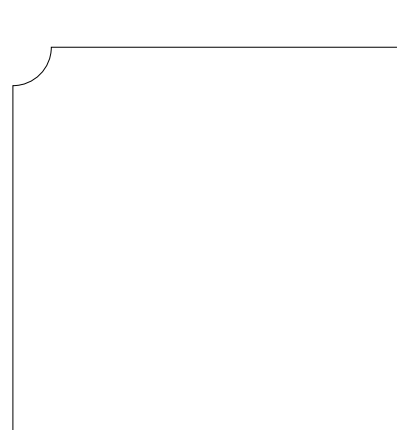
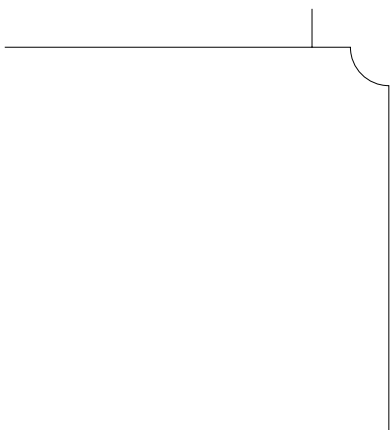


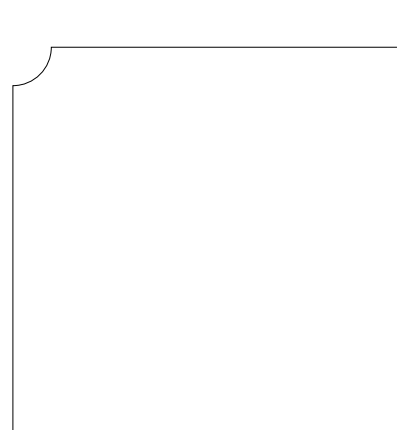
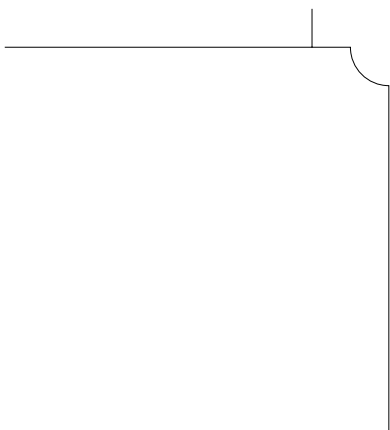
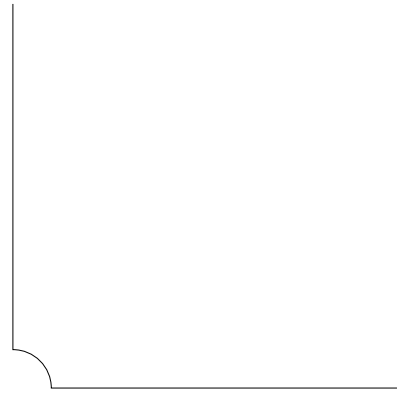
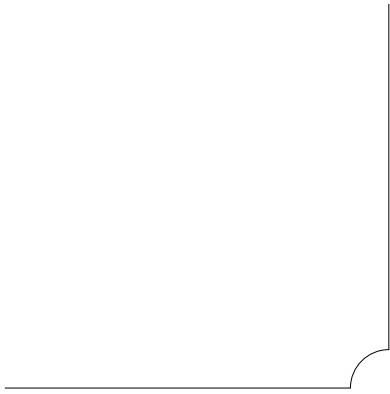


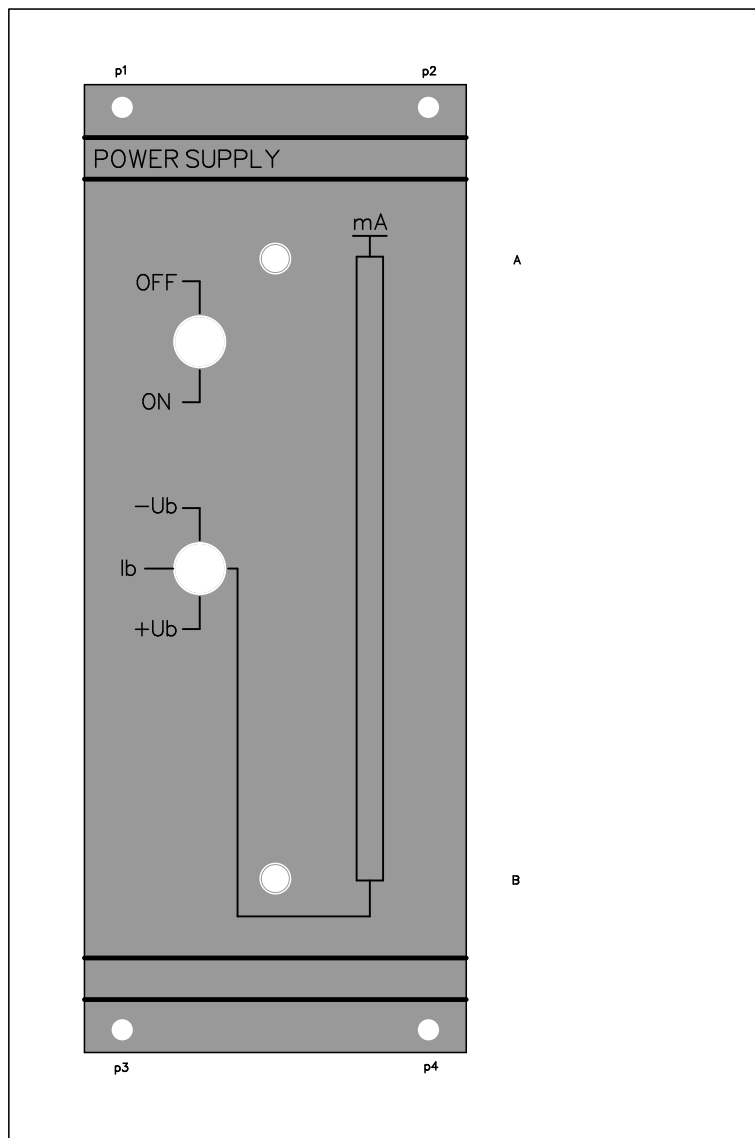
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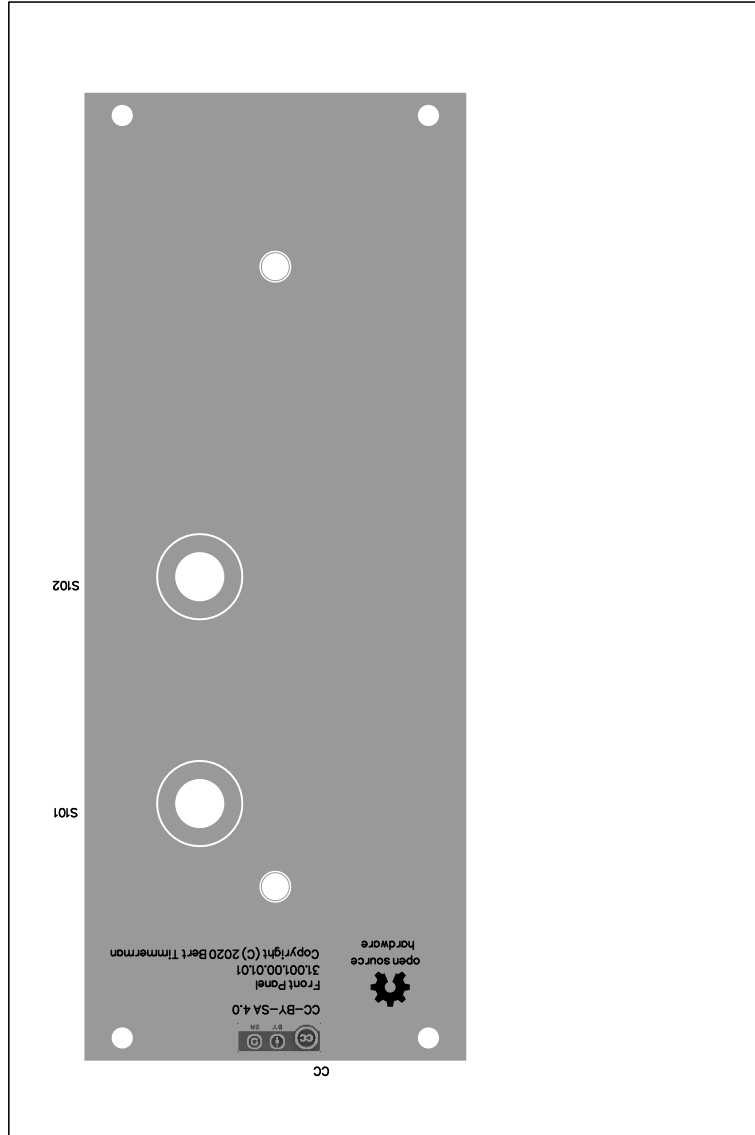
S101

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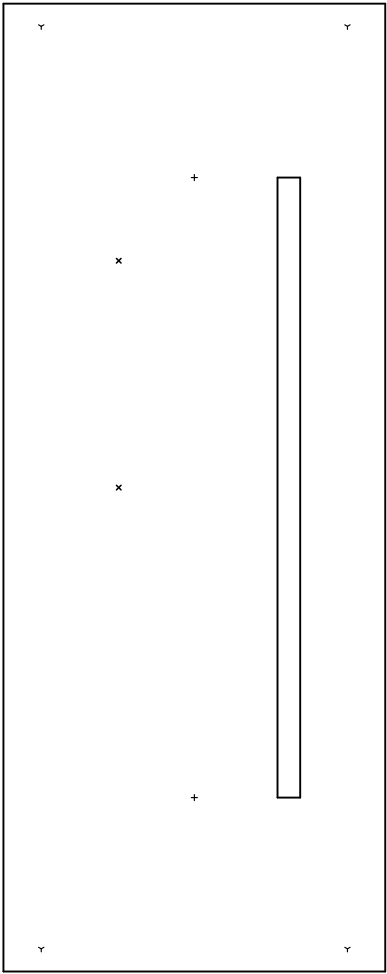






There are 3 different drill sizes used in this layout, 8 holes total

Symbol	Diam. (Inch)	Count	Plated?	
γ	0.110	4	YES	Title: 31.001.00.01.01.pcb – Fabrication Drawing
+	0.146	2	NO	Author: Bert Timmerman
×	0.256	2	YES	Date: Sun Jan 3 11:16:31 2021 UTC



Board outline is the centerline of this path