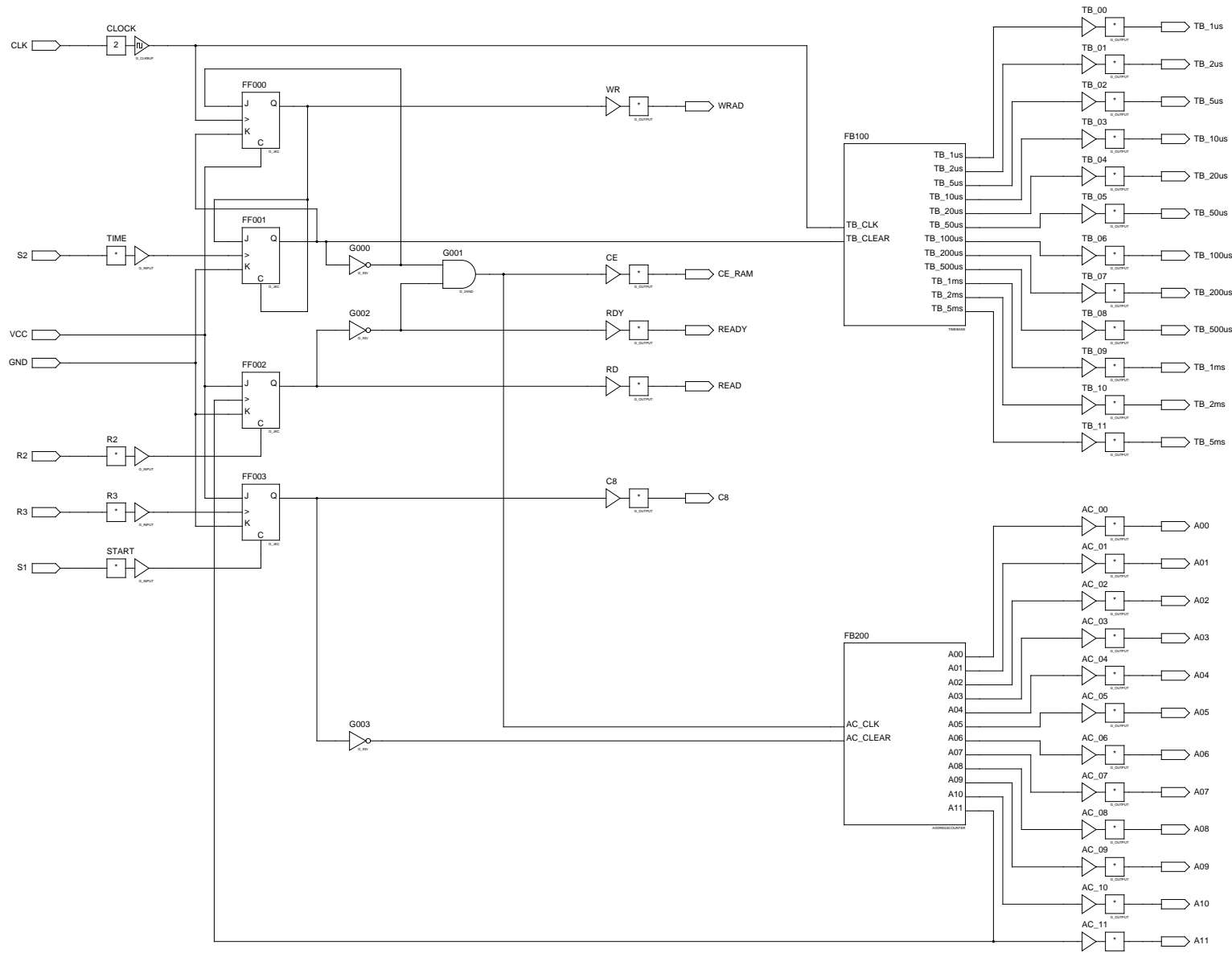
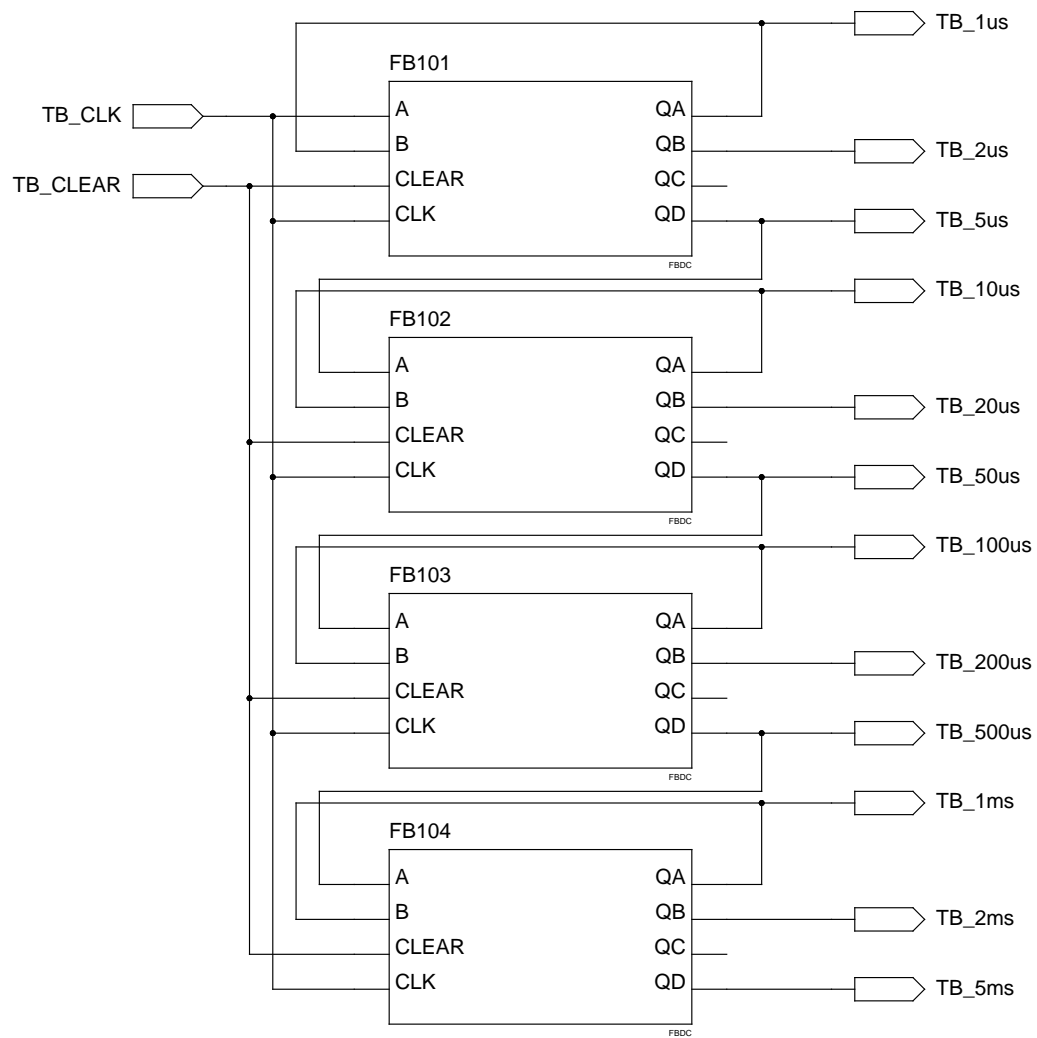
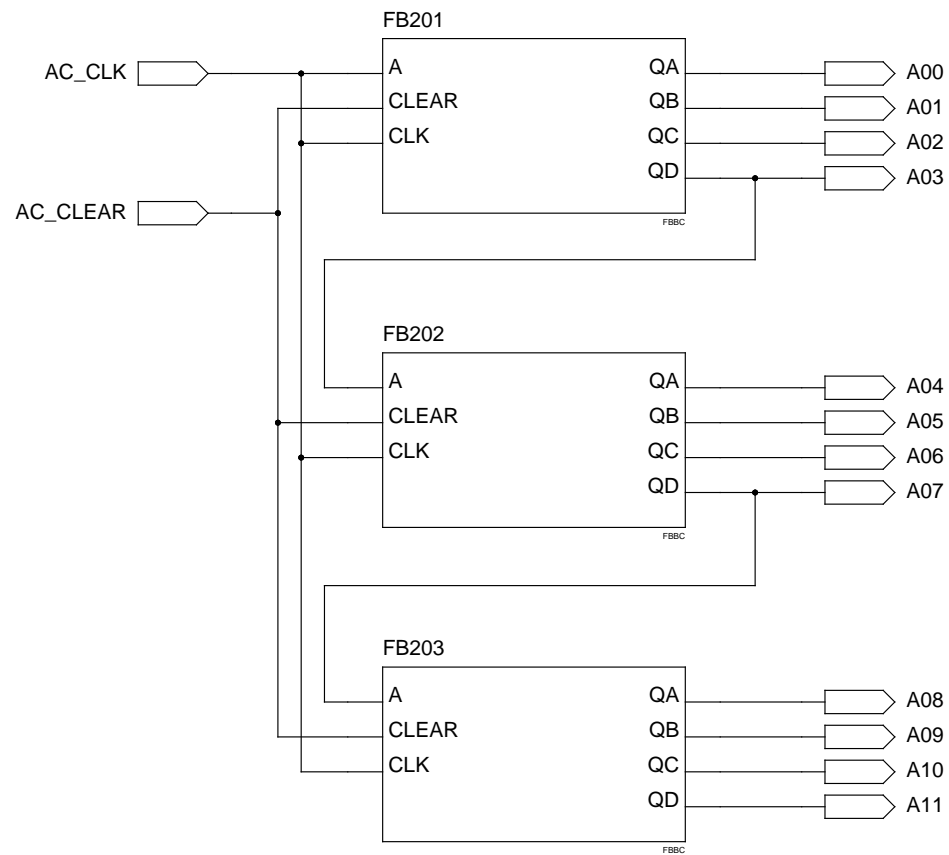


Analog input channel 1 (X) READ Analog output channel 1 (X) Analog output channel 1 (X) (unamplified) Analog output channel 2 (Y) Analog output channel 2 (Y) (unamplified) WRAD Analog input channel 2 (Y) CE_RAM TRIGGER TRIGGER_1 TRIGGER_2 A10 A9 A8 A7 A6 A5 A4 A3 A2 A1 A0 +U -U reserved for 220V AC reserved for 220V AC -15V DC GND +15VDC			VLF Scope System - Control Unit Schematic	
TITLE Control_Unit			REVISION: 20070318 DRAWN BY: Bert Timmerman	
FILE: 54.000.00.01.01.sch			A1	
PAGE	1	OF 1		

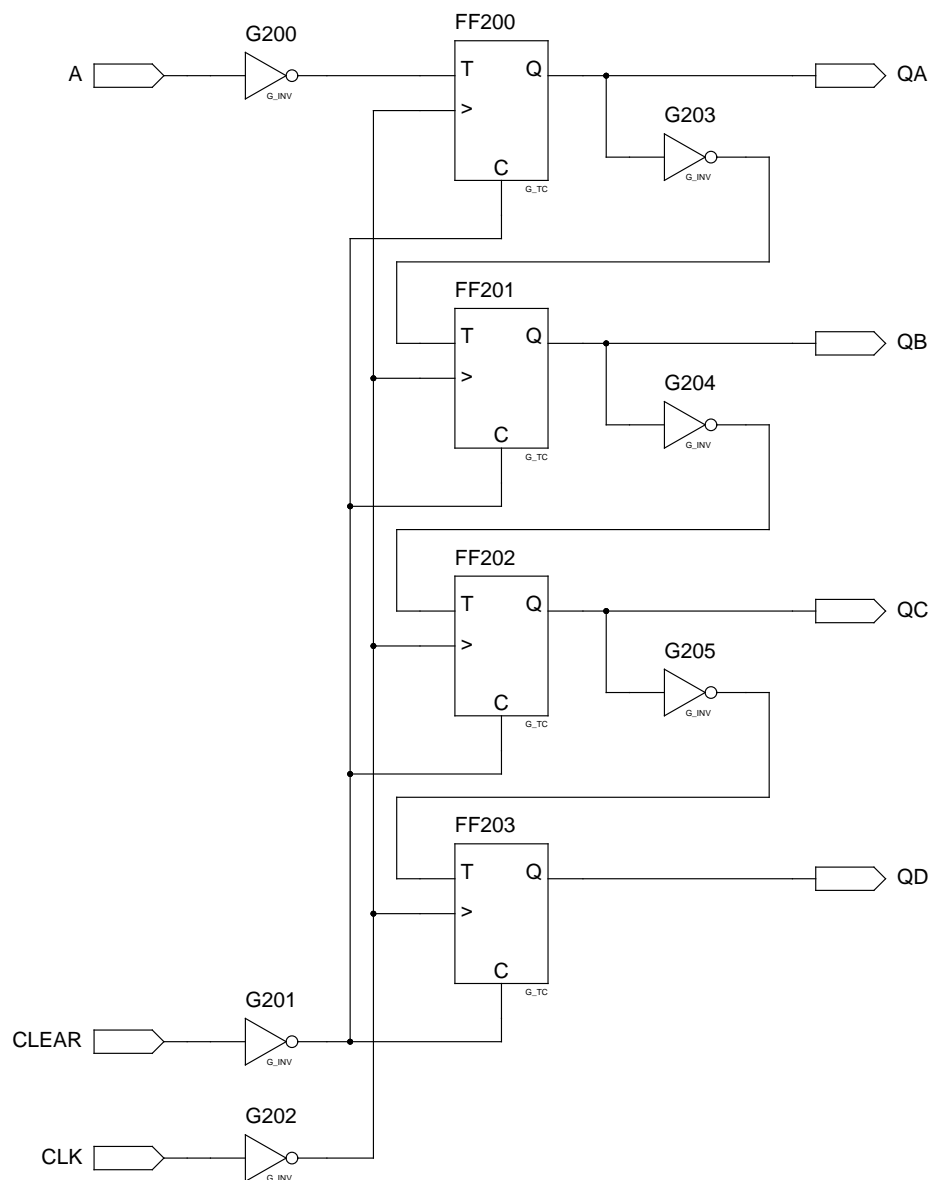




VLF Scope System – Control Unit TIMEBASE schematic		
TITLE	TIMEBASE	
FILE:	54.001.00.01.11.sch	REVISION: 20070318
PAGE	1 OF 1	DRAWN BY: Bert Timmerman



VLF Scope System – Control Unit Address Counter schematic ADDRESSCOUNTER			
FILE:	54.001.00.01.12.sch	REVISION:	20070318
PAGE	1	OF	1
DRAWN BY:		Bert Timmerman	



VLF Scope System – Control Unit Four Bit Binary Counter schematic FBBC			A3
FILE:	54.001.00.01.22.sch	REVISION:	
PAGE	1 OF 1	DRAWN BY:	