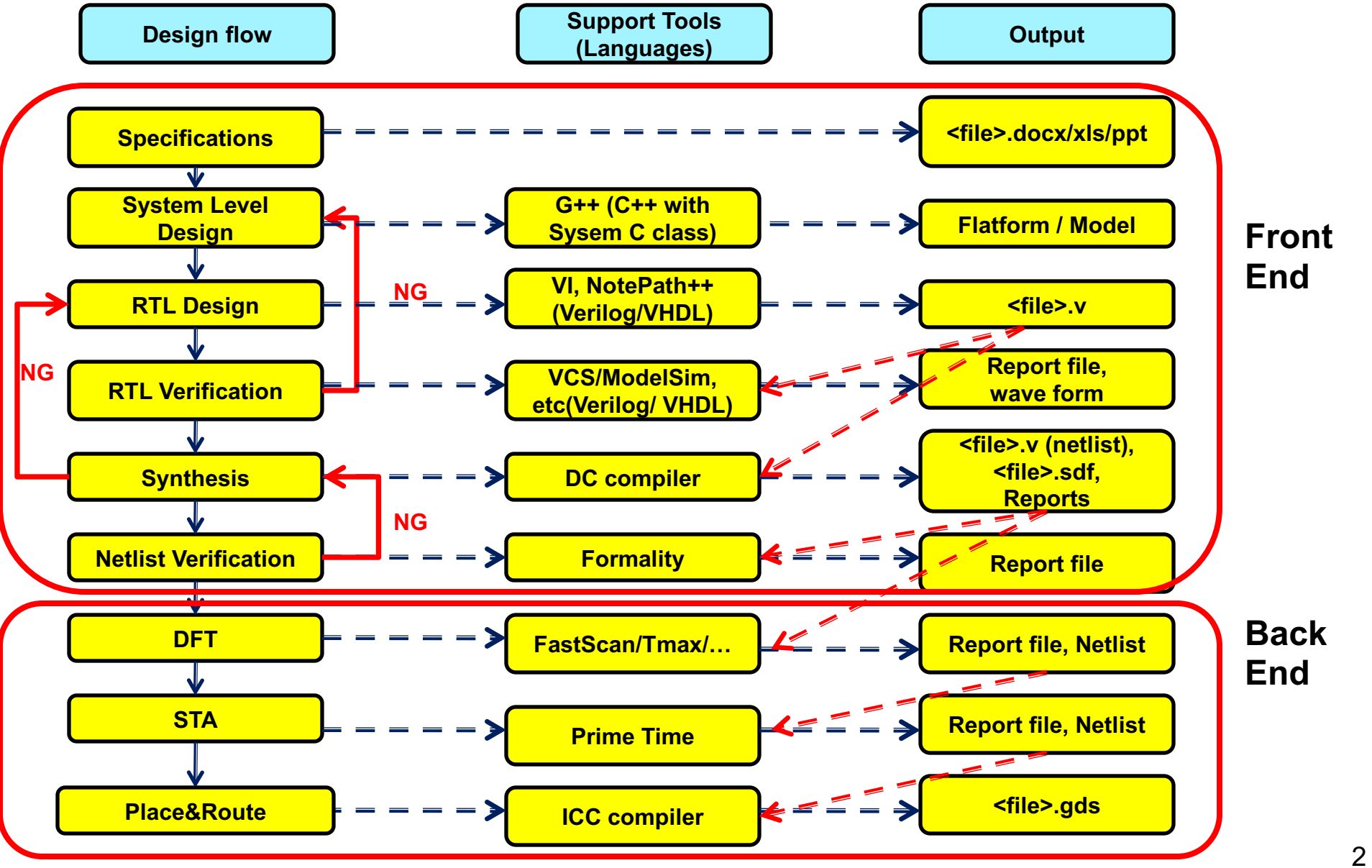
System Level Design in Cell Based Design Flow

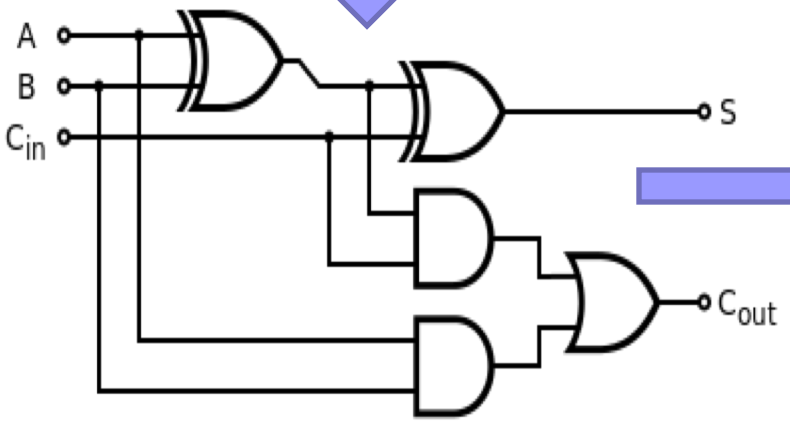
Cell Based Design Flow

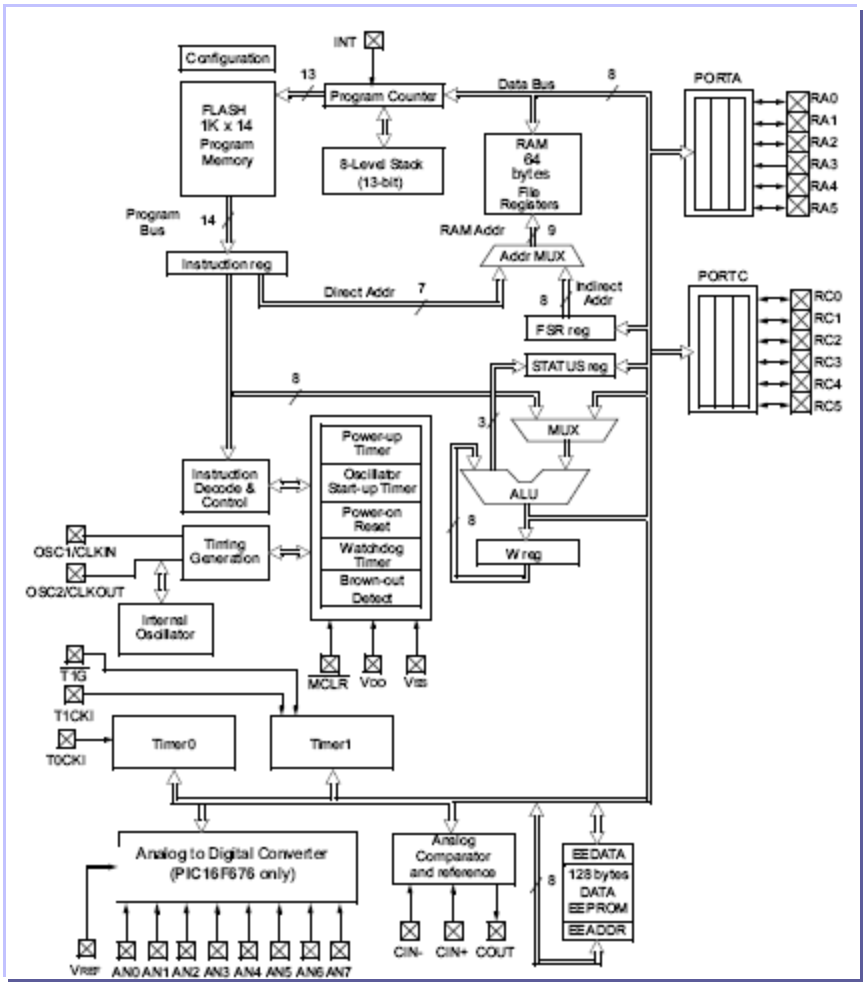


Cell Based Design Flow

Design flow	Support Tools (Languages)	Output
Specifications		<file>.docx/xls/ppt

Design the one bit adder:
+ Three inputs (A, B, Cin)
+ Two output (S, Cout)





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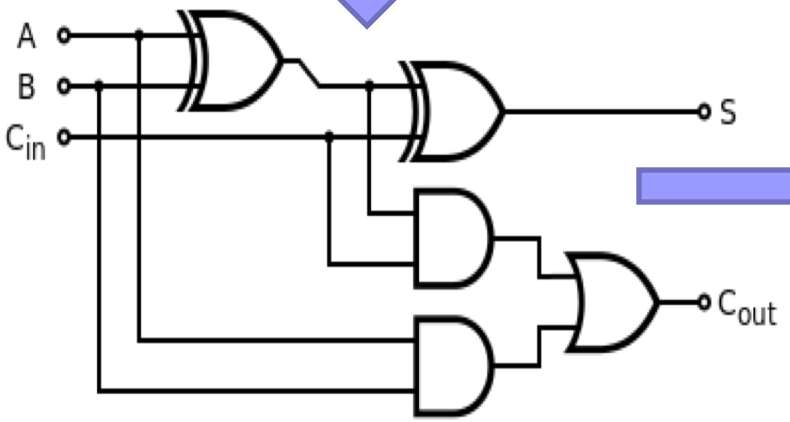
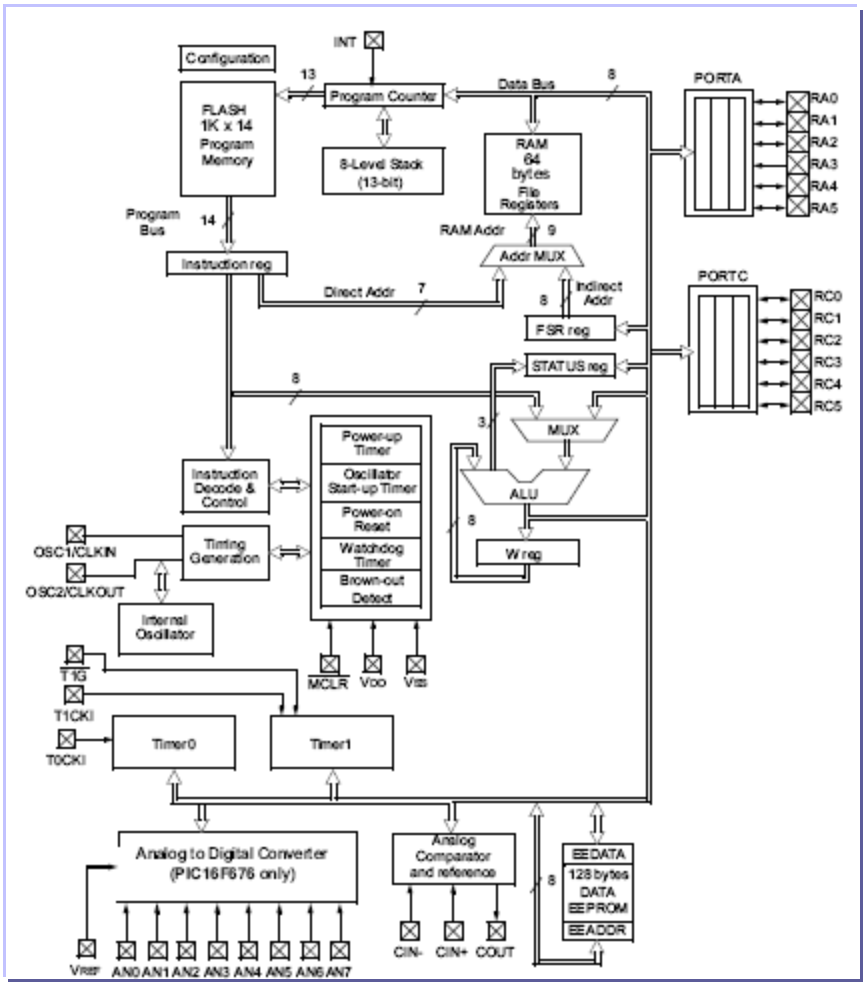
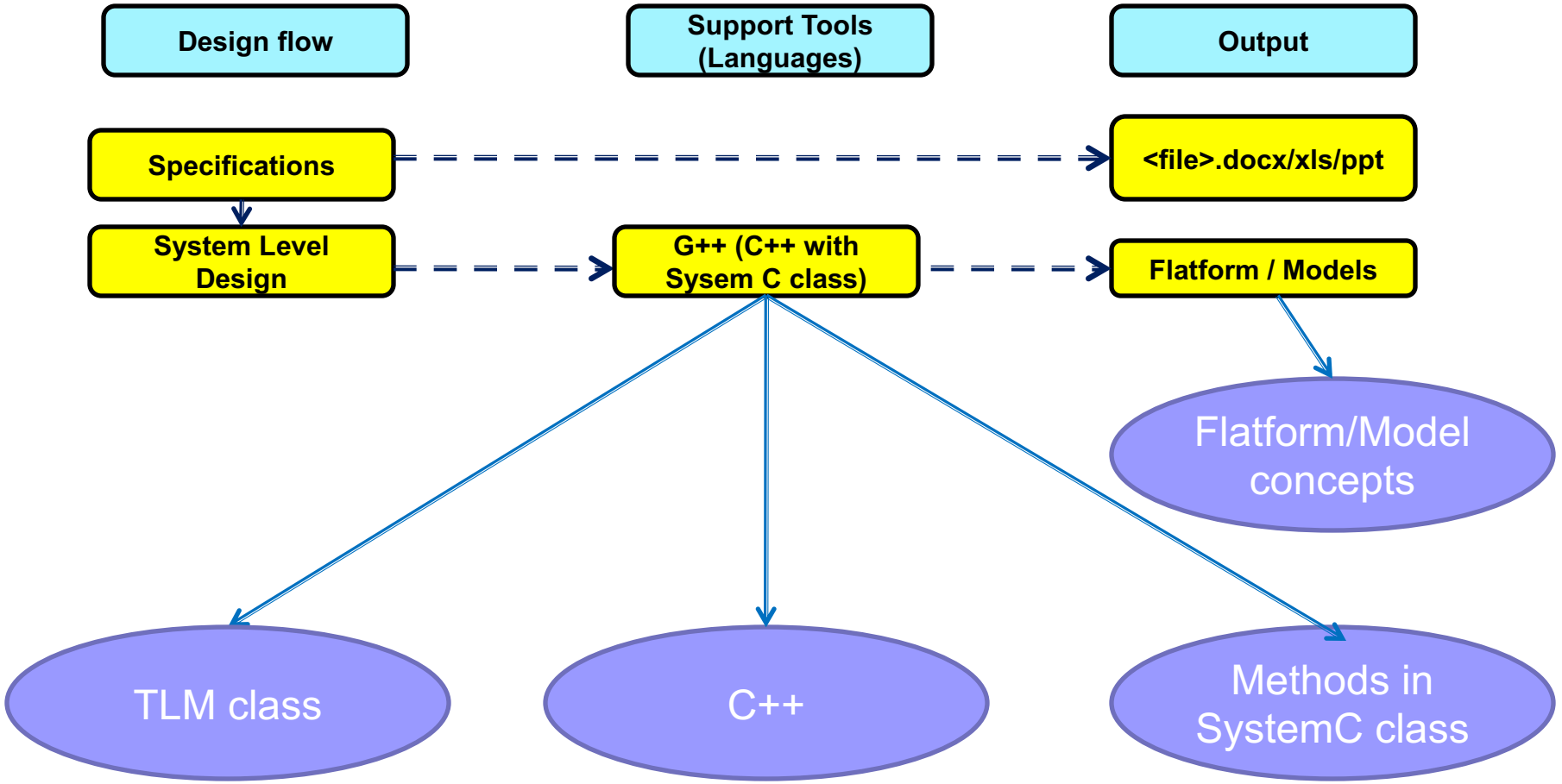
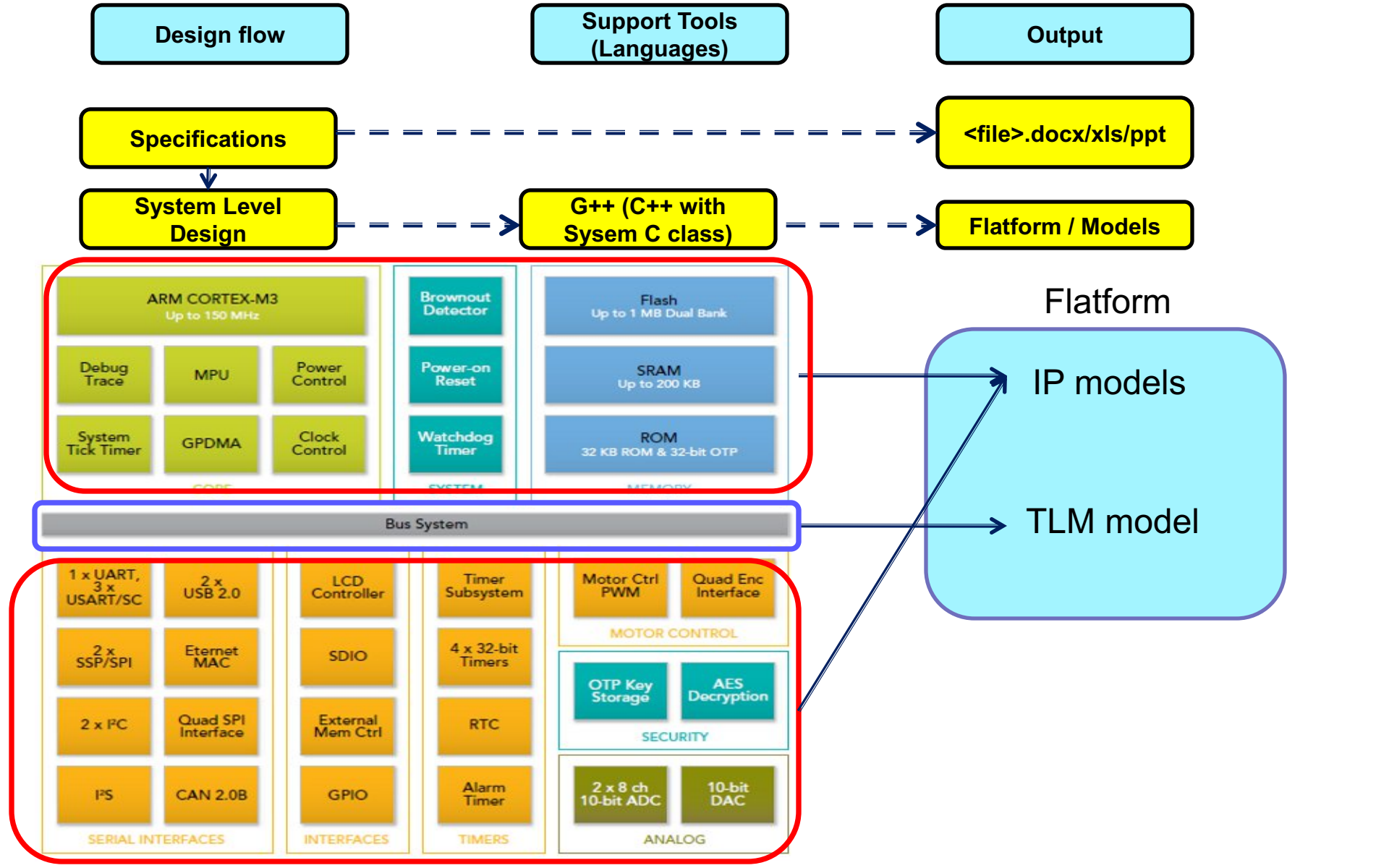




System Level Design Step



System Level Design Step



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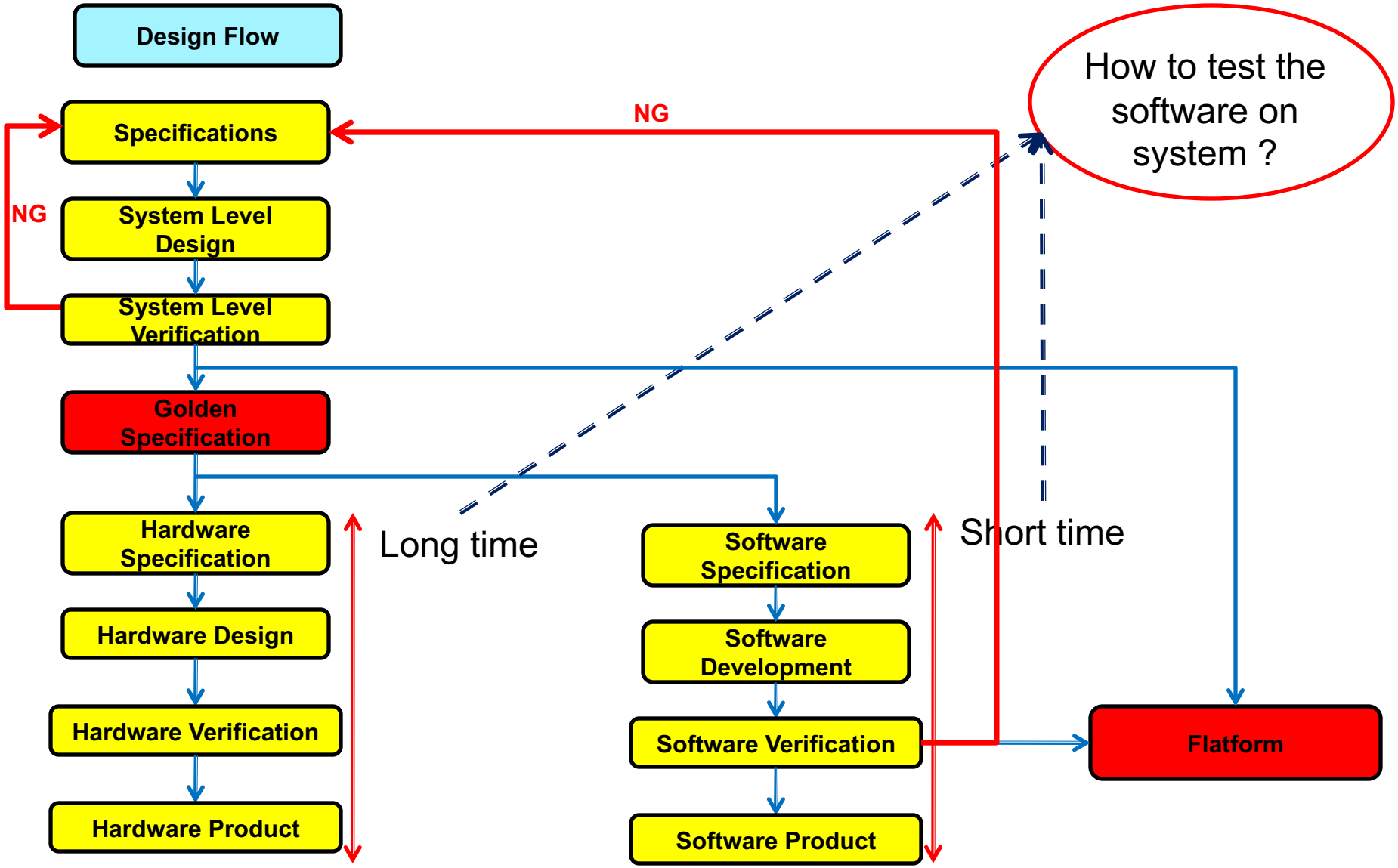
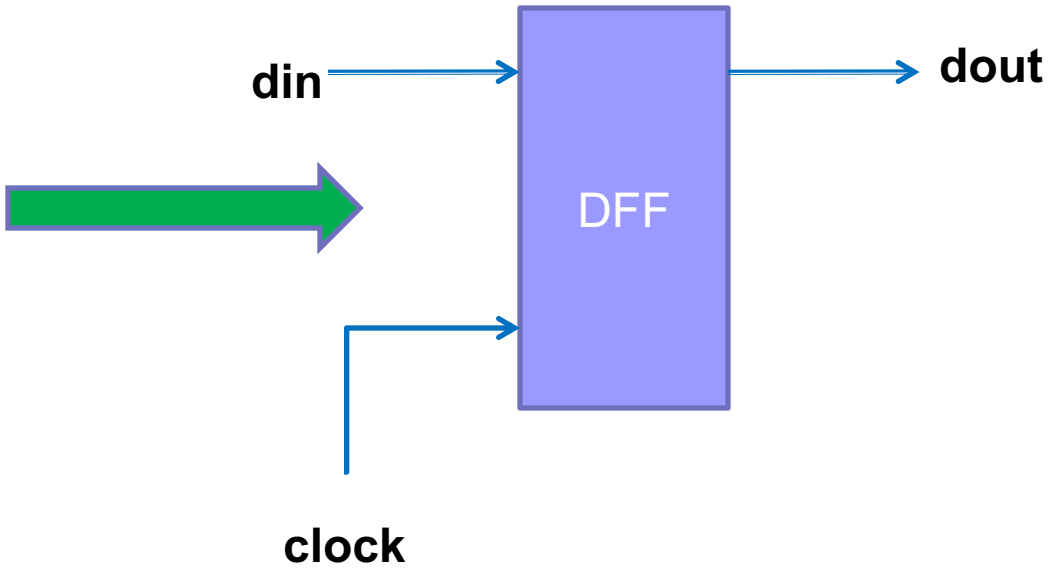


System Level Design Step

```
#include "systemc.h"
SC_MODULE(d_ff) {
    sc_in<bool> din;
    sc_in<bool> clock
    sc_out<bool> dout;
    void doit() {
        dout = din;
        cout << dout;
    };
    SC_CTOR(d_ff) {
        SC_METHOD(doit);
        sensitive_pos << clock;
    }
}
```



Q & A