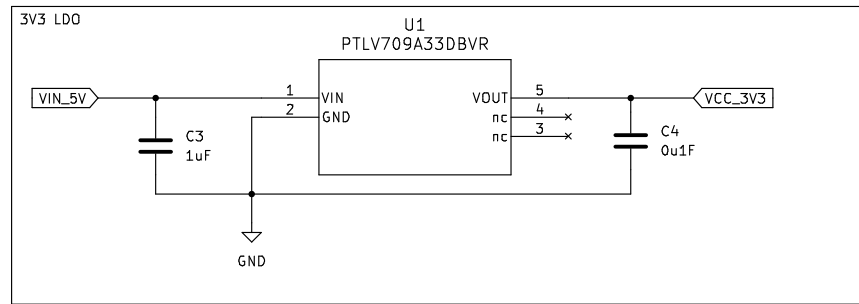
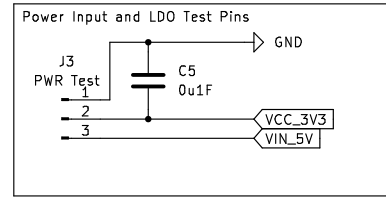


MCLK signal not needed with echo dot



Mika Laitio

Sheet: /external input/

File: external_connections.kicad_sch

Title: I2S Audio to AES/EBU Optical Toslink

Size: A4

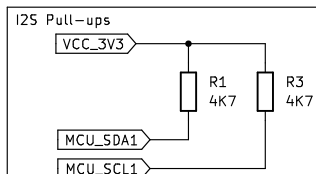
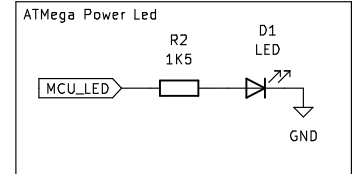
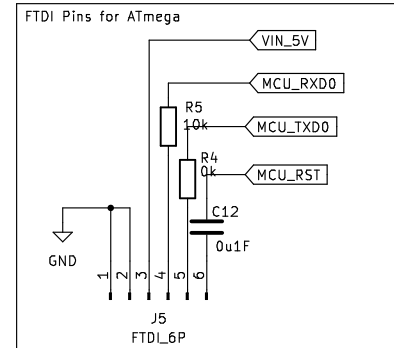
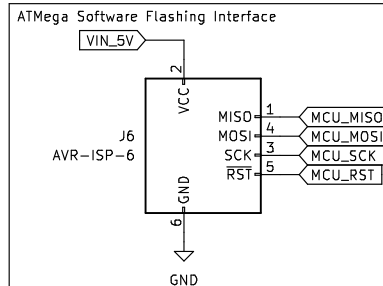
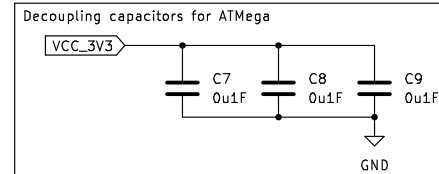
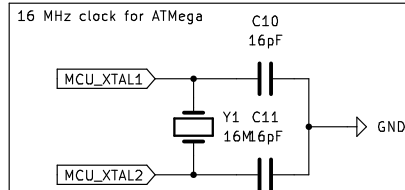
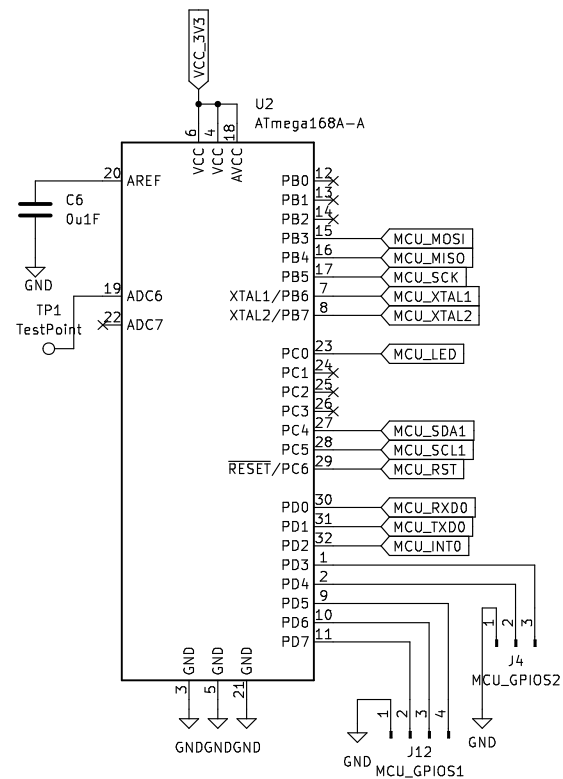
Date: 2023-12-26

Rev: v0.1

KiCad E.D.A. kicad 7.0.10-1-dev-1fdf7d0cf4

Id: 1/6

ATMega for codec and clock chip configuration with I2C



Mika Laitio

Sheet: /mcu/

File: mcu.kicad_sch

Title: I2S Audio to AES/EBU Optical Toslink

Size: A4

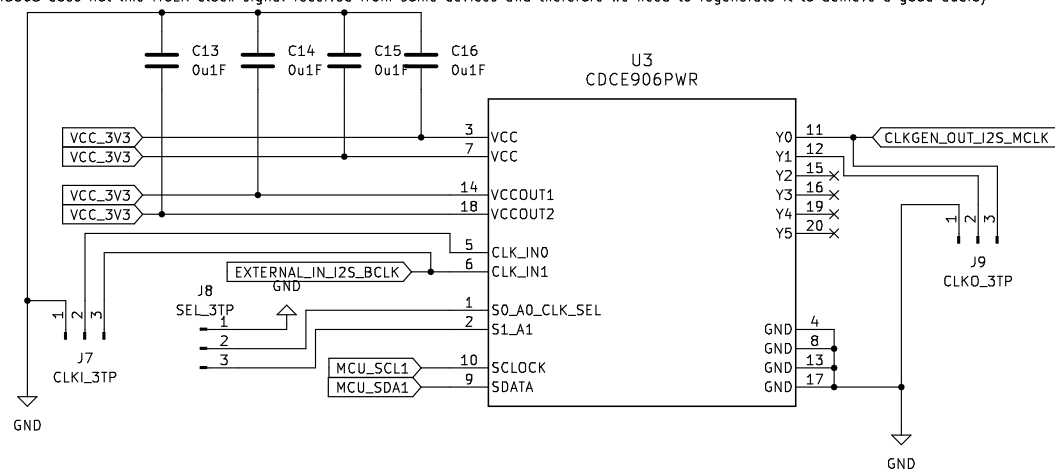
Date: 2023-12-26

Rev: v0.1

KiCad E.D.A. kicad 7.0.10-1-dev-1fdf7d0cf4

Id: 3/6

I2S MCLK clock signal generator for from the I2S BCLK clock
(WM8805 does not like MCLK clock signal received from some devices and therefore we need to regenerate it to achieve a good audio)



Mika Laitio

Sheet: /clocks/

File: clocks.kicad_sch

Title: I2S Audio to AES/EBU Optical Toslink

Size: A4

Date: 2023-12-26

Rev: v0.1

KiCad E.D.A. kicad 7.0.10-1-dev-1fdf7d0cf4

Id: 4/6

