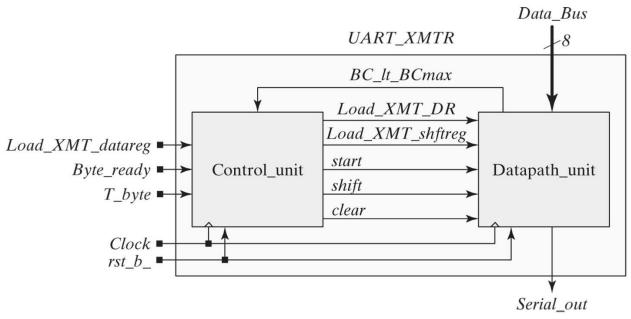
## **UART Block Diagram and Internal DataPath registers:**



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