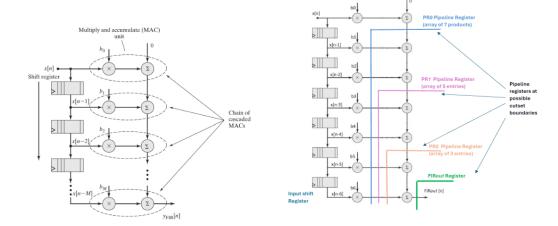## Data Flow graph (DFG)

Bitwave

Latency = 3 clock cycle