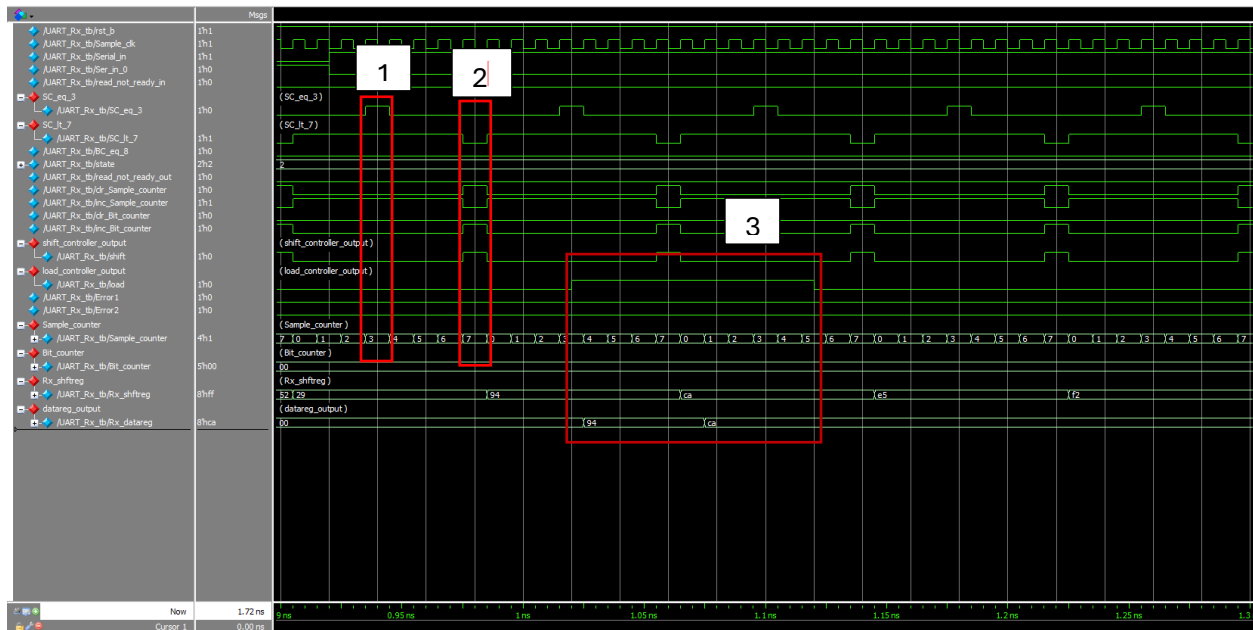


Bitwave



1. The SC_eq_3 only goes high when the Sample_counter is 3
2. SC_Lt_7 goes low when the Sample_counter is 7
3. The Rx_datareg is loaded with the Rx_shftreg when load signal is received from the control_unit