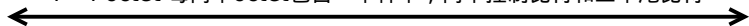


F=4 octet 每两个octet包含一个样本，两个控制比特和三个尾比特



| 配置数据 |
|-------|
| CF=0 |
| CS=2 |
| F=4 |
| HD=0 |
| L=8 |
| M=16 |
| N=11 |
| N'=16 |

| | | | | |
|--------|------------|------------------------|------------|------------------------|
| Lane 0 | Cr0[10:3] | Cr0[2:0,C1:C0] TTT | Cr1[10:3] | Cr1[2:0,C1:C0] TTT |
| Lane 1 | Cr2[10:3] | Cr2[2:0,C1:C0] TTT | Cr3[10:3] | Cr3[2:0,C1:C0] TTT |
| Lane 2 | Cr4[10:3] | Cr4[2:0,C1:C0] TTT | Cr5[10:3] | Cr5[2:0,C1:C0] TTT |
| Lane 3 | Cr6[10:3] | Cr6[2:0,C1:C0] TTT | Cr7[10:3] | Cr7[2:0,C1:C0] TTT |
| Lane 4 | Cr8[10:3] | Cr8[2:0,C1:C0] TTT | Cr9[10:3] | Cr9[2:0,C1:C0] TTT |
| Lane 5 | Cr10[10:3] | Cr10[2:0,C1:C0] TTT | Cr11[10:3] | Cr11[2:0,C1:C0] TTT |
| Lane 6 | Cr12[10:3] | Cr12[2:0,C1:C0] TTT | Cr13[10:3] | Cr13[2:0,C1:C0] TTT |
| Lane 7 | Cr14[10:3] | Cr14[2:0,C1:C0] TTT | Cr15[10:3] | Cr15[2:0,C1:C0] TTT |