1. Description

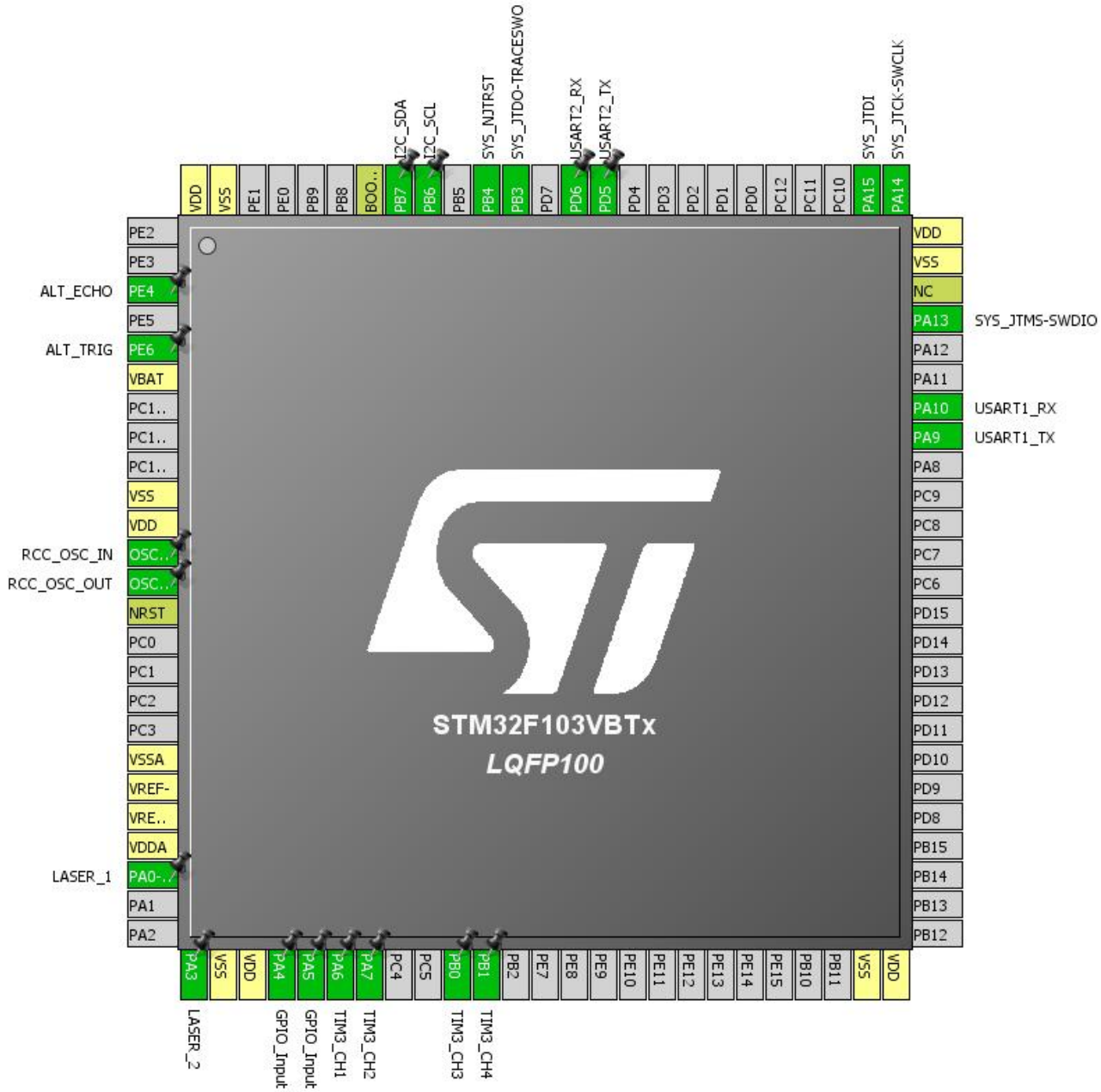
1.1. Project

Project Name	FlyVBTx
Generated with:	STM32CubeMX 4.8.0
Date	06/26/2015

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F103
MCU name	STM32F103VBTx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	OSC_IN
		RCC_OSC_OUT	OSC_OUT
SYS	Debug: JTAG(5-pin)	SYS_JTCK-SWCLK	PA14
		SYS_JTDI	PA15
		SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13
		SYS_NJTRST	PB4
TIM3	Channel1: PWM Generation CH1	TIM3_CH1	PA6
	Channel2: PWM Generation CH2	TIM3_CH2	PA7
	Channel3: PWM Generation CH3	TIM3_CH3	PB0
	Channel4: PWM Generation CH4	TIM3_CH4	PB1
TIM4	Clock Source : Internal Clock	N/A	N/A
USART1	Mode: Asynchronous	USART1_RX	PA10
		USART1_TX	PA9
USART2	Mode: Asynchronous	USART2_RX	PD6
		USART2_TX	PD5

4. Pins Configuration

Pin	Pos	Function(s)	Label
PE4	3	GPIO_EXTI4	ALT_ECHO
PE6 *	5	GPIO_Output	ALT_TRIG
OSC_IN	12	RCC_OSC_IN	
OSC_OUT	13	RCC_OSC_OUT	
PA0-WKUP *	23	GPIO_Input	LASER_1
PA3 *	26	GPIO_Input	LASER_2
PA4 *	29	GPIO_Input	
PA5 *	30	GPIO_Input	
PA6	31	TIM3_CH1	
PA7	32	TIM3_CH2	
PB0	35	TIM3_CH3	
PB1	36	TIM3_CH4	
PA9	68	USART1_TX	
PA10	69	USART1_RX	
PA13	72	SYS_JTMS-SWDIO	
PA14	76	SYS_JTCK-SWCLK	
PA15	77	SYS_JTDI	
PD5	86	USART2_TX	
PD6	87	USART2_RX	
PB3	89	SYS_JTDO-TRACESWO	
PB4	90	SYS_NJTRST	
PB6 *	92	GPIO_Output	I2C_SCL
PB7 *	93	GPIO_Output	I2C_SDA

* The pin is affected with an I/O function

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F1
Line	STM32F103
MCU	STM32F103VBTx
Datasheet	13587_Rev16

5.2. Parameter Selection

Temperature	25
Vdd	3.3

6. Software Project

6.1. Project Settings

Name	Value
Project Name	FlyVBTx
Project Folder	E:\C\ARM\32CUBE\FlyVBTx
Toolchain / IDE	MDK-ARM 4.73
Firmware Package Name and Version	STM32Cube FW_F1 V1.0.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed