# 1. Description

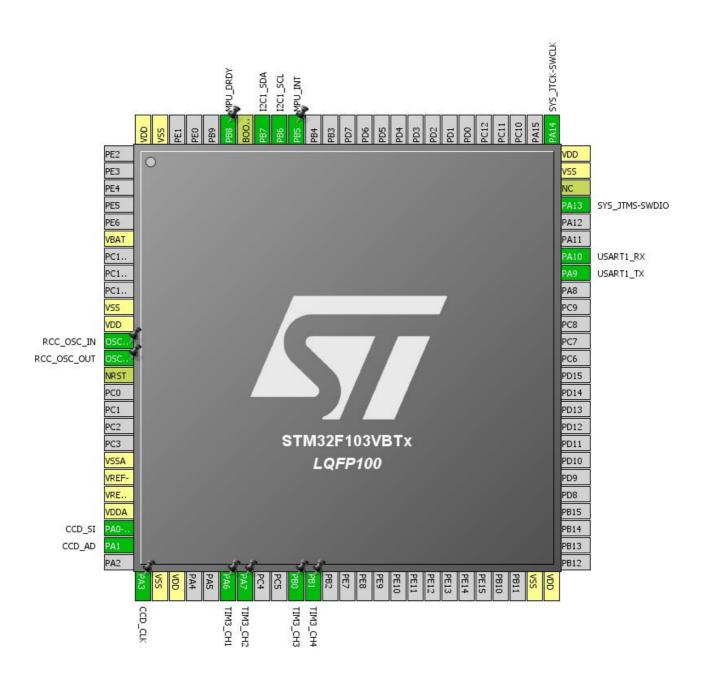
## 1.1. Project

Project Name	FlyVBTx
Generated with:	STM32CubeMX 4.6.0
Date	05/04/2015

### 1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F103
MCU name	STM32F103VBTx
MCU Package	LQFP100
MCU Pin number	100

## 2. Pinout Configuration



# 3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
ADC1	IN1	ADC1_IN1	PA1
1004	I2C:	I2C1_SCL	PB6
I2C1	I2C	I2C1_SDA	PB7
D00	High Speed Clock (HSE):	RCC_OSC_IN	OSC_IN
RCC	Crystal/Ceramic Resonator	RCC_OSC_OUT	OSC_OUT
0.40	Debug:	SYS_JTCK-SWCLK	PA14
SYS	Serial-Wire	SYS_JTMS-SWDIO	PA13
	Channel1: PWM Generation CH1	TIM3_CH1	PA6
	Channel2: PWM Generation CH2	TIM3_CH2	PA7
TIM3	Channel3: PWM Generation CH3	TIM3_CH3	PB0
	Channel4: PWM Generation CH4	TIM3_CH4	PB1
	Mode:	USART1_RX	PA10
USART1	Asynchronous	USART1_TX	PA9

# 4. Pins Configuration

Pin	Pos	Function(s)	Label
OSC_IN	12	RCC_OSC_IN	
OSC_OUT	13	RCC_OSC_OUT	
PA0-WKUP *	23	GPIO_Output	CCD_SI
PA1	24	ADC1_IN1	CCD_AD
PA3 *	26	GPIO_Output	CCD_CLK
PA6	31	TIM3_CH1	
PA7	32	TIM3_CH2	
PB0	35	TIM3_CH3	
PB1	36	TIM3_CH4	
PA9	68	USART1_TX	
PA10	69	USART1_RX	
PA13	72	SYS_JTMS-SWDIO	
PA14	76	SYS_JTCK-SWCLK	
PB5 *	91	GPIO_Input	MPU_INT
PB6	92	I2C1_SCL	
PB7	93	I2C1_SDA	
PB8	95	GPIO_EXTI8	MPU_DRDY

<sup>\*</sup> The pin is affected with an I/O function

## 5. Power Plugin report

#### 5.1. Microcontroller Selection

Serie	STM32F1
Line	STM32F103
MCU	STM32F103VBTx
Datasheet	13587_Rev16

#### 5.2. Parameter Selection

Temperature	25
	3.3

## 6. Software Project

### 6.1. Project Settings

Name	Value
Project Name	Fly∀BTx
Project Folder	E:\C\ARM\32CUBE\FIyVBTx
Toolchain / IDE	MDK-ARM 4.73
Firmware Package Name and Version	STM32Cube FW_F1 V1.0.0

### 6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	

#### 6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed