A3 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8

POWER DRIVER BOARD II

PAGE	CIRCUIT
2	INVERTER-INTERFACE CONVERTER-INTERFACE
3	MAIN SMPS: HIGH VOLTAGE INPUT CIRCUIT
4	MAIN SMPS: LOW VOLTAGE INPUT CIRCUIT
5	MAIN SMPS: INTERLOCK CIRCUIT BETWEEN HIGH- AND LOW VOLTAGE SMPS FEEDBACK CIRCUIT
6	SECONDARY-SIDE RECTIFICATION +24V_HL2, HL2 SHORT-CIRCUIT PROTECTION AND MONITORING CIRCUIT
7	SAFETY RELAY
8	CONVERTER CURRENT MEASUREMENT
9	INVERTER CURRENT MEASUREMENT 15V_CHECK DBR TEMP SENSE OR LINE REACTOR THRMAL SWITCH
10	BRAKE CURRENT MEASUREMENT CURRENT TO FREQUENCY CONVERTER
11	FAN CONTROL
12	INV RELAY DRIVER
13	MECHANICAL PARTS
14- 15	ADD-ON: NETS BY PAGE
16- 21	ADD-ON: CREF PART REPORT
22- 24	LAYOUT DRAWINGS

PART NUMBER GDA26800KP
TEST REQUIREMENT GDA26800KP_TR
BASIC DATA GDA26800KP_BD
BARE BOARD GDA610AAW1

PCB_CODE=DXAAW1--

APPLICATION	BOARD AUTHORIZATION	WITH CONVERTER FUNCTIONALITY	LOW VOLTAGE INPUT RANGE	LINE / MOTOR CURRENT MEASUREMENT AMPLIFICATION FACTOR	WITH FR4 INSULATING COVER ABOVE THE F2 AND F3 FUSES	FAN MAXIMAL OUTPUT VOLTAGE	BRAKE ERROR FADE-OUT (USED IN ARO MODE) INTERFACE PREPARED FOR:	BRAKE CURRENT MEASUREMENT RANGE
GDA26800KP1 / ASSEMBLY_SEC1=NO OVF20CR 9KW NON-REGENERATIVE	AUTHORIZED		26V..56V	-- / 2.245	X	12V..24V	PBX	-8A..+8A
GDA26800KP2 / ASSEMBLY_SEC2=NO OVF20CR 15KW NON-REGENERATIVE	AUTHORIZED		26V..56V	-- / 1.273		12V..24V	PBX_REC	-8A..+8A
GDA26800KP3 / ASSEMBLY_SEC3=NO OVFR2A-406 60A REGENERATIVE	AUTHORIZED	X	26V..56V	1.273 / 1.273	X	8V..14V	SET TO GND, NOT USED	-8A..+8A
GDA26800KP4 / ASSEMBLY_SEC4=NO OVFR2A-412 120A REGENERATIVE	AUTHORIZED	X	26V..56V	3.43 / 3.43	X	12V..24V	SET TO GND, NOT USED	-8A..+8A
GDA26800KP5 / ASSEMBLY_SEC5=NO OVFR2A-416 160A REGENERATIVE	AUTHORIZED	X	22V..56V	3.43 / 3.43	X	12V..24V	SET TO GND, NOT USED	-12A..+12A

DRAWING
TITLE=PDB_II
ABBREV=PDB_II
LAST_MODIFIED=Thu Sep 18 14:49:21 2014

Changes

2014-08-08	G(D)A	PAGE 6	CN576228	JG
2014-09-18	G(D)A	ASSEMBLY_SEC1 AND SEC2 ADDED		AF

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

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CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG **GDA26800KP**

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	28 SHEETS
AUTH	CA47A-000039	SHEET 1

OECB_A3
REV.2004-09-06

INVERTER-INTERFACE

ROOM=INTERFACE

TO MCB3X P10 / GDCB P1

TO PBX P5 / PBX BIDI P5
PBX-INV P5 AND PBX-REC P5

CONVERTER POWER FAIL MONITOR CIRCUIT

CONVERTER-INTERFACE

TO GDCB AND PBX_CONV

P7 CON50 — IU, IV, IW, UDC, TEMP_IGBT, 15V_CHECK, OCT_OVLT, OLOAD_PF_IGBT_INV, BRK_ERR, PFAIL_C, PF_IGBT_INV, TEMP_DBR_BY1_BY2_NC, PWM_O1_N, PWM_U1_N, PWM_O2_N, PWM_U2_N, PWM_O3_N, PWM_U3_N, V2F_IDC, V2F_IBR, I2C_SDA0, I2C_SCL0, BRK_CHOP_N, OUT_EN, FAN_N, BY_N, CHRG_N, INV_N, PVT_V+, PVT_V-, DBD_IN, 24V_HL2

R123 10K 1% 0.125W 0805

ASSEMBLY_SEC1=NO
ASSEMBLY_SEC2=NO
PFAIL_CONV

ASSEMBLY_SEC3=NO
ASSEMBLY_SEC4=NO
OLOAD

ASSEMBLY_SEC5=NO

JP5, JP3, JP2, JP1

ALT_VALUE_SEC3=2.2U ALT_PARTNO_SEC3=GAA234DC1
ALT_VALUE_SEC4=2.2U ALT_PARTNO_SEC4=GAA234DC1
ALT_VALUE_SEC5=2.2U ALT_PARTNO_SEC5=GAA234DC1

P10 CON50 — 24V_SE, 24V_SE_RTN, SPARE_2, SPARE_1, DUMMY_CON3, PWM_O1_N, PWM_U1_N, PWM_O2_N, PWM_U2_N, PWM_O3_N, PWM_U3_N, CHRG_N, INV_N, DUMMY_CON4, BRK_CHOP_N, OUT_EN, I2C_SCL0, I2C_SDA0, DUMMY_CON5, DUMMY_UVLT, OCT_OVLT, OLOAD, BRK_ERR_PBX, V2F_IDC, PF_IGBT, DUMMY_CON6, TEMP_IGBT, UDC, LEM_IU, LEM_IV, DUMMY_CON7, DUMMY_CON8

HL2, V+15V, V-15V, A

P9 CON50 — 1..30: EN-ROUTE TO GDCB P2; 11..50: EN-ROUTE TO PBX_CONV P3; 11..50: EN-ROUTE TO PBX_BIDI P3

IR, IS, VRS_OUT_N, VST_OUT_N, TEMP_CNV, IT, DIO_1C-CNV_FLT, DIO_2C-DEV_FLT, DIO_3C-PWM_XP, DIO_4C-PWM_XN, DIO_5C-PWM_YP, DIO_6C-PWM_YN, DIO_7C-PWM_ZP, DIO_8C-PWM_ZN

PF_IGBT_CNV, OCT_FLT_JMP, REACTOR_THERMAL_FLT_N, LOW_VOLTAGE_MODE, 24V_HL2_FAIL

PWM_RP, PWM_RN, PWM_SP, PWM_SN, PWM_TP, PWM_TN — N.C.

I2C_SDA0, I2C_SCL0, OUT_EN, CHRG_N, PFAIL_CONV, VST_OUT_N, VRS_OUT_N, TEMP_CNV, LEM_IR, LEM_IS

0805 R134 10K 1% 0.125W
0805 R133 10K 1% 0.125W
0805 R127 10K 1% 0.125W

0805 50V C140 100N 10%
0805 50V C136 100N 10%

0805 R128 10K 1% 0.125W

INV_N

U18 74HC14 (VCC:14;GND:7) ROOM=INTERFACE

DUMMY_INV_1_D, DUMMY_INV_1_C, DUMMY_INV_1_B, DUMMY_INV_1_A — N.C.

R59 10K
BAT_SMPS_N

JP8
LOW_VOLTAGE_MODE
LOGIC SIGNAL FOR LOW VOLTAGE MODE DETECTION

DIO_7C-PWM_ZP, DIO_8C-PWM_ZN
JP6, JP4
SPARE_1, SPARE_2
0805 R135 10K 1% 0.125W
0805 R132 10K 1% 0.125W

SOIC U15 24C64
SCL, WP, SDA, A0, A1, A2 (VCC:8 GND:4)

R58 10K 5% 0.062W CAY0603
0805 R160 10K 1% 0.125W

SMD Q5 BC817
R159 10K 1% 0.125W

I2C DEVICE ADR.: #3

ROOM=EEPROM

DRAWING
TITLE=PDB_II
ABBREV=PDB_II

LAST_MODIFIED=Thu Sep 18 14:55:20 2014

Changes

2014-09-18	G(D)A	ASSEMBLY_SEC1 AND SEC2 ADDED	AF

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CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG **GDA26800KP**

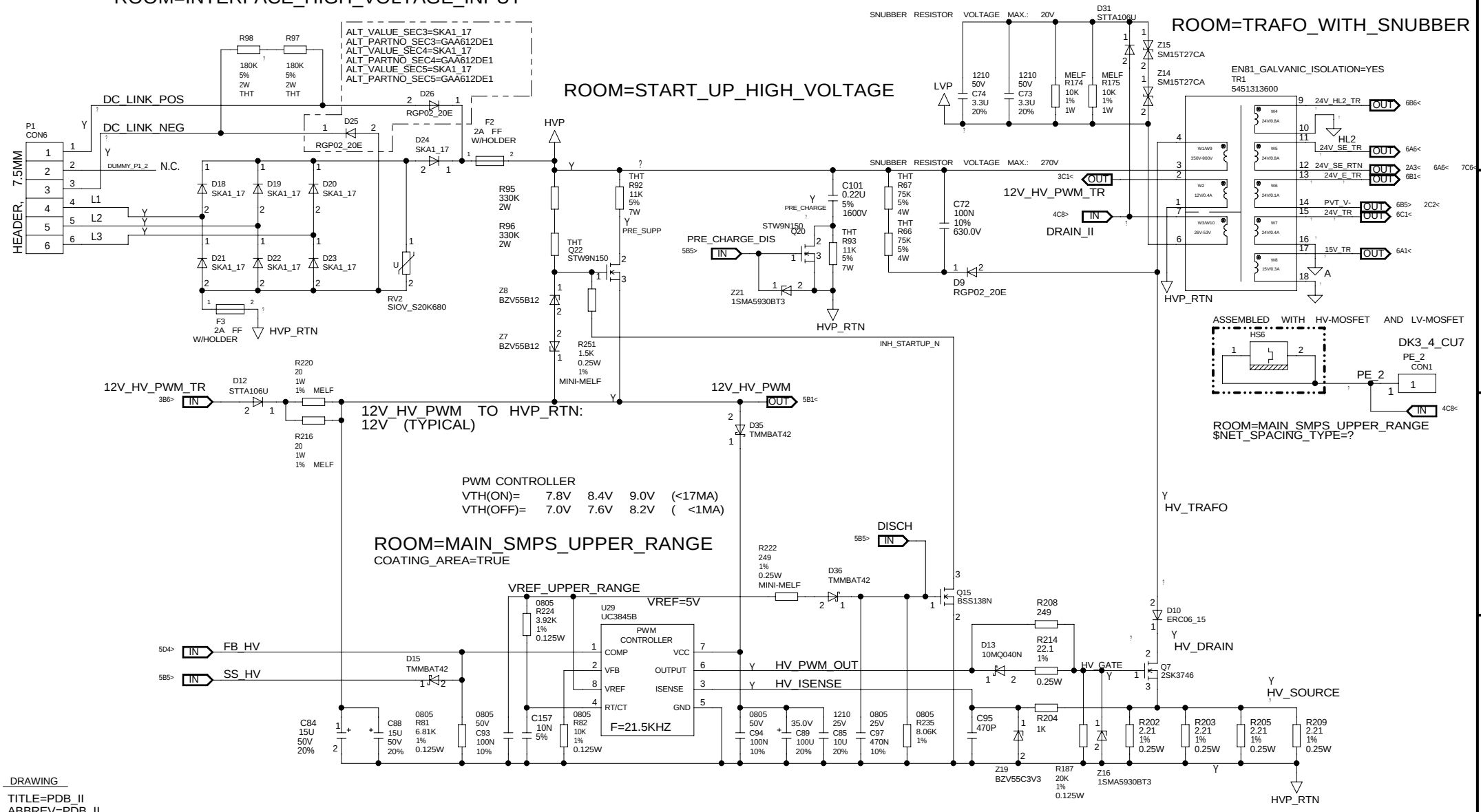
OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 2

OECB_A3
REV.2004-09-06

MAIN SMPS: HIGH VOLTAGE INPUT CIRCUIT

ROOM=INTERFACE_HIGH_VOLTAGE_INPUT



Changes

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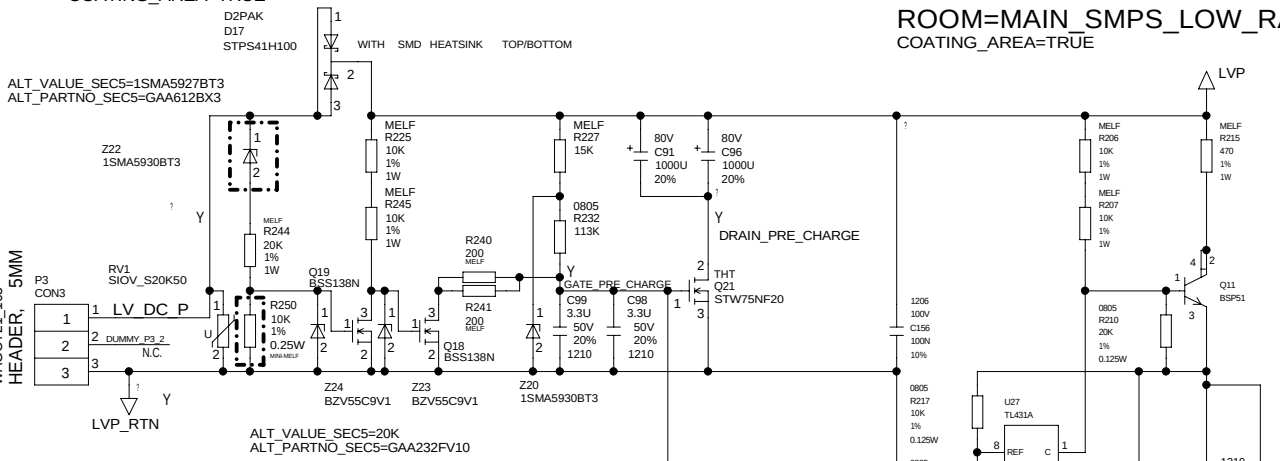
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 3

OECB_A3
REV.2004-09-06

MAIN SMPS: LOW VOLTAGE INPUT CIRCUIT

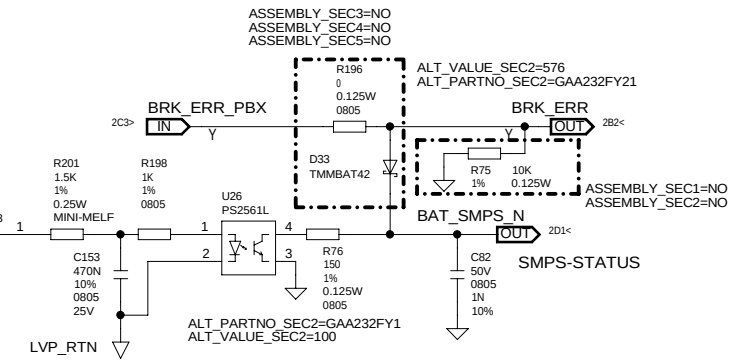
ROOM=INTERFACE_LOW_VOLTAGE_INPUT
COATING_AREA=TRUE



ROOM=MAIN_SMPS_LOW_RANGE_PWM_SUPPLY
COATING_AREA=TRUE

BRAKE ERROR FADE-OUT (USED IN ARO MODE)

ROOM=BRK_ERR_FADE_OUT_SMPS_STATUS
COATING_AREA=TRUE
EN81_GALVANIC_ISOLATION=YES

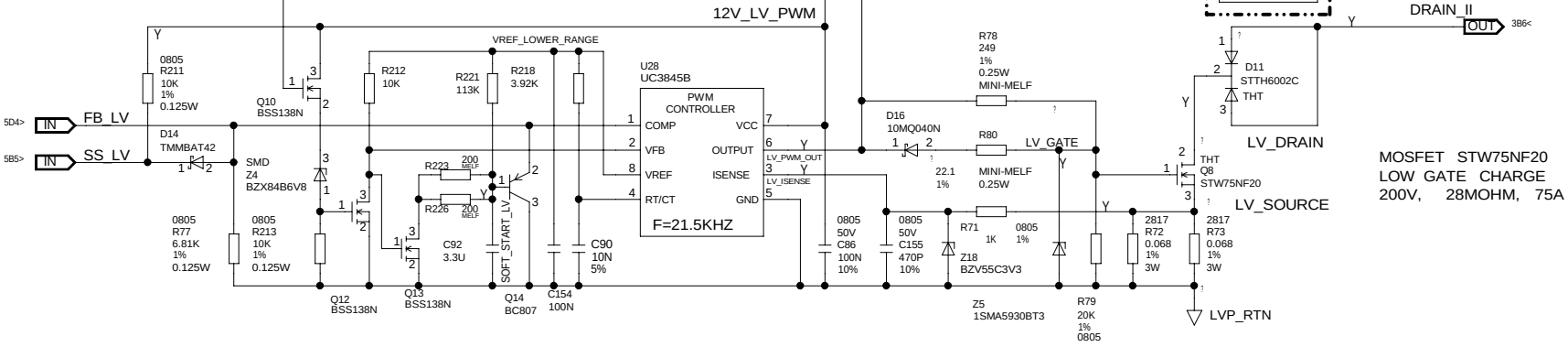


INPUT VOLTAGE RANGE:

GCA26800KP1..KP4:
LV_DC_P TO LVP_RTN: 26V..56V

GCA26800KP5:
LV_DC_P TO LVP_RTN: 22V..56V

ROOM=MAIN_SMPS_LOWER_RANGE
COATING_AREA=TRUE



MOSFET STW75NF20
LOW GATE CHARGE
200V, 28MOHM, 75A

DRAWING
TITLE=DCB_II
ABBREV=DCB_II
LAST_MODIFIED=Wed Sep 17 16:53:46 2014

Changes

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

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CIRCUIT DIAGRAM FOR
PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
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APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 4

OECB_A3
REV.2004-09-06

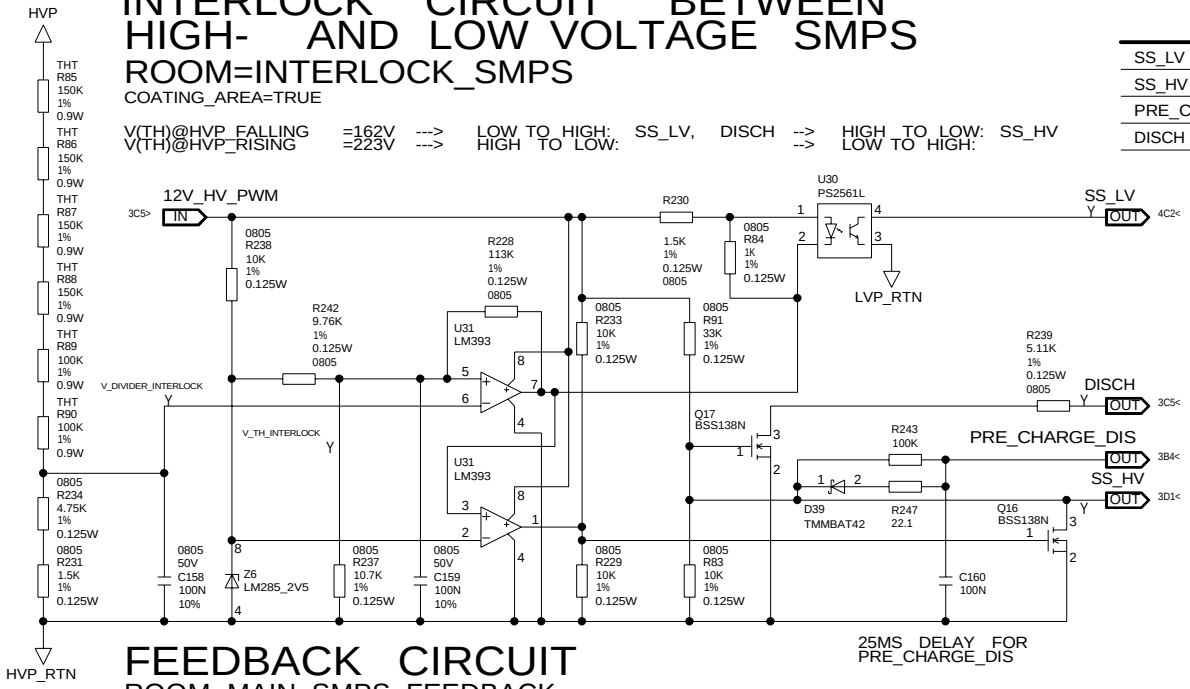
MAIN SMPS:

INTERLOCK CIRCUIT BETWEEN HIGH- AND LOW VOLTAGE SMPS

ROOM=INTERLOCK_SMPS
COATING_AREA=TRUE

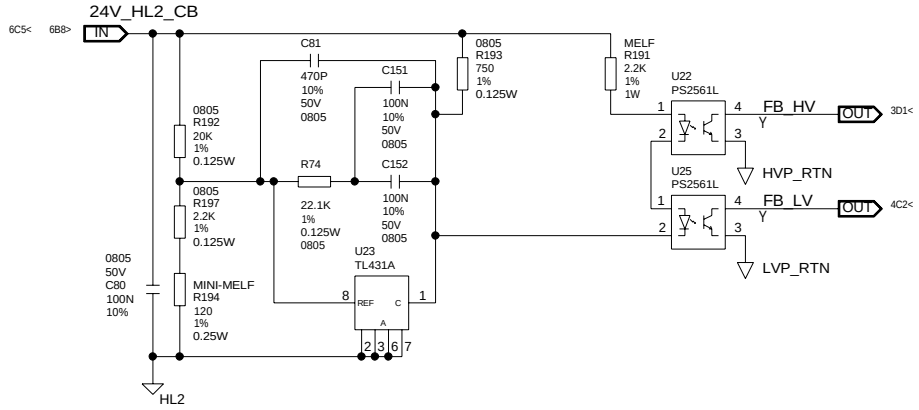
V(TH)@HVP_FALLING =162V ---> LOW TO HIGH: SS_LV, DISCH --> HIGH TO LOW: SS_HV
V(TH)@HVP_RISING =223V ---> HIGH TO LOW: --> LOW TO HIGH:

	HVP <162V	HVP >223V
SS_LV	HIGH-IMPEDANCE	LOW
SS_HV	LOW	HIGH-IMPEDANCE
PRE_CHARGE_DIS	LOW (22.1 OHM)	HIGH-IMPEDANCE
DISCH	HIGH-IMPEDANCE	LOW (5.11K)



FEEDBACK CIRCUIT

ROOM=MAIN_SMPS_FEEDBACK
COATING_AREA=TRUE



DRAWING
TITLE=PDB_II
ABBREV=PDB_II
LAST_MODIFIED=Wed Sep 17 16:53:44 2014

Changes

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PDB_II

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OTIS ENGINEERING CENTER
BERLIN, GERMANY

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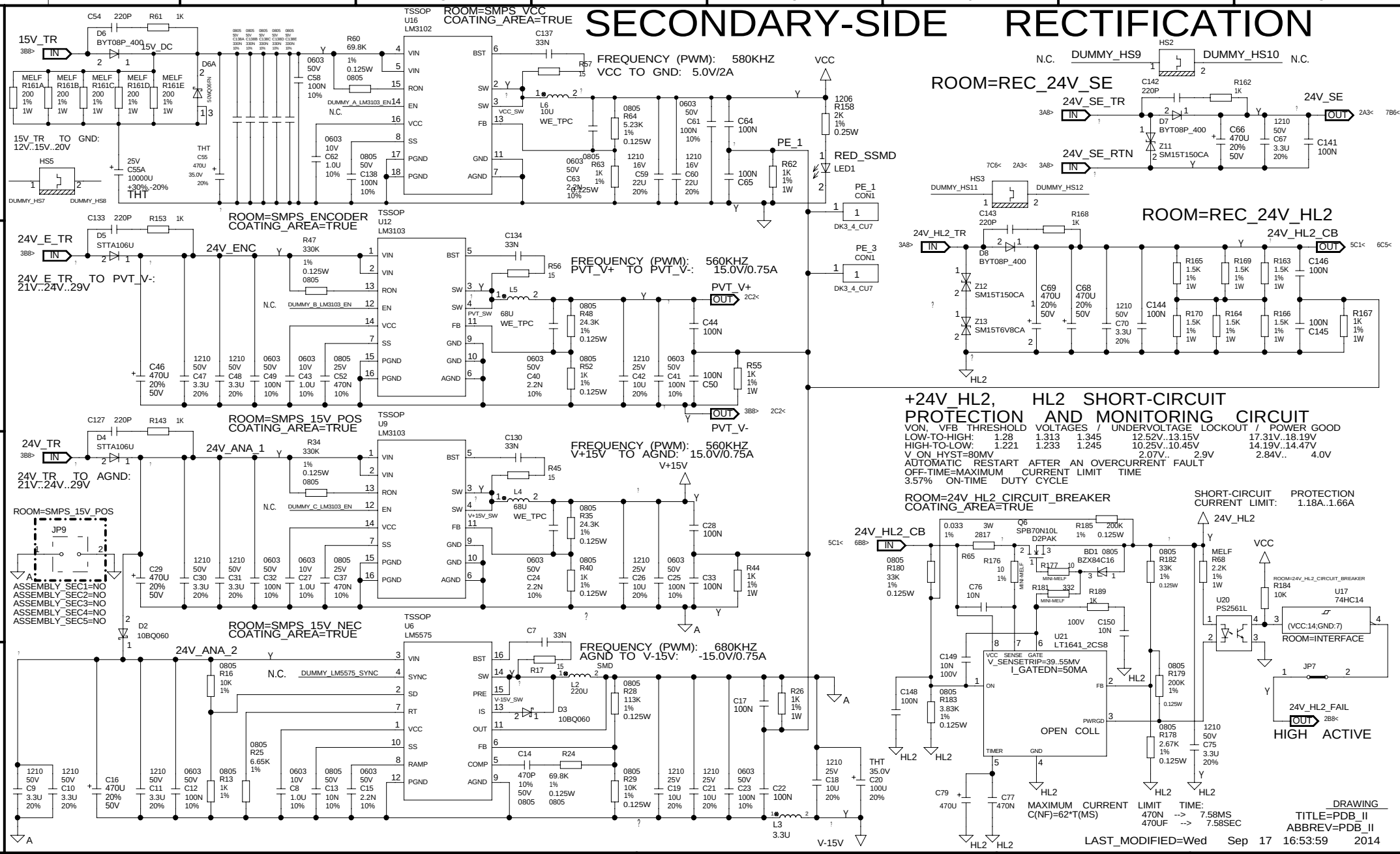
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 5

OECB_A3
REV 2004-09-06

SECONDARY-SIDE RECTIFICATION



Changes

2014-08-08 G(D)A C55A, C138A-C138E, R161A-R161E, D6A ADDED; C56, C57 REMOVED CN576228 JG

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

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CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED CADENCE-CONCEPT

DWG **GDA26800KP**

OTIS ENGINEERING CENTER
BERLIN, GERMANY

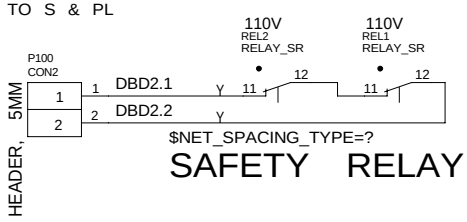
DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 6

OECB_A3
REV.2004-09-06

SAFETY RELAY

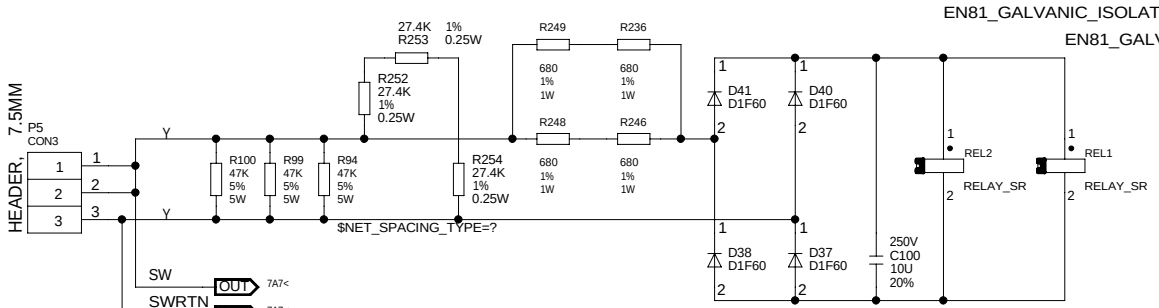
ROOM=2ND_DBD_TO_S_AND_PL

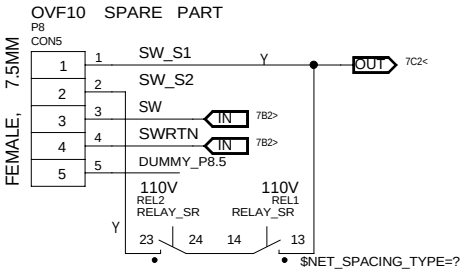
SIMBA!
TO S & PL



SAFETY RELAY

ROOM=EN81_SAFETY_REL

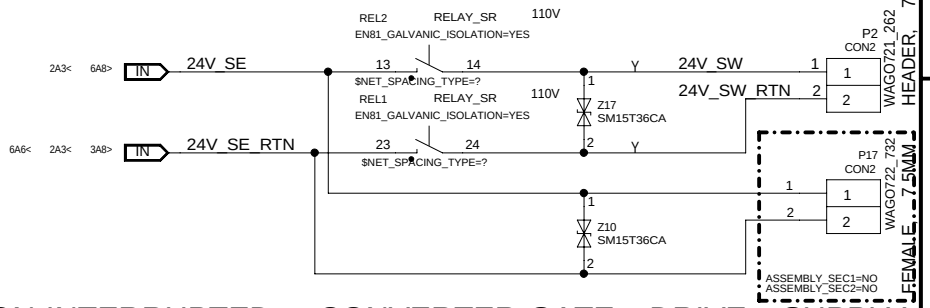




SAFETY RELAY

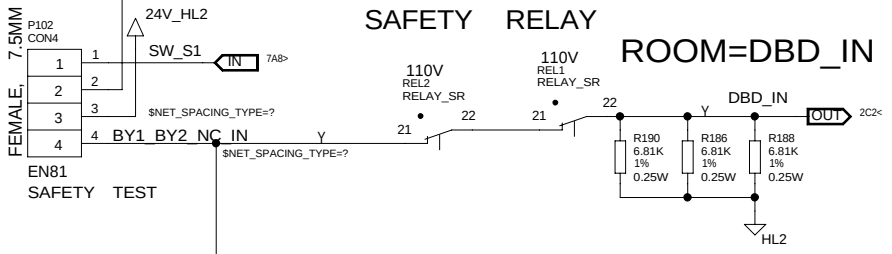
INTERRUPTED INVERTER-GATE DRIVE SUPPLY

ROOM=EN81_IGBT_SUPPLY



NON-INTERRUPTED CONVERTER-GATE DRIVE SUPPLY

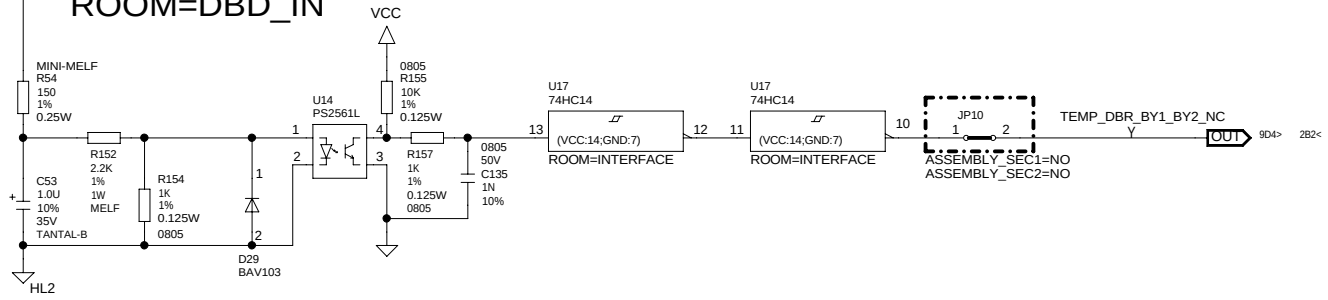
ROOM=REC_24V_SE



SAFETY RELAY

ROOM=DBD_IN

ROOM=DBD_IN



DRAWING

TITLE=PDB_II
ABBREV=PDB_II

LAST_MODIFIED=Thu Sep 18 14:54:55 2014

Changes

2014-09-18	G(D)A	ASSEMBLY_SEC1 AND SEC2 ADDED	AF

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

Otis
A United Technologies Company

CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG **GDA26800KP**

OTIS ENGINEERING CENTER
BERLIN, GERMANY

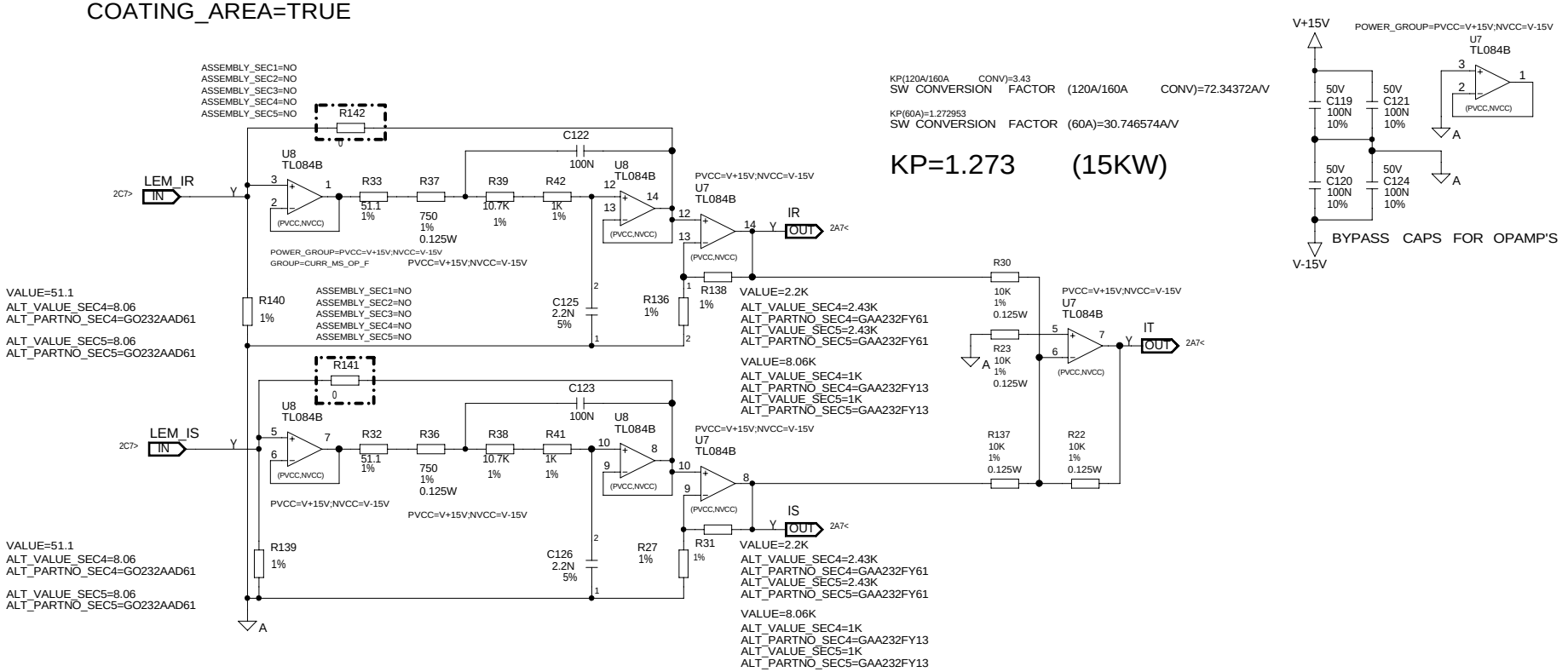
DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 7

OECB_A3
REV.2004-09-06

A3 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8

CONVERTER CURRENT MEASUREMENT

ROOM=CONVERTER_CURRENT_MEASUREMENT
COATING_AREA=TRUE



DRAWING
TITLE=PBX_CONV
ABBREV=PBX_CONV

LAST_MODIFIED=Wed Sep 17 16:53:50 2014

Changes

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

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DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

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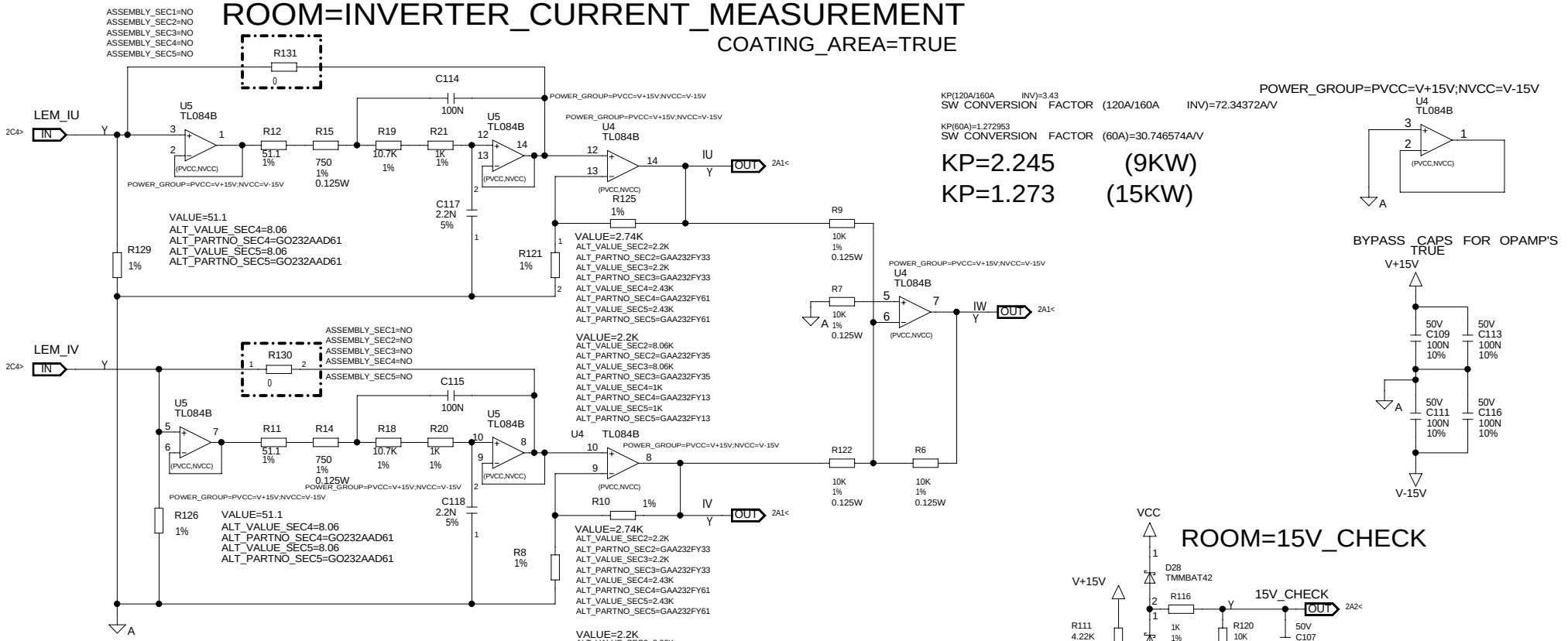
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 8

OECB_A3
REV 2004-09-06

INVERTER CURRENT MEASUREMENT

ROOM=INVERTER_CURRENT_MEASUREMENT

COATING_AREA=TRUE



KP(120A/160A INV)=3.43
SW CONVERSION FACTOR (120A/160A INV)=72.34372A/V

KP(60A)=1.272953
SW CONVERSION FACTOR (60A)=30.746574A/V

KP=2.245 (9KW)

KP=1.273 (15KW)

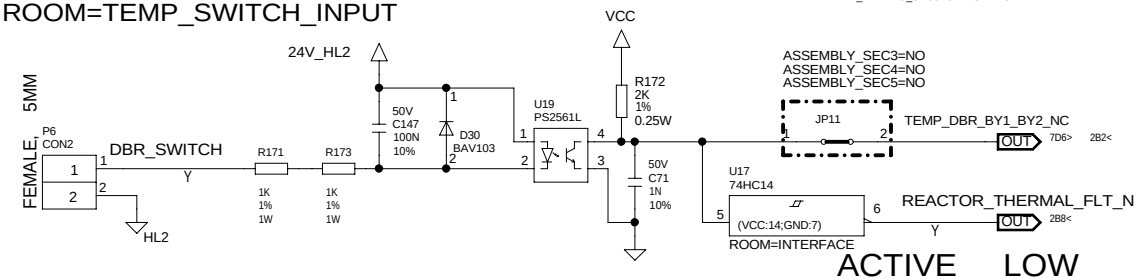
POWER_GROUP=PVCC=V+15V;NVCC=V-15V

BYPASS CAPS FOR OPAMP'S
TRUE

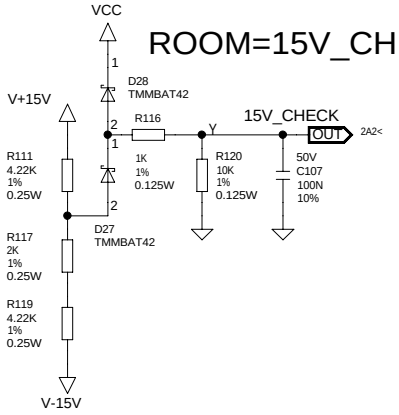
DBR TEMP SENSE

OR LINE REACTOR THRMAL SWITCH

ROOM=TEMP_SWITCH_INPUT



ACTIVE LOW



ROOM=15V_CHECK

DRAWING

TITLE=PDB_II

ABBREV=PDB_II

LAST_MODIFIED=Wed Sep 17 16:53:40 2014

Changes

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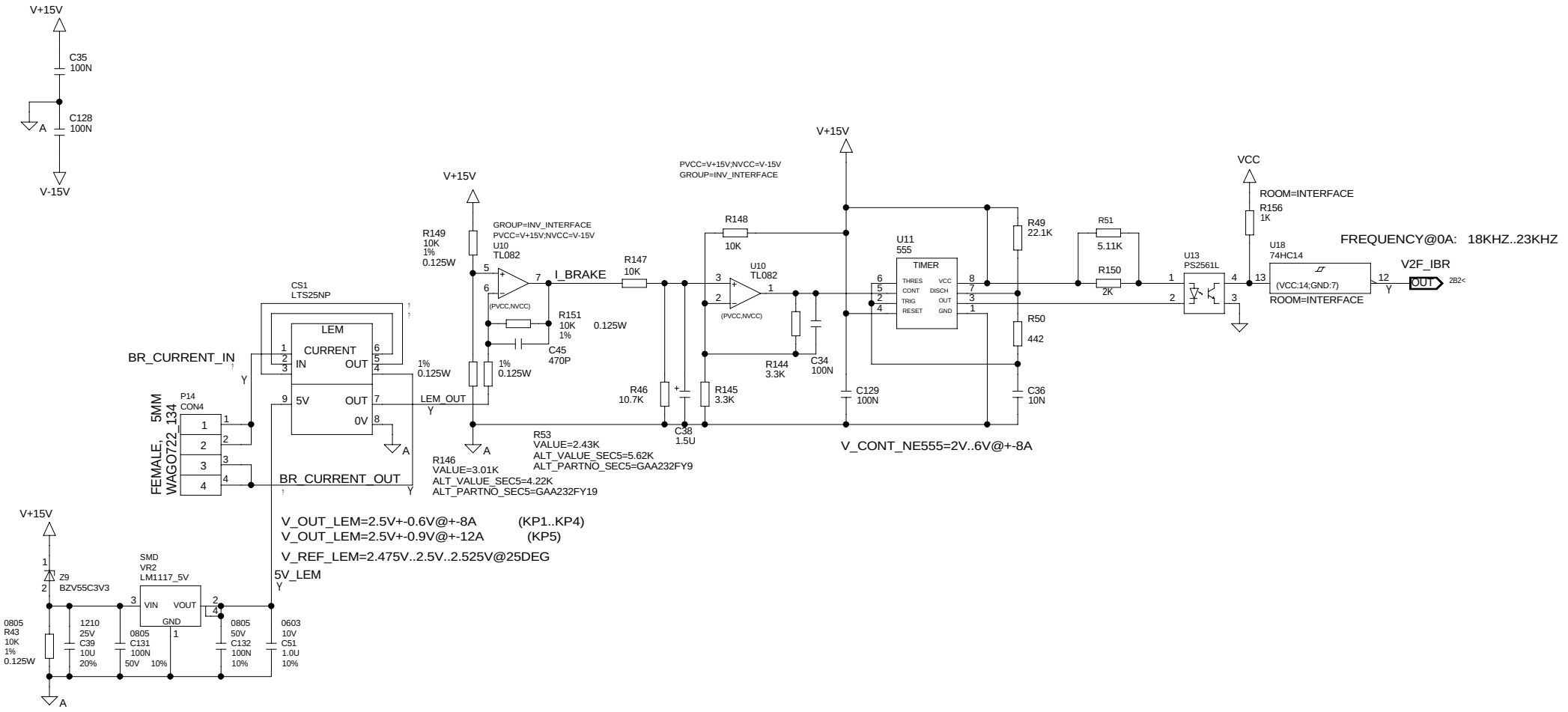
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 9

OECB_A3
REV.2004-09-06

BRAKE CURRENT MEASUREMENT
CURRENT TO FREQUENCY CONVERTER

ROOM=BRAKE_CURRENT_MEASUREMENT
COATING_AREA=TRUE



DRAWING
TITLE=PDB_II
ABBREV=PDB_II
LAST_MODIFIED=Wed Sep 17 16:53:54 2014

Changes

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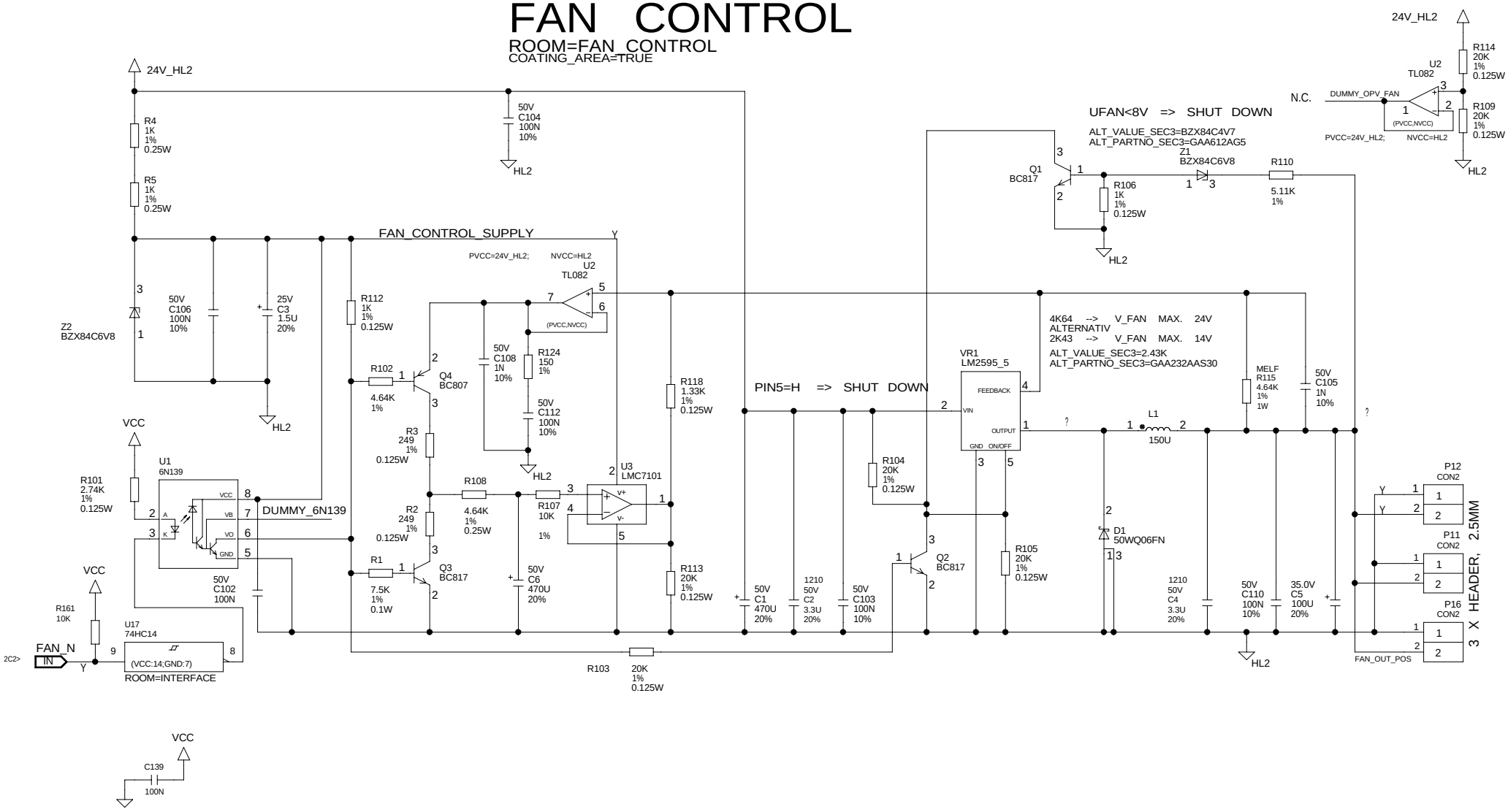
OECB_A3
REV 2004-09-06

FAN CONTROL

ROOM=FAN_CONTROL
COATING_AREA=TRUE



DRAWING

TITLE=PDB_II
ABBREV=PDB_II

LAST_MODIFIED=Wed Sep 17 16:53:42 2014

Changes

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

Otis
A United Technologies Company

CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG **GDA26800KP**

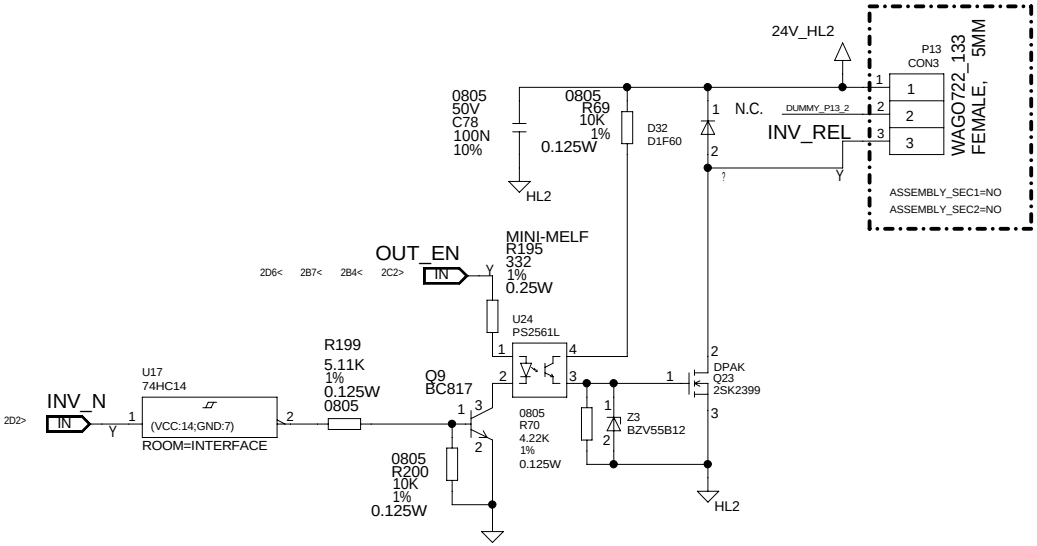
OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 11

OECB_A3
REV 2004-09-06

INV RELAY DRIVER

ROOM=INV_RELAY_DRIVER



DRAWING

TITLE=PBX_CONV
ABBREV=PBX_CONV

LAST_MODIFIED=Thu Sep 18 14:52:25 2014

Changes

2014-09-18	G(D)A	ASSEMBLY_SEC1 AND SEC2 ADDED		AF

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OECB_A3
REV.2004-09-06

A3 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8

MECHANICAL PARTS

MAIN POWER SUPPLY

FUSE	1000V AC/DC FUSE_F2 2AFF	GAA375CE2
FUSE	1000V AC/DC FUSE_F3 2AFF	GAA375CE2
FUSE HOLDER	FUSE_HOLDER_1_F2	GAA653R1
FUSE HOLDER	FUSE_HOLDER_1_F3	GAA653R1
INSULATOR	TR1_CORE_INSULATOR	GAA129GB1

MAIN SMPS: MOSFETS (LV,HV)

THERMO CAP	CAP_HSINKS_6_1 GAA180CJR2	CLIP	CLIP_HSINKS_6_3 GAA180CJR3
THERMO CAP	CAP_HSINKS_6_2 GAA180CJR2	CLIP	CLIP_HSINKS_6_4 GAA180CJR3

MAIN SMPS: DIODE (LV)

THERMO CAP	CAP_HSINKS_4_1 GAA180CJR2	CLIP	CLIP_HSINKS_4_2 GAA180CJR1

SPACER	SPACER_MP1	GAA180CJW8
SPACER	SPACER_MP2	GAA180CJW8
SPACER	SPACER_MP3	GAA180CJW8
INSULATOR	INSULATOR	GAA129FR1

FR4 INSULATING COVER ABOVE THE F2 AND F3 FUSES

ASSEMBLY_SEC2=NO

DRAWING

TITLE=PDB_II
ABBREV=PDB_II

LAST_MODIFIED=Wed Sep 17 16:54:04 2014

Changes

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AUTH	CA47A-000039	SHEET 13

OECB_A3
REV 2004-09-06

Title: Basenet Report
Design: pdb_ii
Date: Aug 6 9:26:56 2014

Base nets and synonyms for proj_lib.PDB_II(@proj_lib.pdb_ii(sch_1))

Base Signal	Synonyms	Location([Zone][dir])
5V_LEM	5V_LEM - @proj_lib.PDB_II	10D2
12V_HV_PWM	12V_HV_PWM - @proj_lib.PDB_II	3C5> 5B1<
12V_HV_PWM_TR	12V_HV_PWM_TR - @proj_lib.PDB_II	3B6> 3C1<
12V_LV_PWM	12V_LV_PWM - @proj_lib.PDB_II	4C5
15V_CHECK	15V_CHECK - @proj_lib.PDB_II	9C7> 2A2<
15V_DC	15V_DC - @proj_lib.PDB_II	6A1
15V_TR	15V_TR - @proj_lib.PDB_II	3B8> 6A1<
24V_ANA_1	24V_ANA_1 - @proj_lib.PDB_II	6C2
24V_ANA_2	24V_ANA_2 - @proj_lib.PDB_II	6D1
24V_ENC	24V_ENC - @proj_lib.PDB_II	6B2
24V_E_TR	24V_E_TR - @proj_lib.PDB_II	3B8> 6B1<
24V_HL2_CB	24V_HL2_CB - @proj_lib.PDB_II	6B8> 5C1< 6C5<
24V_HL2_FAIL	24V_HL2_FAIL - @proj_lib.PDB_II	6D8> 2B8<
	DIO_6C-PWM_YN - @proj_lib.PDB_II	2B6
24V_HL2_TR	24V_HL2_TR - @proj_lib.PDB_II	3A8> 6B6<
24V_SE	24V_SE - @proj_lib.PDB_II	6A8> 2A3< 7B6<
24V_SE_RTN	24V_SE_RTN - @proj_lib.PDB_II	3A8> 2A3< 6A6< 7C6<
24V_SE_TR	24V_SE_TR - @proj_lib.PDB_II	3A8> 6A6<
24V_SW	24V_SW - @proj_lib.PDB_II	7B8
24V_SW_RTN	24V_SW_RTN - @proj_lib.PDB_II	7C8
24V_TR	24V_TR - @proj_lib.PDB_II	3B8> 6C1<
BAT_SMPS_N	BAT_SMPS_N - @proj_lib.PDB_II	4B8> 2D1<
BRK_CHOP_N	BRK_CHOP_N - @proj_lib.PDB_II	2B1 2B4
BRK_ERR	BRK_ERR - @proj_lib.PDB_II	4B8> 2B2<
BRK_ERR_PBX	BRK_ERR_PBX - @proj_lib.PDB_II	2C3> 4B6<
BR_CURRENT_IN	BR_CURRENT_IN - @proj_lib.PDB_II	10C1
BR_CURRENT_OUT	BR_CURRENT_OUT - @proj_lib.PDB_II	10C2
BY1_BY2_NC_IN	BY1_BY2_NC_IN - @proj_lib.PDB_II	7C1
BY_N	BY_N - @proj_lib.PDB_II	2C1
CHRG_N	CHRG_N - @proj_lib.PDB_II	2C2> 2C2> 2C7<
DBD2.1	DBD2.1 - @proj_lib.PDB_II	7A1
DBD2.2	DBD2.2 - @proj_lib.PDB_II	7A1
DBD_IN	DBD_IN - @proj_lib.PDB_II	7C4> 2C2<
DBR_SWITCH	DBR_SWITCH - @proj_lib.PDB_II	9D1
DC_LINK_NEG	DC_LINK_NEG - @proj_lib.PDB_II	3A1
DC_LINK_POS	DC_LINK_POS - @proj_lib.PDB_II	3A1
DIO_1C-CNV_FLT	DIO_1C-CNV_FLT - @proj_lib.PDB_II	2A6
DIO_2C-DEV_FLT	DIO_2C-DEV_FLT - @proj_lib.PDB_II	2B6
	PF_IGBT_CNV - @proj_lib.PDB_II	2B7
DIO_3C-PWM_XP	DIO_3C-PWM_XP - @proj_lib.PDB_II	2B6
	OCT_FLT_JMP - @proj_lib.PDB_II	2B7
DIO_4C-PWM_XN	DIO_4C-PWM_XN - @proj_lib.PDB_II	2B6
	REACTOR_THERMAL_FLT_N - @proj_lib.PDB_II	9D4> 2B8<
DIO_5C-PWM_YP	DIO_5C-PWM_YP - @proj_lib.PDB_II	2B6
	LOW_VOLTAGE_MODE - @proj_lib.PDB_II	2D3> 2B8<
DIO_7C-PWM_ZP	DIO_7C-PWM_ZP - @proj_lib.PDB_II	2B7<> 2D5<>
DIO_8C-PWM_ZN	DIO_8C-PWM_ZN - @proj_lib.PDB_II	2B7<> 2D5<>
DISCH	DISCH - @proj_lib.PDB_II	5B5> 3C5<
DRAIN_II	DRAIN_II - @proj_lib.PDB_II	4C8> 3B6<
DRAIN_PRE_CHARGE	DRAIN_PRE_CHARGE - @proj_lib.PDB_II	4B3
DUMMY_6N139	DUMMY_6N139 - @proj_lib.PDB_II	11C2
DUMMY_A_LM3103_EN	DUMMY_A_LM3103_EN - @proj_lib.PDB_II	6A2
DUMMY_B_LM3103_EN	DUMMY_B_LM3103_EN - @proj_lib.PDB_II	6B2
DUMMY_CON3	DUMMY_CON3 - @proj_lib.PDB_II	2A4
DUMMY_CON4	DUMMY_CON4 - @proj_lib.PDB_II	2B4
DUMMY_CON5	DUMMY_CON5 - @proj_lib.PDB_II	2B4
DUMMY_CON6	DUMMY_CON6 - @proj_lib.PDB_II	2C4
DUMMY_CON7	DUMMY_CON7 - @proj_lib.PDB_II	2C4
DUMMY_CON8	DUMMY_CON8 - @proj_lib.PDB_II	2C4
DUMMY_C_LM3103_EN	DUMMY_C_LM3103_EN - @proj_lib.PDB_II	6C2
DUMMY_HS7	DUMMY_HS7 - @proj_lib.PDB_II	6A1
DUMMY_HS8	DUMMY_HS8 - @proj_lib.PDB_II	6A1
DUMMY_HS9	DUMMY_HS9 - @proj_lib.PDB_II	6A7
DUMMY_HS10	DUMMY_HS10 - @proj_lib.PDB_II	6A7
DUMMY_HS11	DUMMY_HS11 - @proj_lib.PDB_II	6A6
DUMMY_HS12	DUMMY_HS12 - @proj_lib.PDB_II	6A6
DUMMY_INV_1_A	DUMMY_INV_1_A - @proj_lib.PDB_II	2D4
DUMMY_INV_1_B	DUMMY_INV_1_B - @proj_lib.PDB_II	2D4
DUMMY_INV_1_C	DUMMY_INV_1_C - @proj_lib.PDB_II	2D4
DUMMY_INV_1_D	DUMMY_INV_1_D - @proj_lib.PDB_II	2D4
DUMMY_LM5575_SYNC	DUMMY_LM5575_SYNC - @proj_lib.PDB_II	6D2
DUMMY_OPV_FAN	DUMMY_OPV_FAN - @proj_lib.PDB_II	11A7
DUMMY_P1_2	DUMMY_P1_2 - @proj_lib.PDB_II	3A1
DUMMY_P3_2	DUMMY_P3_2 - @proj_lib.PDB_II	4B1
DUMMY_P8.5	DUMMY_P8.5 - @proj_lib.PDB_II	7A7
DUMMY_P13_2	DUMMY_P13_2 - @proj_lib.PDB_II	12B4
DUMMY_UVLT	DUMMY_UVLT - @proj_lib.PDB_II	2B4
FAN_CONTROL_SUPPLY	FAN_CONTROL_SUPPLY - @proj_lib.PDB_II	11B3
FAN_N	FAN_N - @proj_lib.PDB_II	2C2> 11C1<
FAN_OUT_POS	FAN_OUT_POS - @proj_lib.PDB_II	11C8
FB_HV	FB_HV - @proj_lib.PDB_II	5D4> 3D1<
FB_LV	FB_LV - @proj_lib.PDB_II	5D4> 4C2<
GATE_PRE_CHARGE	GATE_PRE_CHARGE - @proj_lib.PDB_II	4B3
HV_DRAIN	HV_DRAIN - @proj_lib.PDB_II	3D7
HV_GATE	HV_GATE - @proj_lib.PDB_II	3D6
HV_ISENSE	HV_ISENSE - @proj_lib.PDB_II	3D5
HV_PWM_OUT	HV_PWM_OUT - @proj_lib.PDB_II	3D5
HV_SOURCE	HV_SOURCE - @proj_lib.PDB_II	3D7
HV_TRAFO	HV_TRAFO - @proj_lib.PDB_II	3C7
I2C_SCL0	I2C_SCL0 - @proj_lib.PDB_II	2B1 2B4 2B6 2C7
I2C_SDA0	I2C_SDA0 - @proj_lib.PDB_II	2B1 2B4 2B6 2C7
INH_STARTUP_N	INH_STARTUP_N - @proj_lib.PDB_II	3B5
INV_N	INV_N - @proj_lib.PDB_II	2D2> 2D2> 2D2> 12C2<
INV_REL	INV_REL - @proj_lib.PDB_II	12B4
IR	IR - @proj_lib.PDB_II	8B5> 2A7<
IS	IS - @proj_lib.PDB_II	8C5> 2A7<
IT	IT - @proj_lib.PDB_II	8B7> 2A7<

Changes

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AUTH	CA47A-000039	SHEET 14

OECB_A3
REV 2004-09-06

IU	IU - @proj_lib.PDB_II	9A5> 2A1<
IV	IV - @proj_lib.PDB_II	9C5> 2A1<
IW	IW - @proj_lib.PDB_II	9B6> 2A1<
I_BRAKE	I_BRAKE - @proj_lib.PDB_II	10B3
L1	L1 - @proj_lib.PDB_II	3B1
L2	L2 - @proj_lib.PDB_II	3B1
L3	L3 - @proj_lib.PDB_II	3B1
LEM_IR	LEM_IR - @proj_lib.PDB_II	2C7> 8B2<
LEM_IS	LEM_IS - @proj_lib.PDB_II	2C7> 8C2<
LEM_IU	LEM_IU - @proj_lib.PDB_II	2C4> 9A1<
LEM_IV	LEM_IV - @proj_lib.PDB_II	2C4> 9B1<
LEM_OUT	LEM_OUT - @proj_lib.PDB_II	10C3
LV_DC_P	LV_DC_P - @proj_lib.PDB_II	4B1
LV_DRAIN	LV_DRAIN - @proj_lib.PDB_II	4C7
LV_GATE	LV_GATE - @proj_lib.PDB_II	4C6
LV_ISENSE	LV_ISENSE - @proj_lib.PDB_II	4D5
LV_PWM_OUT	LV_PWM_OUT - @proj_lib.PDB_II	4C5
LV_SOURCE	LV_SOURCE - @proj_lib.PDB_II	4D7
OCT_OVLT	OCT_OVLT - @proj_lib.PDB_II	2A1 2B4
OLOAD	OLOAD - @proj_lib.PDB_II	2B3> 2A3<
OLOAD_PF_IGBT_INV	OLOAD_PF_IGBT_INV - @proj_lib.PDB_II	2B1
OUT_EN	OUT_EN - @proj_lib.PDB_II	2C2> 2B4< 2B7< 2D6< 12B3<
PE_1	PE_1 - @proj_lib.PDB_II	6A5
PE_2	PE_2 - @proj_lib.PDB_II	3C8< 4C8<
PFAIL_C	PFAIL_C - @proj_lib.PDB_II	2B1
PFAIL_CONV	PFAIL_CONV - @proj_lib.PDB_II	2C7> 2A3<
PF_IGBT	PF_IGBT - @proj_lib.PDB_II	2C4
PF_IGBT_INV	PF_IGBT_INV - @proj_lib.PDB_II	2B1
PRE_CHARGE	PRE_CHARGE - @proj_lib.PDB_II	3B5
PRE_CHARGE_DIS	PRE_CHARGE_DIS - @proj_lib.PDB_II	5B5> 3B4<
PRE_SUPP	PRE_SUPP - @proj_lib.PDB_II	3B4
PVT_SW	PVT_SW - @proj_lib.PDB_II	6B3
PVT_V+	PVT_V+ - @proj_lib.PDB_II	6B5> 2C2<
PVT_V-	PVT_V- - @proj_lib.PDB_II	3B8> 6B5> 2C2<
PWM_O1_N	PWM_O1_N - @proj_lib.PDB_II	2A4 2B1
PWM_O2_N	PWM_O2_N - @proj_lib.PDB_II	2B1 2B4
PWM_O3_N	PWM_O3_N - @proj_lib.PDB_II	2B1 2B4
PWM_RN	PWM_RN - @proj_lib.PDB_II	2B7>
PWM_RP	PWM_RP - @proj_lib.PDB_II	2B7>
PWM_SN	PWM_SN - @proj_lib.PDB_II	2B7>
PWM_SP	PWM_SP - @proj_lib.PDB_II	2B7>
PWM_TN	PWM_TN - @proj_lib.PDB_II	2B7>
PWM_TP	PWM_TP - @proj_lib.PDB_II	2B7>
PWM_U1_N	PWM_U1_N - @proj_lib.PDB_II	2B1 2B4
PWM_U2_N	PWM_U2_N - @proj_lib.PDB_II	2B1 2B4
PWM_U3_N	PWM_U3_N - @proj_lib.PDB_II	2B1 2B4
SOFT_START_LV	SOFT_START_LV - @proj_lib.PDB_II	4D4
SPARE_1	SPARE_1 - @proj_lib.PDB_II	2A3<> 2D7<>
SPARE_2	SPARE_2 - @proj_lib.PDB_II	2A3<> 2D7<>
SS_HV	SS_HV - @proj_lib.PDB_II	5B5> 3D1<
SS_LV	SS_LV - @proj_lib.PDB_II	5B5> 4C2<
SW	SW - @proj_lib.PDB_II	7B2> 7A7<
SWRTN	SWRTN - @proj_lib.PDB_II	7B2> 7A7<
SW_S1	SW_S1 - @proj_lib.PDB_II	7A8> 7C2<
SW_S2	SW_S2 - @proj_lib.PDB_II	7A7
TEMP_CNV	TEMP_CNV - @proj_lib.PDB_II	2C7> 2A7<
TEMP_DBR_BY1_BY2_NC	TEMP_DBR_BY1_BY2_NC - @proj_lib.PDB_II	7D6> 9D4> 2B2<
TEMP_IGBT	TEMP_IGBT - @proj_lib.PDB_II	2A1 2C4
UDC	UDC - @proj_lib.PDB_II	2A1 2C4
V+15V_SW	V+15V_SW - @proj_lib.PDB_II	6C3
V-15V_SW	V-15V_SW - @proj_lib.PDB_II	6D3
V2F_IBR	V2F_IBR - @proj_lib.PDB_II	10B8> 2B2<
V2F_IDC	V2F_IDC - @proj_lib.PDB_II	2B1 2C4
VCC_SW	VCC_SW - @proj_lib.PDB_II	6A3
VREF_LOWER_RANGE	VREF_LOWER_RANGE - @proj_lib.PDB_II	4C4
VREF_UPPER_RANGE	VREF_UPPER_RANGE - @proj_lib.PDB_II	3C3
VRS_OUT_N	VRS_OUT_N - @proj_lib.PDB_II	2C7> 2A7<
VST_OUT_N	VST_OUT_N - @proj_lib.PDB_II	2C7> 2A7<
V_DIVIDER_INTERLOCK	V_DIVIDER_INTERLOCK - @proj_lib.PDB_II	5B1
V_TH_INTERLOCK	V_TH_INTERLOCK - @proj_lib.PDB_II	5B2

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OECB_A3
REV 2004-09-06

Title: Cref Part Report
Design: pdb_ii
Date: Aug 6 9:26:56 2014

BD1	BZX84C16_SMD	pdb_ii[6C7]
C1	CAP_THT	pdb_ii[11C4]
C2	C_1210	pdb_ii[11C5]
C3	CAP_TANTAL-B	pdb_ii[11B2]
C4	C_1210	pdb_ii[11C7]
C5	CAP_THT	pdb_ii[11C7]
C6	CAP_THT	pdb_ii[11C3]
C7	C_0805	pdb_ii[6C4]
C8	C_0603	pdb_ii[6D2]
C9	C_1210	pdb_ii[6D1]
C10	C_1210	pdb_ii[6D1]
C11	C_1210	pdb_ii[6D1]
C12	C_0603	pdb_ii[6D1]
C13	C_0805	pdb_ii[6D2]
C14	C_0805	pdb_ii[6D3]
C15	C_0603	pdb_ii[6D2]
C16	CAP_THT	pdb_ii[6D1]
C17	C_1206	pdb_ii[6D5]
C18	C_1210	pdb_ii[6D5]
C19	C_1210	pdb_ii[6D4]
C20	CAP_THT	pdb_ii[6D5]
C21	C_1210	pdb_ii[6D4]
C22	C_1206	pdb_ii[6D5]
C23	C_0603	pdb_ii[6D5]
C24	C_0603	pdb_ii[6C4]
C25	C_0603	pdb_ii[6C4]
C26	C_1210	pdb_ii[6C4]
C27	C_0603	pdb_ii[6C2]
C28	C_1206	pdb_ii[6C4]
C29	CAP_THT	pdb_ii[6C1]
C30	C_1210	pdb_ii[6C2]
C31	C_1210	pdb_ii[6C2]
C32	C_0603	pdb_ii[6C2]
C33	C_1206	pdb_ii[6C4]
C34	C_0805	pdb_ii[10C5]
C35	C_0805	pdb_ii[10B1]
C36	C_1206	pdb_ii[10C6]
C37	C_0805	pdb_ii[6C2]
C38	CAP_TANTAL-B	pdb_ii[10C4]
C39	C_1210	pdb_ii[10D1]
C40	C_0603	pdb_ii[6B4]
C41	C_0603	pdb_ii[6B4]
C42	C_1210	pdb_ii[6B4]
C43	C_0603	pdb_ii[6B2]
C44	C_1206	pdb_ii[6B4]
C45	C_0805	pdb_ii[10C3]
C46	CAP_THT	pdb_ii[6B1]
C47	C_1210	pdb_ii[6B2]
C48	C_1210	pdb_ii[6B2]
C49	C_0603	pdb_ii[6B2]
C50	C_1206	pdb_ii[6B4]
C51	C_0603	pdb_ii[10D2]
C52	C_0805	pdb_ii[6B2]
C53	CAP_TANTAL-B	pdb_ii[7D1]
C54	C_0805	pdb_ii[6A1]
C55	CAP_THT	pdb_ii[6A2]
C55A	CAP_THT	pdb_ii[6A1]
C58	C_0603	pdb_ii[6A2]
C59	C_1210	pdb_ii[6A4]
C60	C_1210	pdb_ii[6A4]
C61	C_0603	pdb_ii[6A5]
C62	C_0603	pdb_ii[6A2]
C63	C_0603	pdb_ii[6A4]
C64	C_1206	pdb_ii[6A5]
C65	C_1206	pdb_ii[6A5]
C66	CAP_THT	pdb_ii[6A7]
C67	C_1210	pdb_ii[6A8]
C68	CAP_THT	pdb_ii[6B7]
C69	CAP_THT	pdb_ii[6B6]
C70	C_1210	pdb_ii[6B7]
C71	C_0805	pdb_ii[9D3]
C72	C_THT	pdb_ii[3B6]
C73	C_1210	pdb_ii[3A6]
C74	C_1210	pdb_ii[3A6]
C75	C_1210	pdb_ii[6D7]
C76	C_0805	pdb_ii[6C6]
C77	C_1812	pdb_ii[6D6]
C78	C_0805	pdb_ii[12B3]
C79	CAP_THT	pdb_ii[6D6]
C80	C_0805	pdb_ii[5D2]
C81	C_0805	pdb_ii[5C2]
C82	C_0805	pdb_ii[4B7]
C83	CAP_THT	pdb_ii[4B5]
C84	CAP_TANTAL-E	pdb_ii[3D2]
C85	C_1210	pdb_ii[3D5]
C86	C_0805	pdb_ii[4D5]
C87	C_1210	pdb_ii[4B5]
C88	CAP_TANTAL-E	pdb_ii[3D3]
C89	CAP_THT	pdb_ii[3D5]
C90	C_1206	pdb_ii[4D4]
C91	CAP_THT	pdb_ii[4A3]
C92	C_1210	pdb_ii[4D4]
C93	C_0805	pdb_ii[3D3]
C94	C_0805	pdb_ii[3D5]
C95	C_0805	pdb_ii[3D6]
C96	CAP_THT	pdb_ii[4A3]
C97	C_0805	pdb_ii[3D5]
C98	C_1210	pdb_ii[4B3]
C99	C_1210	pdb_ii[4B3]
C100	C_THT	pdb_ii[7B4]
C101	C_THT	pdb_ii[3B5]
C102	C_0805	pdb_ii[11C2]
C103	C_0805	pdb_ii[11C5]
C104	C_0805	pdb_ii[11A3]
C105	C_0805	pdb_ii[11B7]

Changes

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CIRCUIT DIAGRAM FOR

PDB_II

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DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 16

OECB_A3
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C106	C_0805	pdb_ii[11B2]
C107	C_0805	pdb_ii[9C7]
C108	C_0805	pdb_ii[11B3]
C109	C_0805	pdb_ii[9B8]
C110	C_0805	pdb_ii[11C7]
C111	C_0805	pdb_ii[9B8]
C112	C_0805	pdb_ii[11B3]
C113	C_0805	pdb_ii[9B8]
C114	C_0805	pdb_ii[9A3]
C115	C_0805	pdb_ii[9B3]
C116	C_0805	pdb_ii[9B8]
C117	C_0805	pdb_ii[9B3]
C118	C_0805	pdb_ii[9C3]
C119	C_0805	pdb_ii[8A7]
C120	C_0805	pdb_ii[8B7]
C121	C_0805	pdb_ii[8A7]
C122	C_0805	pdb_ii[8A4]
C123	C_0805	pdb_ii[8B4]
C124	C_0805	pdb_ii[8B7]
C125	C_0805	pdb_ii[8B4]
C126	C_0805	pdb_ii[8C4]
C127	C_0805	pdb_ii[6B1]
C128	C_0805	pdb_ii[10B1]
C129	C_0805	pdb_ii[10C5]
C130	C_0805	pdb_ii[6C3]
C131	C_0805	pdb_ii[10D1]
C132	C_0805	pdb_ii[10D2]
C133	C_0805	pdb_ii[6B1]
C134	C_0805	pdb_ii[6B3]
C135	C_0805	pdb_ii[7D3]
C136	C_0805	pdb_ii[2C8]
C137	C_0805	pdb_ii[6A4]
C138	C_0805	pdb_ii[6A2]
C138A	C_0805	pdb_ii[6A2]
C138B	C_0805	pdb_ii[6A2]
C138C	C_0805	pdb_ii[6A2]
C138D	C_0805	pdb_ii[6A2]
C138E	C_0805	pdb_ii[6A2]
C139	C_0805	pdb_ii[11D1]
C140	C_0805	pdb_ii[2C8]
C141	C_0805	pdb_ii[6A8]
C142	C_0805	pdb_ii[6A7]
C143	C_0805	pdb_ii[6B6]
C144	C_0805	pdb_ii[6B7]
C145	C_1206	pdb_ii[6B8]
C146	C_1206	pdb_ii[6B8]
C147	C_0805	pdb_ii[9D2]
C148	C_0805	pdb_ii[6D6]
C149	C_0805	pdb_ii[6D6]
C150	C_0805	pdb_ii[6C7]
C151	C_0805	pdb_ii[5C3]
C152	C_0805	pdb_ii[5D3]
C153	C_0805	pdb_ii[4B6]
C154	C_0805	pdb_ii[4D4]
C155	C_0805	pdb_ii[4D5]

C156	C_1206	pdb_ii[4B4]
C157	C_1206	pdb_ii[3D3]
C158	C_0805	pdb_ii[5C1]
C159	C_0805	pdb_ii[5C2]
C160	C_0805	pdb_ii[5C4]
CAP_HSINKS_4_1	THERMO_CAP_MECH_PART_MECH_PART	pdb_ii[13B3]
CAP_HSINKS_6_1	THERMO_CAP_MECH_PART_MECH_PART	pdb_ii[13B3]
CAP_HSINKS_6_2	THERMO_CAP_MECH_PART_MECH_PART	pdb_ii[13B3]
CLIP_HSINKS_4_2	CLIP_MECH_PART_MECH_PART	pdb_ii[13B5]
CLIP_HSINKS_6_3	CLIP_MECH_PART_MECH_PART	pdb_ii[13B5]
CLIP_HSINKS_6_4	CLIP_MECH_PART_MECH_PART	pdb_ii[13B5]
CS1	LTS25NP_THT	pdb_ii[10C2]
D1	50WQ06FN_DPAK	pdb_ii[11C6]
D2	10BQ060_SMD	pdb_ii[6C1]
D3	10BQ060_SMD	pdb_ii[6D3]
D4	STTA106U_SMD	pdb_ii[6C1]
D5	STTA106U_SMD	pdb_ii[6B1]
D6	BYT08P_400_THT	pdb_ii[6A1]
D6A	50WQ06FN_DPAK	pdb_ii[6A2]
D7	BYT08P_400_THT	pdb_ii[6A7]
D8	BYT08P_400_THT	pdb_ii[6B6]
D9	RGP02_20E_THT	pdb_ii[3B6]
D10	ERC06_15_THT	pdb_ii[3C7]
D11	STTH6002C_THT	pdb_ii[4C7]
D12	STTA106U_SMD	pdb_ii[3C2]
D13	10MQ040N_SMD	pdb_ii[3D6]
D14	TMMBAT42_MELF	pdb_ii[4C3]
D15	TMMBAT42_MELF	pdb_ii[3D3]
D16	10MQ040N_SMD	pdb_ii[4C6]
D17	STPS41H100_D2PAK	pdb_ii[4A2]
D18	SKA1_17_KOMBI	pdb_ii[3B2]
D19	SKA1_17_KOMBI	pdb_ii[3B2]
D20	SKA1_17_KOMBI	pdb_ii[3B2]
D21	SKA1_17_KOMBI	pdb_ii[3B2]
D22	SKA1_17_KOMBI	pdb_ii[3B2]
D23	SKA1_17_KOMBI	pdb_ii[3B2]
D24	SKA1_17_KOMBI	pdb_ii[3A3]
D25	RGP02_20E_THT_PDBII	pdb_ii[3A2]
D26	RGP02_20E_THT_PDBII	pdb_ii[3A3]
D27	TMMBAT42_MELF	pdb_ii[9C6]
D28	TMMBAT42_MELF	pdb_ii[9C6]
D29	BAV103_SMD	pdb_ii[7D2]
D30	BAV103_SMD	pdb_ii[9D2]
D31	STTA106U	pdb_ii[3A7]
D32	D1F60_SMD	pdb_ii[12B4]
D33	TMMBAT42_MELF	pdb_ii[4B7]
D34	BAV103_SMD	pdb_ii[4B6]
D35	TMMBAT42_MELF	pdb_ii[3C5]
D36	TMMBAT42_MELF	pdb_ii[3C5]

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APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 17

OECB_A3
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D37	D1F60_SMD	pdb_ii[7B4]
D38	D1F60_SMD	pdb_ii[7B3]
D39	TMMBAT42_MELF	pdb_ii[5B4]
D40	D1F60_SMD	pdb_ii[7B4]
D41	D1F60_SMD	pdb_ii[7B3]
F2	FUSE_ELU	pdb_ii[3A3]
F3	FUSE_ELU	pdb_ii[3B2]
FUSE_F2	FUSE_MECH_PART_MECH_PART	pdb_ii[13B1]
FUSE_F3	FUSE_MECH_PART_MECH_PART	pdb_ii[13B1]
FUSE_HOLDER_1_F2	FUSE_CLIP_HOLDER_MECH_PART_MECH_PART	pdb_ii[13B1]
FUSE_HOLDER_1_F3	FUSE_CLIP_HOLDER_MECH_PART_MECH_PART	pdb_ii[13B1]
HS2	HEATSINK_CT3034	pdb_ii[6A7]
HS3	HEATSINK_CT3034	pdb_ii[6A6]
HS4	HEATSINK_CT45335	pdb_ii[4C7]
HS5	HEATSINK_CT3034	pdb_ii[6A1]
HS6	HEATSINK_CT3338	pdb_ii[3B7]
INSULATOR	INSULATOR_MECH_PART_MECH_PART	pdb_ii[13B7]
JP1	JUMPER_0805	pdb_ii[2B2]
JP2	JUMPER_0805	pdb_ii[2B2]
JP3	JUMPER_0805	pdb_ii[2A2]
JP4	JUMPER_0805	pdb_ii[2D6]
JP5	JUMPER_0805	pdb_ii[2A2]
JP6	JUMPER_0805	pdb_ii[2D6]
JP7	JUMPER_0805	pdb_ii[6D8]
JP8	JUMPER_0805	pdb_ii[2D2]
JP9	JUMPER_0805	pdb_ii[6C1]
JP10	JUMPER_0805	pdb_ii[7D5]
JP11	JUMPER_0805	pdb_ii[9D4]
L1	L_SMD	pdb_ii[11B7]
L2	L_SMD	pdb_ii[6D4]
L3	L_WE_PD2S	pdb_ii[6D5]
L4	L_WE_TPC	pdb_ii[6C3]
L5	L_WE_TPC	pdb_ii[6B3]
L6	L_WE_TPC	pdb_ii[6A4]
LED1	LED_RED_SSMD	pdb_ii[6A5]
P1	CON6_WAGO721_836	pdb_ii[3A1]
P2	CON2_WAGO721_262	pdb_ii[7B8]
P3	CON3_WAGO721_163	pdb_ii[4B1]
P5	CON3_WAGO721_263	pdb_ii[7B1]
P6	CON2_WAGO722_132	pdb_ii[9D1]
P7	CON50_PCB_HEADER-LON G-PIN	pdb_ii[2B1]
P8	CON5_WAGO722_735	pdb_ii[7A6]
P9	CON50_PCB_HEADER-LON G-PIN	pdb_ii[2B5]
P10	CON50_PCB_HEADER-LON G-PIN	pdb_ii[2B4]
P11	CON2_WAGO733_332	pdb_ii[11C8]
P12	CON2_WAGO733_332	pdb_ii[11C8]
P13	CON3_WAGO722_133	pdb_ii[12B5]
P14	CON4_WAGO722_134	pdb_ii[10C1]
P16	CON2_WAGO733_332	pdb_ii[11C8]
P17	CON2_WAGO722_732	pdb_ii[7C8]
P100	CON2_WAGO_VP5MM	pdb_ii[7A1]
P102	CON4_WAGO722_734	pdb_ii[7C1]
PE_1	CON1_DK3_4_CU7	pdb_ii[6A5]
PE_2	CON1_DK3_4_CU7	pdb_ii[3B8]
PE_3	CON1_DK3_4_CU7	pdb_ii[6B5]
Q1	BC817_SMD	pdb_ii[11A6]
Q2	BC817_SMD	pdb_ii[11C5]
Q3	BC817_SMD	pdb_ii[11C3]
Q4	BC807_SMD	pdb_ii[11B3]
Q5	BC817_SMD	pdb_ii[2D7]
Q6	SPB70N10L_D2PAK	pdb_ii[6C6]
Q7	2SK3746_THT	pdb_ii[3D7]
Q8	STW75NF20_THT	pdb_ii[4C7]
Q9	BC817_SMD	pdb_ii[12C3]
Q10	BSS138N_SMD	pdb_ii[4C3]
Q11	BSP51_SMD	pdb_ii[4B5]
Q12	BSS138N_SMD	pdb_ii[4D3]
Q13	BSS138N_SMD	pdb_ii[4D4]
Q14	BC807_SMD	pdb_ii[4D4]
Q15	BSS138N_SMD	pdb_ii[3C6]
Q16	BSS138N_SMD	pdb_ii[5C5]
Q17	BSS138N_SMD	pdb_ii[5B4]
Q18	BSS138N_SMD	pdb_ii[4B2]
Q19	BSS138N_SMD	pdb_ii[4B2]
Q20	STW9N150_THT	pdb_ii[3B5]
Q21	STW75NF20_THT	pdb_ii[4B3]
Q22	STW9N150_THT	pdb_ii[3B4]
Q23	2SK2399_DPAK	pdb_ii[12C4]
R1	R_0603	pdb_ii[11C3]
R2	R_0805	pdb_ii[11C3]
R3	R_0805	pdb_ii[11B3]
R4	R_MINI-MELF	pdb_ii[11A1]
R5	R_MINI-MELF	pdb_ii[11A1]
R6	R_0805	pdb_ii[9C5]
R7	R_0805	pdb_ii[9B5]
R8	R_0805	pdb_ii[9C4]
R9	R_0805	pdb_ii[9B5]
R10	R_0805	pdb_ii[9C4]
R11	R_1206	pdb_ii[9B2]
R12	R_1206	pdb_ii[9A2]
R13	R_0805	pdb_ii[6D2]
R14	R_0805	pdb_ii[9B2]
R15	R_0805	pdb_ii[9A2]
R16	R_0805	pdb_ii[6D2]
R17	R_0603	pdb_ii[6D4]
R18	R_0805	pdb_ii[9B3]
R19	R_0805	pdb_ii[9A3]
R20	R_0805	pdb_ii[9B3]
R21	R_0805	pdb_ii[9A3]
R22	R_0805	pdb_ii[8C6]
R23	R_0805	pdb_ii[8B6]
R24	R_0805	pdb_ii[6D4]

Changes

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APPD	M.	Mann	SHEETS
AUTH		CA47A-000039	SHEET 18

OECB_A3
REV 2004-09-06

A3	1	2	3	4	5	6	7	8

R25	R_0805	pdb_ii[6D2]
R26	R_MELF	pdb_ii[6D5]
R27	R_0805	pdb_ii[8C4]
R28	R_0805	pdb_ii[6D4]
R29	R_0805	pdb_ii[6D4]
R30	R_0805	pdb_ii[8B6]
R31	R_0805	pdb_ii[8C4]
R32	R_1206	pdb_ii[8C3]
R33	R_1206	pdb_ii[8B3]
R34	R_0805	pdb_ii[6C2]
R35	R_0805	pdb_ii[6C4]
R36	R_0805	pdb_ii[8C3]
R37	R_0805	pdb_ii[8B3]
R38	R_0805	pdb_ii[8C4]
R39	R_0805	pdb_ii[8B4]
R40	R_0805	pdb_ii[6C4]
R41	R_0805	pdb_ii[8C4]
R42	R_0805	pdb_ii[8B4]
R43	R_0805	pdb_ii[10D1]
R44	R_MELF	pdb_ii[6C5]
R45	R_0603	pdb_ii[6C3]
R46	R_0805	pdb_ii[10C4]
R47	R_0805	pdb_ii[6B2]
R48	R_0805	pdb_ii[6B4]
R49	R_0805	pdb_ii[10B6]
R50	R_MINI-MELF	pdb_ii[10C6]
R51	R_0805	pdb_ii[10B6]
R52	R_0805	pdb_ii[6B4]
R53	R_0805	pdb_ii[10C3]
R54	R_MINI-MELF	pdb_ii[7D1]
R55	R_MELF	pdb_ii[6B5]
R56	R_0603	pdb_ii[6B3]
R57	R_0603	pdb_ii[6A4]
R58	RP4_CAY0603	pdb_ii[2D7]
R59	RP4_CAY0603	pdb_ii[2D2]
R60	R_0805	pdb_ii[6A3]
R61	R_MELF	pdb_ii[6A1]
R62	R_MELF	pdb_ii[6A5]
R63	R_0805	pdb_ii[6A4]
R64	R_0805	pdb_ii[6A4]
R65	R_2817	pdb_ii[6C6]
R66	R_THT	pdb_ii[3B5]
R67	R_THT	pdb_ii[3B5]
R68	R_MELF	pdb_ii[6C7]
R69	R_0805	pdb_ii[12B4]
R70	R_0805	pdb_ii[12C4]
R71	R_0805	pdb_ii[4D6]
R72	R_2817	pdb_ii[4D6]
R73	R_2817	pdb_ii[4D7]
R74	R_0805	pdb_ii[5D2]
R75	R_0805	pdb_ii[4B7]
R76	R_0805	pdb_ii[4B7]
R77	R_0805	pdb_ii[4D3]
R78	R_MINI-MELF	pdb_ii[4C6]
R79	R_0805	pdb_ii[4D6]

R80	R_MINI-MELF	pdb_ii[4C6]
R81	R_0805	pdb_ii[3D3]
R82	R_0805	pdb_ii[3D4]
R83	R_0805	pdb_ii[5C3]
R84	R_0805	pdb_ii[5B4]
R85	R_THT	pdb_ii[5A1]
R86	R_THT	pdb_ii[5A1]
R87	R_THT	pdb_ii[5B1]
R88	R_THT	pdb_ii[5B1]
R89	R_THT	pdb_ii[5B1]
R90	R_THT	pdb_ii[5B1]
R91	R_0805	pdb_ii[5B3]
R92	R_THT	pdb_ii[3B4]
R93	R_THT	pdb_ii[3B5]
R94	R_THT	pdb_ii[7B2]
R95	R_THT	pdb_ii[3B4]
R96	R_THT	pdb_ii[3B4]
R97	R_THT	pdb_ii[3A2]
R98	R_THT	pdb_ii[3A2]
R99	R_THT	pdb_ii[7B2]
R100	R_THT	pdb_ii[7B1]
R101	R_0805	pdb_ii[11C1]
R102	R_1206	pdb_ii[11B3]
R103	R_0805	pdb_ii[11C4]
R104	R_0805	pdb_ii[11C5]
R105	R_0805	pdb_ii[11C6]
R106	R_0805	pdb_ii[11A6]
R107	R_0805	pdb_ii[11C3]
R108	R_1206	pdb_ii[11C3]
R109	R_0805	pdb_ii[11A8]
R110	R_0805	pdb_ii[11A7]
R111	R_1206	pdb_ii[9C6]
R112	R_0805	pdb_ii[11B2]
R113	R_0805	pdb_ii[11C4]
R114	R_0805	pdb_ii[11A8]
R115	R_MELF	pdb_ii[11B7]
R116	R_0805	pdb_ii[9C7]
R117	R_1206	pdb_ii[9C6]
R118	R_0805	pdb_ii[11B4]
R119	R_1206	pdb_ii[9D6]
R120	R_0805	pdb_ii[9C7]
R121	R_0805	pdb_ii[9B4]
R122	R_0805	pdb_ii[9C5]
R123	R_0805	pdb_ii[2A2]
R124	R_0805	pdb_ii[11B3]
R125	R_0805	pdb_ii[9B4]
R126	R_1206	pdb_ii[9C2]
R127	R_0805	pdb_ii[2B8]
R128	R_0805	pdb_ii[2C2]
R129	R_1206	pdb_ii[9B1]
R130	R_0805	pdb_ii[9B2]
R131	R_0805	pdb_ii[9A2]
R132	R_0805	pdb_ii[2D6]
R133	R_0805	pdb_ii[2B8]
R134	R_0805	pdb_ii[2B7]

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REV 2004-09-06

A3	1	2	3	4	5	6	7	8

R135	R_0805	pdb_ii[2D6]
R136	R_0805	pdb_ii[8B4]
R137	R_0805	pdb_ii[8C6]
R138	R_0805	pdb_ii[8B4]
R139	R_1206	pdb_ii[8C2]
R140	R_1206	pdb_ii[8B2]
R141	R_0805	pdb_ii[8B3]
R142	R_0805	pdb_ii[8A3]
R143	R_MELF	pdb_ii[6B1]
R144	R_0805	pdb_ii[10C5]
R145	R_0805	pdb_ii[10C4]
R146	R_0805	pdb_ii[10C3]
R147	R_0805	pdb_ii[10B4]
R148	R_0805	pdb_ii[10B4]
R149	R_0805	pdb_ii[10B3]
R150	R_1206	pdb_ii[10B6]
R151	R_0805	pdb_ii[10C3]
R152	R_MELF	pdb_ii[7D2]
R153	R_MELF	pdb_ii[6B1]
R154	R_0805	pdb_ii[7D2]
R155	R_0805	pdb_ii[7D3]
R156	R_0805	pdb_ii[10B7]
R157	R_0805	pdb_ii[7D3]
R158	R_1206	pdb_ii[6A5]
R159	R_0805	pdb_ii[2D7]
R160	R_0805	pdb_ii[2D7]
R161	R_0805	pdb_ii[11C1]
R161A	R_MELF	pdb_ii[6A1]
R161B	R_MELF	pdb_ii[6A1]
R161C	R_MELF	pdb_ii[6A1]
R161D	R_MELF	pdb_ii[6A1]
R161E	R_MELF	pdb_ii[6A1]
R162	R_MELF	pdb_ii[6A7]
R163	R_MELF	pdb_ii[6B8]
R164	R_MELF	pdb_ii[6B7]
R165	R_MELF	pdb_ii[6B7]
R166	R_MELF	pdb_ii[6B8]
R167	R_MELF	pdb_ii[6B8]
R168	R_MELF	pdb_ii[6B6]
R169	R_MELF	pdb_ii[6B7]
R170	R_MELF	pdb_ii[6B7]
R171	R_MELF	pdb_ii[9D2]
R172	R_1206	pdb_ii[9D3]
R173	R_MELF	pdb_ii[9D2]
R174	R_MELF	pdb_ii[3A6]
R175	R_MELF	pdb_ii[3A6]
R176	R_MINI-MELF	pdb_ii[6C6]
R177	R_MINI-MELF	pdb_ii[6C6]
R178	R_0805	pdb_ii[6D7]
R179	R_0805	pdb_ii[6D7]
R180	R_0805	pdb_ii[6C6]
R181	R_MINI-MELF	pdb_ii[6C6]
R182	R_0805	pdb_ii[6C7]
R183	R_0805	pdb_ii[6D6]
R184	R_0805	pdb_ii[6C8]

R185	R_0805	pdb_ii[6C7]
R186	R_1206	pdb_ii[7C3]
R187	R_0805	pdb_ii[3D6]
R188	R_1206	pdb_ii[7C3]
R189	R_0805	pdb_ii[6C7]
R190	R_1206	pdb_ii[7C3]
R191	R_MELF	pdb_ii[5C3]
R192	R_0805	pdb_ii[5D2]
R193	R_0805	pdb_ii[5C3]
R194	R_MINI-MELF	pdb_ii[5D2]
R195	R_MINI-MELF	pdb_ii[12B3]
R196	R_0805	pdb_ii[4B7]
R197	R_0805	pdb_ii[5D2]
R198	R_0805	pdb_ii[4B6]
R199	R_0805	pdb_ii[12C3]
R200	R_0805	pdb_ii[12C3]
R201	R_MINI-MELF	pdb_ii[4B6]
R202	R_MINI-MELF	pdb_ii[3D7]
R203	R_MINI-MELF	pdb_ii[3D7]
R204	R_0805	pdb_ii[3D6]
R205	R_MINI-MELF	pdb_ii[3D7]
R206	R_MELF	pdb_ii[4A5]
R207	R_MELF	pdb_ii[4A5]
R208	R_MINI-MELF	pdb_ii[3D6]
R209	R_MINI-MELF	pdb_ii[3D8]
R210	R_0805	pdb_ii[4B5]
R211	R_0805	pdb_ii[4C2]
R212	R_0805	pdb_ii[4C3]
R213	R_0805	pdb_ii[4D3]
R214	R_MINI-MELF	pdb_ii[3D6]
R215	R_MELF	pdb_ii[4A5]
R216	R_MELF	pdb_ii[3C2]
R217	R_0805	pdb_ii[4B4]
R218	R_0805	pdb_ii[4C4]
R219	R_0805	pdb_ii[4B4]
R220	R_MELF	pdb_ii[3C2]
R221	R_0805	pdb_ii[4C4]
R222	R_MINI-MELF	pdb_ii[3C5]
R223	R_MELF	pdb_ii[4C4]
R224	R_0805	pdb_ii[3D3]
R225	R_MELF	pdb_ii[4A2]
R226	R_MELF	pdb_ii[4D4]
R227	R_MELF	pdb_ii[4A3]
R228	R_0805	pdb_ii[5B3]
R229	R_0805	pdb_ii[5C3]
R230	R_0805	pdb_ii[5B3]
R231	R_0805	pdb_ii[5C1]
R232	R_0805	pdb_ii[4B3]
R233	R_0805	pdb_ii[5B3]
R234	R_0805	pdb_ii[5B1]
R235	R_0805	pdb_ii[3D5]
R236	R_MELF	pdb_ii[7B3]
R237	R_0805	pdb_ii[5C2]
R238	R_0805	pdb_ii[5B2]
R239	R_0805	pdb_ii[5B5]

Changes

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

Otis
A United Technologies Company

CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 20

OECB_A3
REV 2004-09-06

R240	R_MELF	pdb_ii[4B2]	
R241	R_MELF	pdb_ii[4B2]	
R242	R_0805	pdb_ii[5B2]	
R243	R_0805	pdb_ii[5B4]	
R244	R_MELF	pdb_ii[4B1]	
R245	R_MELF	pdb_ii[4A2]	
R246	R_MELF	pdb_ii[7B3]	
R247	R_MINI-MELF	pdb_ii[5B4]	
R248	R_MELF	pdb_ii[7B3]	
R249	R_MELF	pdb_ii[7B3]	
R250	R_MINI-MELF	pdb_ii[4B1]	
R251	R_MINI-MELF	pdb_ii[3B4]	
R252	R_MINI-MELF	pdb_ii[7B2]	
R253	R_MINI-MELF	pdb_ii[7B2]	
R254	R_MINI-MELF	pdb_ii[7B2]	
REL1	RELAY_SR_DOLD_2M2B	pdb_ii[7C7	7C3 7B5 7A2 7B7]
REL2	RELAY_SR_DOLD_2M2B	pdb_ii[7A2	7B7 7B4 7C2 7B7]
RV1	SIOV_S20K50_THT	pdb_ii[4B1]	
RV2	SIOV_S20K680_THT	pdb_ii[3B3]	
SPACER_MP1	SPACER_MECH_PART_MECH_PART	pdb_ii[13A7]	
SPACER_MP2	SPACER_MECH_PART_MECH_PART	pdb_ii[13A7]	
SPACER_MP3	SPACER_MECH_PART_MECH_PART	pdb_ii[13B7]	
TR1	5451313600	pdb_ii[3B7]	
TR1_CORE_INSULATOR	INSULATOR_MECH_PART_MECH_PART	pdb_ii[13C1]	
U1	6N139_SOIC	pdb_ii[11C2]	
U2	TL082_SOIC	pdb_ii[11B4	11A8]
U3	LMC7101_SMD	pdb_ii[11C4]	
U4	TL084B_SOIC	pdb_ii[9A8	9A4 9B5 9C4]
U5	TL084B_SOIC	pdb_ii[9B2	9A2 9B3 9A3]
U6	LM5575_TSSOP	pdb_ii[6D3]	
U7	TL084B_SOIC	pdb_ii[8A8	8C4 8B4 8B6]
U8	TL084B_SOIC	pdb_ii[8B4	8C3 8B3 8C4]
U9	LM3103_TSSOP	pdb_ii[6C3]	
U10	TL082_SOIC	pdb_ii[10B3	10B4]
U11	555_CMOS	pdb_ii[10B5]	
U12	LM3103_TSSOP	pdb_ii[6B3]	
U13	PS2561L_SOIC	pdb_ii[10B7]	
U14	PS2561L_SOIC	pdb_ii[7D3]	
U15	24C64_SOIC	pdb_ii[2D8]	
U16	LM3102_TSSOP	pdb_ii[6A3]	
U17	74HC14_SOIC	pdb_ii[6C8]	
U17	74HC14_SOIC	pdb_ii[7D4	7D4]
U17	74HC14_SOIC	pdb_ii[9D4]	
U17	74HC14_SOIC	pdb_ii[11C2]	
U17	74HC14_SOIC	pdb_ii[12C2]	
U18	74HC14_SOIC	pdb_ii[2D4	2D4 2D2 2D4 2D4]
U18	74HC14_SOIC	pdb_ii[10B7]	
U19	PS2561L_SOIC	pdb_ii[9D3]	
U20	PS2561L_SOIC	pdb_ii[6C7]	
U21	LT1641_2CS8_SOIC	pdb_ii[6D6]	
U22	PS2561L_SOIC	pdb_ii[5D4]	
U23	TL431A_SOIC	pdb_ii[5D3]	
U24	PS2561L_SOIC	pdb_ii[12C4]	
U25	PS2561L_SOIC	pdb_ii[5D4]	
U26	PS2561L_SOIC	pdb_ii[4B7]	
U27	TL431A_SOIC	pdb_ii[4B4]	
U28	UC3845B_SOIC	pdb_ii[4C5]	
U29	UC3845B_SOIC	pdb_ii[3D4]	
U30	PS2561L_SOIC	pdb_ii[5B4]	
U31	LM393_SOIC	pdb_ii[5C3	5B3]
VR1	LM2595_5_SMD	pdb_ii[11B6]	
VR2	LM1117_5V_SMD	pdb_ii[10D1]	
Z1	BZX84C6V8_SMD	pdb_ii[11A7]	
Z2	BZX84C6V8_SMD	pdb_ii[11B1]	
Z3	BZV55B12	pdb_ii[12C4]	
Z4	BZX84B6V8_SMD	pdb_ii[4C3]	
Z5	1SMA5930BT3_SMD	pdb_ii[4D6]	
Z6	LM285_2V5_SOIC	pdb_ii[5C2]	
Z7	BZV55B12_SMD	pdb_ii[3B4]	
Z8	BZV55B12_SMD	pdb_ii[3B4]	
Z9	BZV55C3V3_SMD	pdb_ii[10D1]	
Z10	SM15T36CA_SMD	pdb_ii[7C7]	
Z11	SM15T150CA_SMD	pdb_ii[6A7]	
Z12	SM15T150CA_SMD	pdb_ii[6B6]	
Z13	SM15T6V8CA_SMD	pdb_ii[6B6]	
Z14	SM15T27CA_SMD	pdb_ii[3A7]	
Z15	SM15T27CA_SMD	pdb_ii[3A7]	
Z16	1SMA5930BT3_SMD	pdb_ii[3D6]	
Z17	SM15T36CA_SMD	pdb_ii[7C7]	
Z18	BZV55C3V3_SMD	pdb_ii[4D6]	
Z19	BZV55C3V3_SMD	pdb_ii[3D6]	
Z20	1SMA5930BT3_SMD	pdb_ii[4B3]	
Z21	1SMA5930BT3_SMD	pdb_ii[3B5]	
Z22	1SMA5930BT3_SMD	pdb_ii[4A1]	
Z23	BZV55C9V1_SMD	pdb_ii[4B2]	
Z24	BZV55C9V1_SMD	pdb_ii[4B2]	

Changes

DEVICE TYPE FOR DESIGN REFERENCE ONLY; REFER TO BOM FOR MANUFACTURING

Otis
A United Technologies Company

CIRCUIT DIAGRAM FOR

PDB_II

CAD GENERATED
CADENCE-CONCEPT

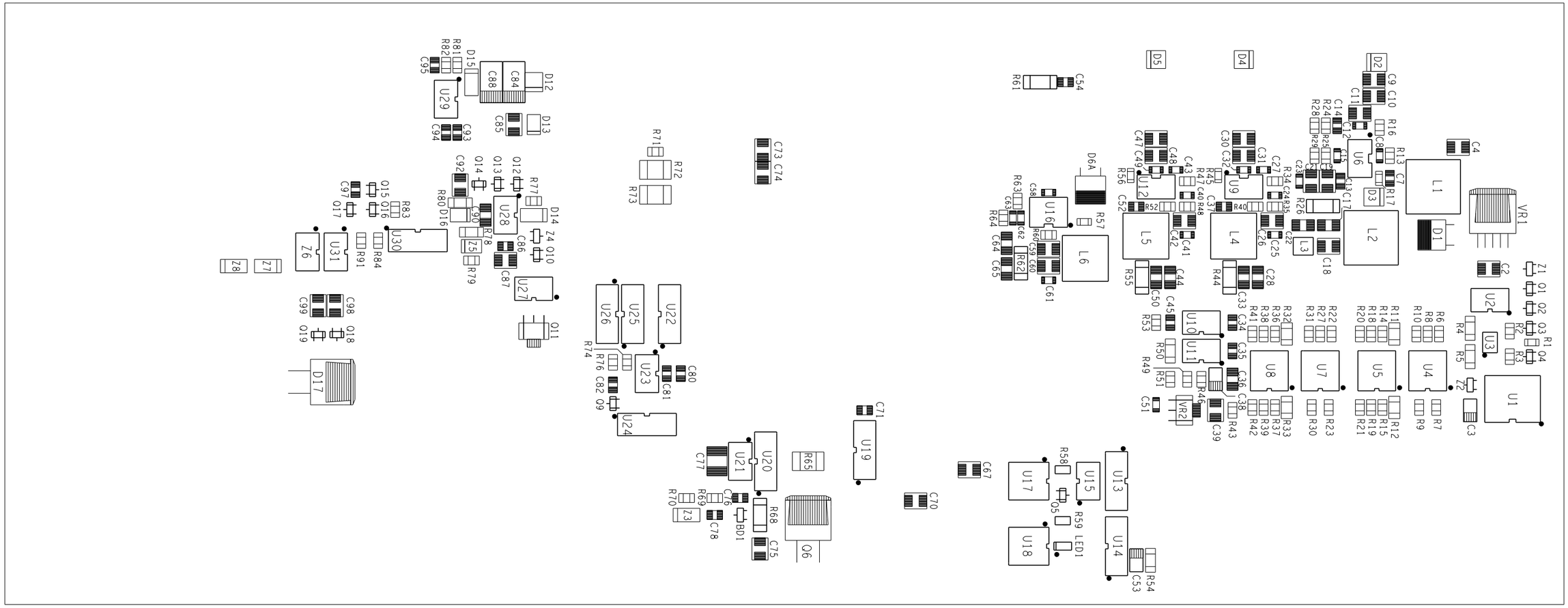
DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	A. Tutat	ORIGINAL DATE
CHK	M. Dehmlow	2003-10-29
APPD	M. Mann	SHEETS
AUTH	CA47A-000039	SHEET 21

OECB_A3
REV 2004-09-06

TOP VIEW
Sicht auf
Bestückungsseite



ALL DIMENSIONS METRIC

CHANGES	
2014-09-18 G(D)A changed, assembly_sec1 added	A. Friedrich

PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

SMD-Lageplan

Part-No. / Teil-Nr. GDA26800KP1

Scale / Massstab 1.5:1

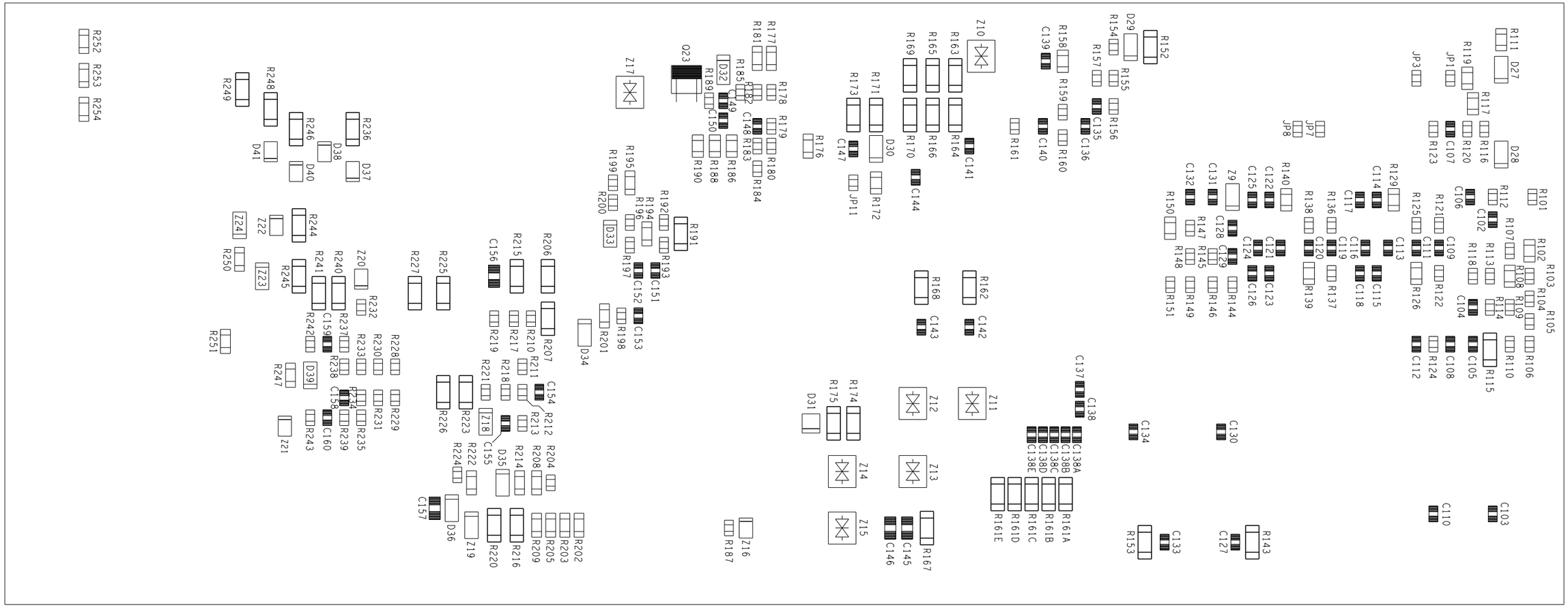
DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	S. KOEPPEN	ORIGINAL DATE 2003-10-29
CHK	A. TUTAT	
APPD	M. DEHMLOW	SHEETS
AUTH	CA47A-000039	SHEET 22

OECB_A2
REV 1998-11-13

BOTTOM VIEW
Sicht auf
Bestückungsseite



ALL DIMENSIONS METRIC

CHANGES	
2014-09-18 G(D)A changed, assembly_sec1 added	A. Friedrich

PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

SMD-Lageplan

Part-No.
Teil-Nr. GDA26800KP1

Scale
Massstab 1.5:1

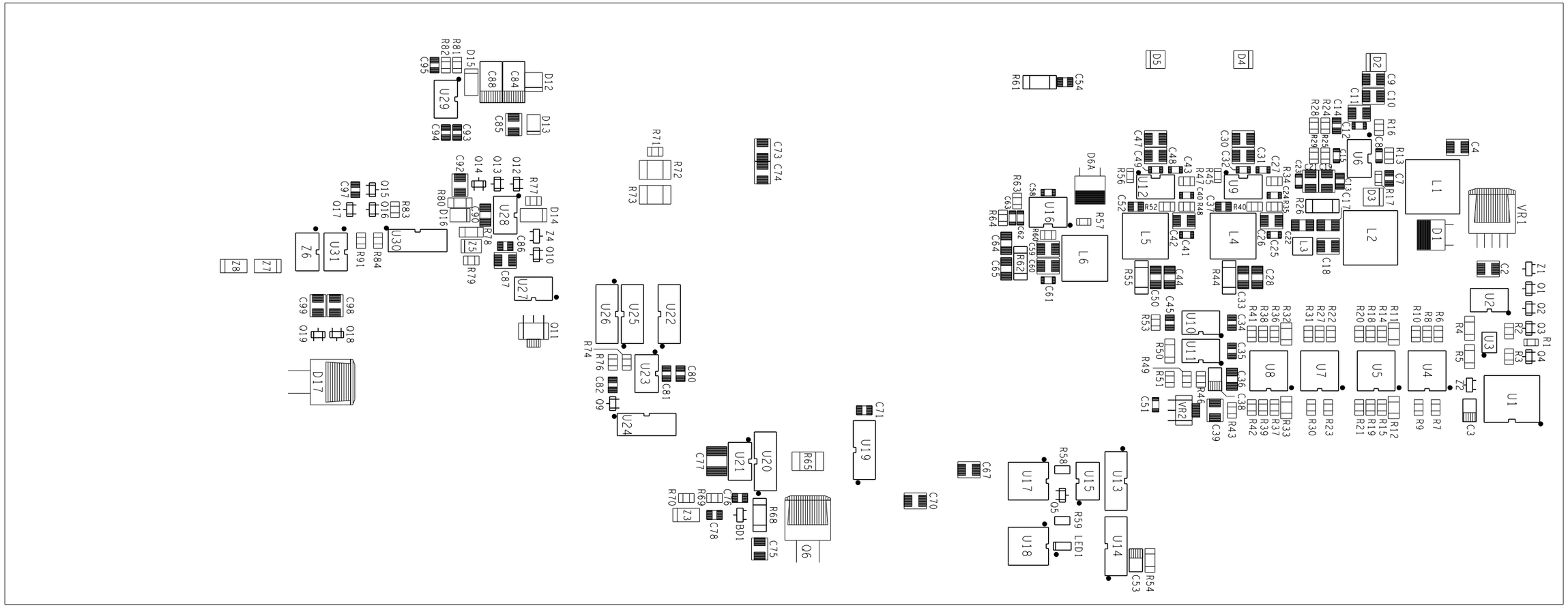
DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	S. KOEPPEN	ORIGINAL DATE 2003-10-29
CHK	A. TUTAT	
APPD	M. DEHMLOW	SHEETS
AUTH	CA47A-000039	SHEET 23

OECB_A2
REV 1998-11-13

TOP VIEW
Sicht auf
Bestückungsseite



ALL DIMENSIONS METRIC

CHANGES	
2014-09-18 G(D)A changed, assembly_sec1 added	A. Friedrich

PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

SMD-Lageplan

Part-No. / Teil-Nr. GDA26800KP2

Scale / Massstab 1.5:1

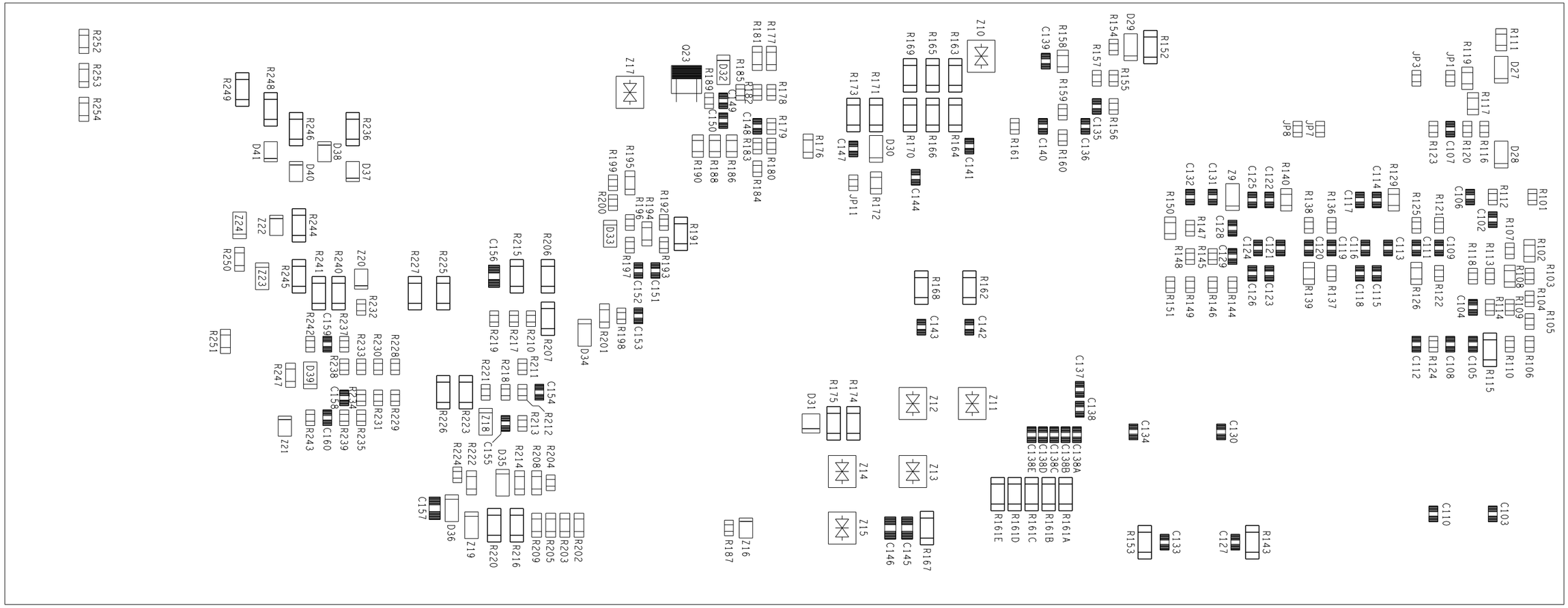
DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	S. KOEPPEN	ORIGINAL DATE 2003-10-29
CHK	A. TUTAT	
APPD	M. DEHMLOW	SHEETS
AUTH	CA47A-000039	SHEET 24

OECB_A2
REV 1998-11-13

BOTTOM VIEW
Sicht auf
Bestückungsseite



ALL DIMENSIONS METRIC

SMD-Lageplan	
Part-No. / Teil-Nr.	GDA26800KP2
Scale / Massstab	1.5:1
DWG	GDA26800KP
	OTIS ENGINEERING CENTER BERLIN, GERMANY
DRAWN S. KOEPPEN	ORIGINAL DATE 2003-10-29
CHK A. TUTAT	
APPD M. DEHMLOW	SHEETS
AUTH CA47A-000039	SHEET 25

CHANGES

2014-09-18 G(D)A changed, assembly_sec1 added — A. Friedrich

OTIS
A United Technologies Company

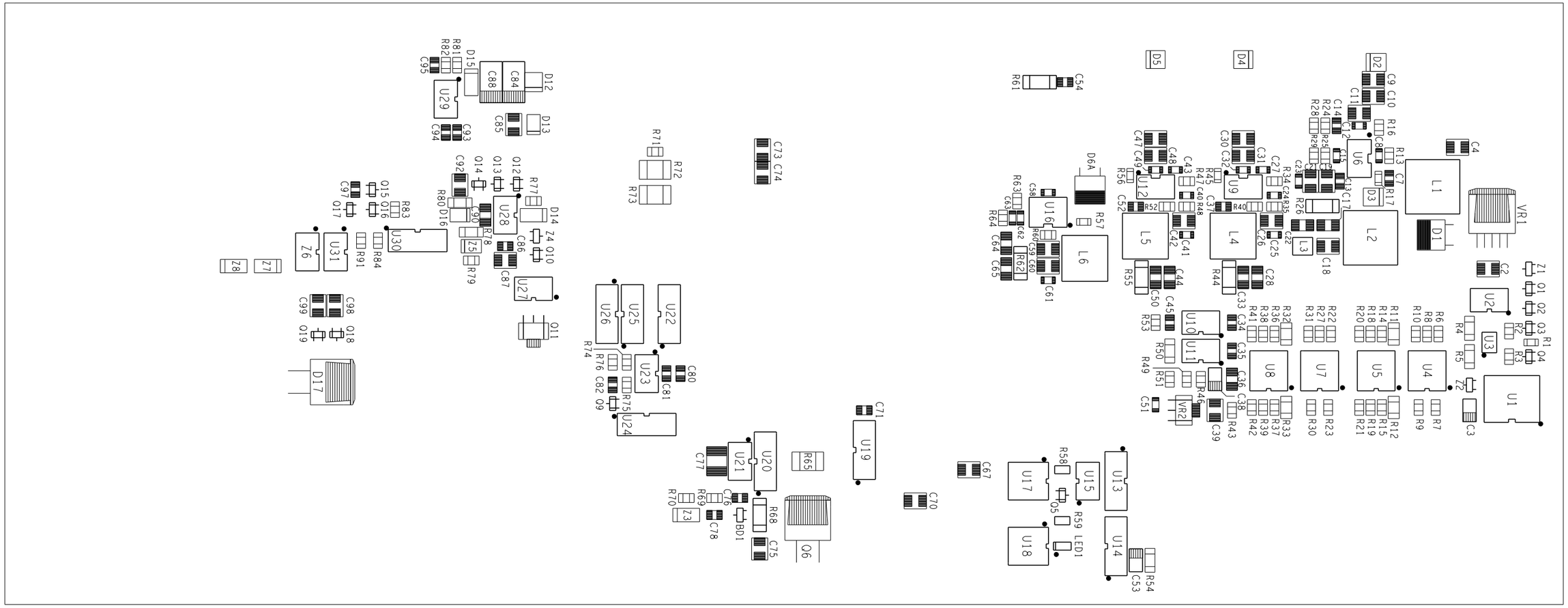
PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

OECB_A2
REV 1998-11-13

TOP VIEW
Sicht auf
Bestückungsseite



ALL DIMENSIONS METRIC

CHANGES		
2014-08-08 G(D)A changed	CN576228	I.ATALMIS

PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

SMD-Lageplan

Part-No. / Teil-Nr. GDA26800KP3/4/5

Scale / Massstab 1.5:1

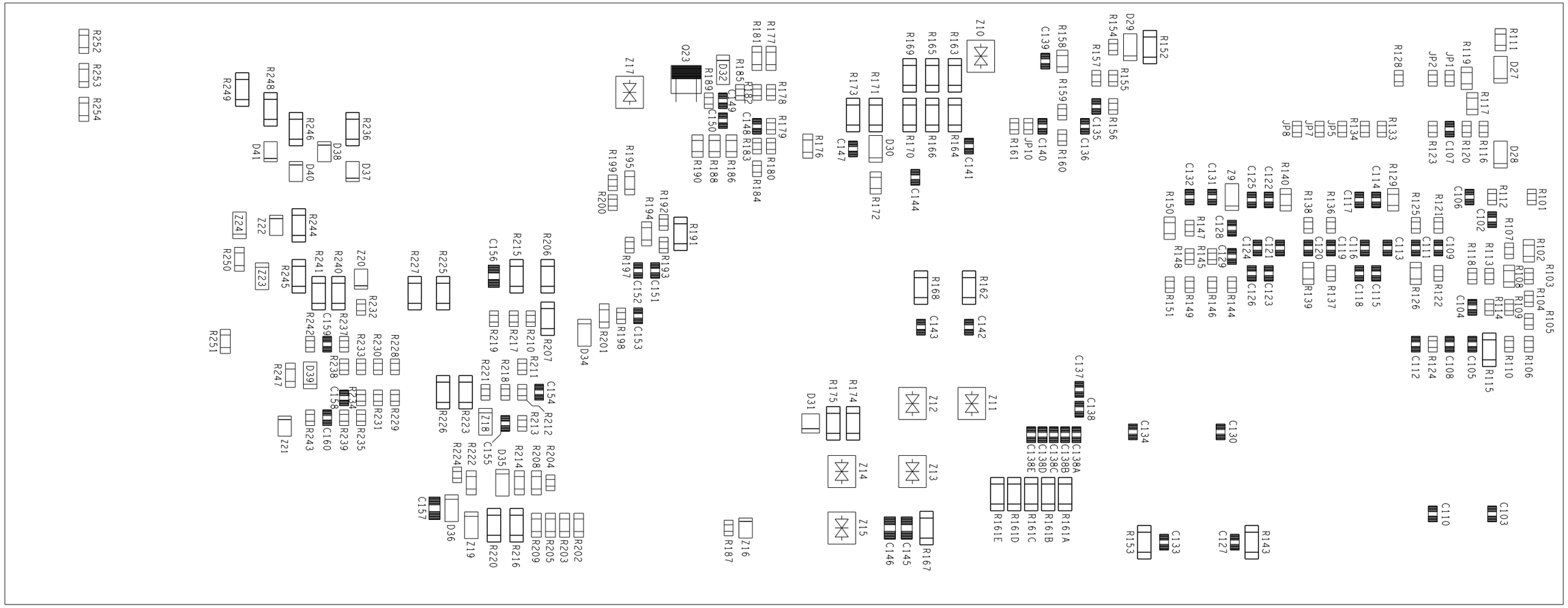
DWG GDA26800KP

OTIS ENGINEERING CENTER
BERLIN, GERMANY

DRAWN	S. KOEPPEN	ORIGINAL DATE 2003-10-29
CHK	A. TUTAT	
APPD	M. DEHMLOW	SHEETS
AUTH	CA47A-000039	SHEET 26

OECB_A2
REV 1998-11-13

BOTTOM VIEW
Sicht auf
Loetseite



ALL DIMENSIONS METRIC

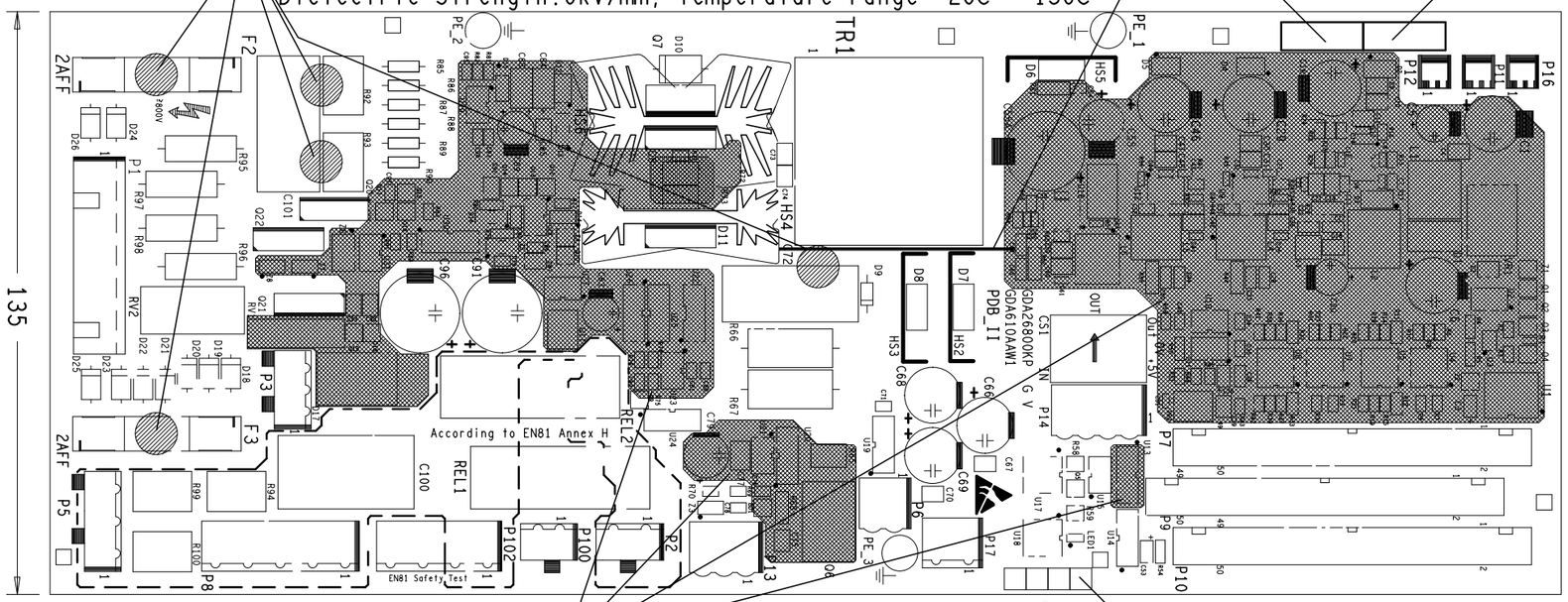
CHANGES	WARNING	PCB LAYOUT	SMD-Lageplan
2014-08-08 G(D)A changed CN576228 I.ATALMIS	This work and the information it contains are the property of OTIS Elevator Company (OTIS). It is delivered to others on the express condition that it will be used only for, or on behalf of, OTIS; that neither it nor the information it contains will be reproduced or disclosed, in whole or in part, without the prior written consent of OTIS; and that on demand it and any copies will be promptly returned to OTIS. Unpublished work (C) OTIS Elevator Company 1998 All rights reserved. OTIS A United Technologies Company	PDB_II CADENCE ALLEGRO CAD GENERATED	Part-No. Teil-Nr. GDA26800KP3/4/5 Scale Massstab 1.5:1 DWG GDA26800KP OTIS ENGINEERING CENTER BERLIN, GERMANY DRAWN S. KOEPPEN — ORIGINAL DATE 2003-10-29 CHK A. TUTAT APPD M. DEHMLOW — SHEETS AUTH CA47A-000039 — SHEET 27

OECB_A2
REV 1998-11-13

A2	8	7	6	5	4	3	2	1

TOP

Klebepunkte C101,R92,R93,F2,F3,GAA129GB1,C72
Glue Point C101,R92,R93,F2,F3,GAA129GB1,C72
Dielectric Strength:6kV/mm, temperature range -20℃ - 150℃



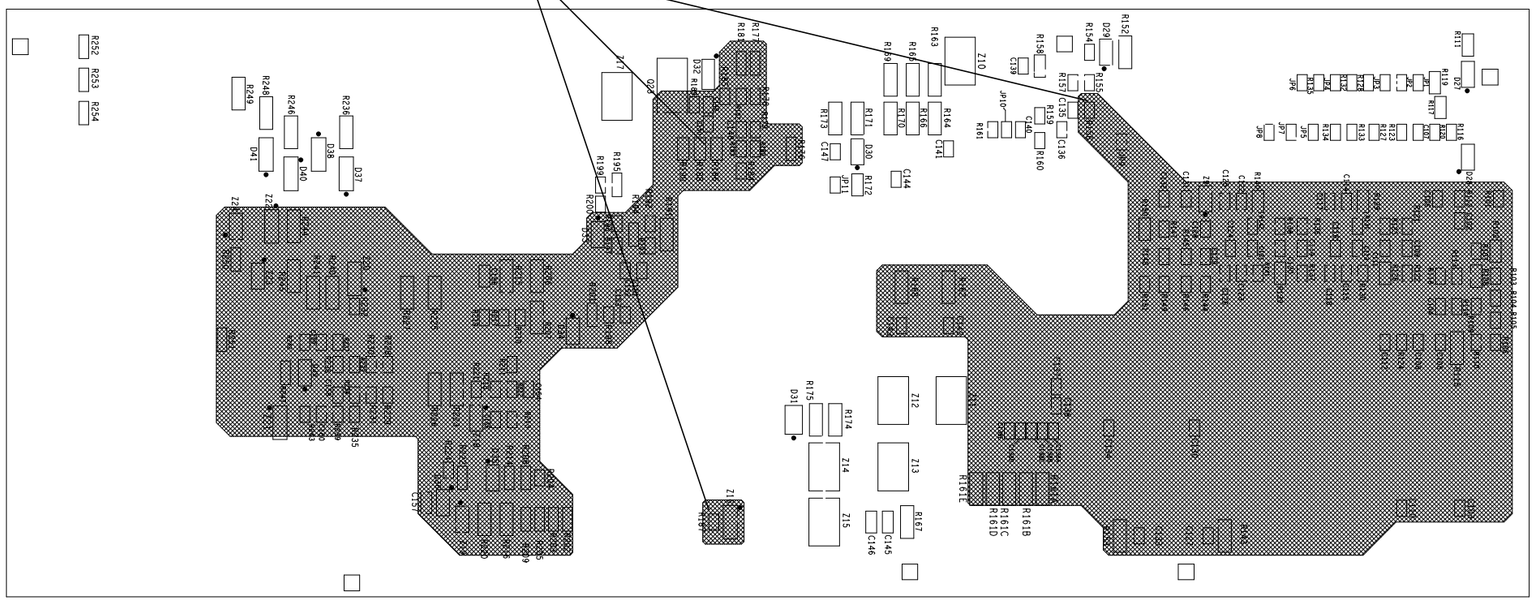
TOP VIEW
Sicht auf
Bestückungsseite

Stecker Plugs	Geeignete Gegenstecker Suitable Plugs	
P1	GAA176EP3	721-206/026-047
P2	GAA176HP1	721-202/026-037
P3	GAA176DM6	721-103/026-047
P4	GAA176FN3	723-604/000-034
P5	GAA176EP1	721-203/026-047
P6	GAA176FS6	721-602/000-034
P7,P9,P10	GAA176KA2	5-215915-0
	GAA176KA22	5-100103-0
P8	GAA176FN1	723-605/000-034
P11,P12,P16	GAA447JL3	733-102/000-047
P13	GAA176FS7	721-603/000-034
P14	GAA176FS1	721-604/000-034
P15	GAA176FP1	721-102/026-034
P17	GAA176EW5	723-602/000-034

BOTTOM

Coating Bereich
Coating Area

Prüfstempel
Test Mark



Abstand zwischen Bauteil und PCB ≥ 3mm :
R66,R67,R95,R96,R97,R98
Distance between component and PCB ≥ 3mm :
R66,R67,R95,R96,R97,R98

Höhe Hight	46 mm
Gewicht Weight	800 g
Teil Nr Part No	GDA26800KP1-5

Quality Criteria GDA26800KP_TR

ALL DIMENSIONS METRIC

CHANGES

2014-08-08 G(D)A changed CN576228 I.ATALMIS

OTIS
A United Technologies Company

PCB LAYOUT

PDB_II

CADENCE ALLEGRO
CAD GENERATED

DWG	GDA26800KP	
OTIS ENGINEERING CENTER BERLIN, GERMANY		
DRAWN	S. KOEPPEN	ORIGINAL DATE
CHK	A. TUTAT	2003-10-29
APPD	M. DEHMLOW	SHEETS
AUTH	CA47A-000039	SHEET 28

OECB_A2
REV 1998-11-13