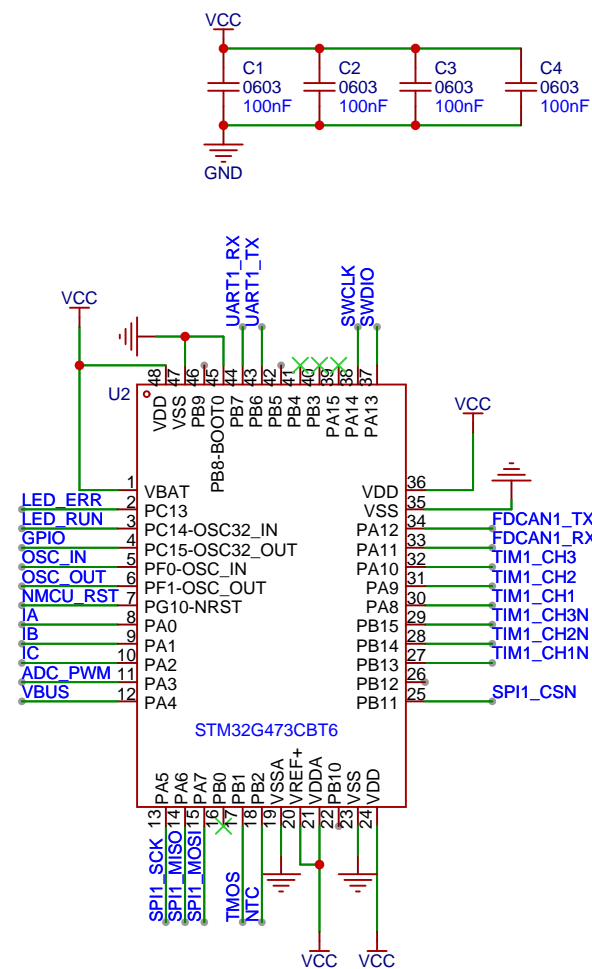
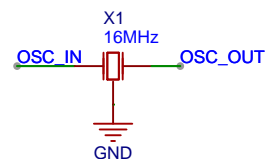


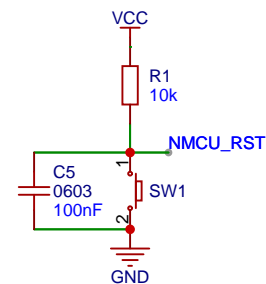
MCU



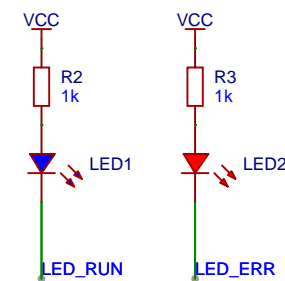
16M晶振



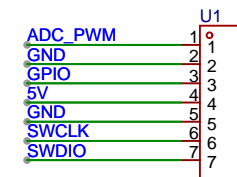
复位按钮



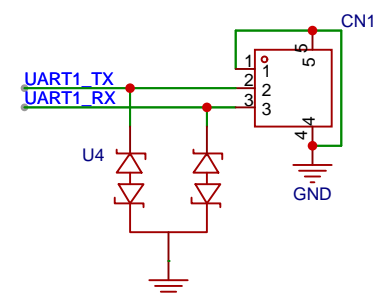
LED



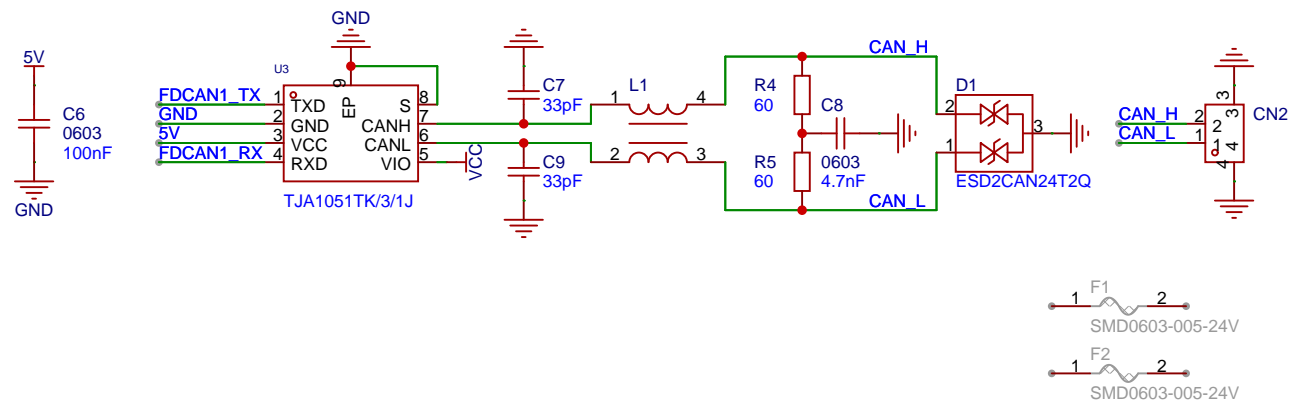
SWD及外部接口



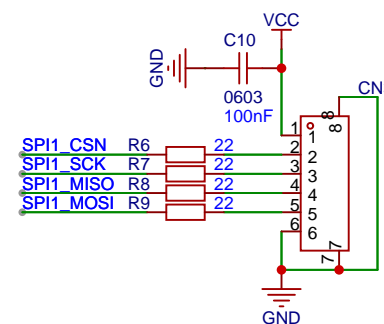
UART



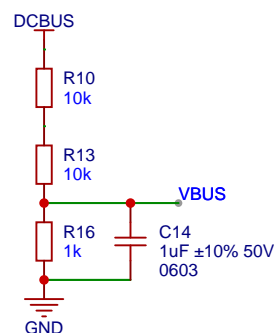
CAN通讯



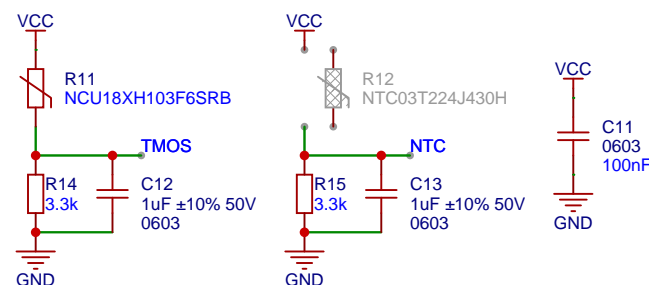
SPI接口



母线电压



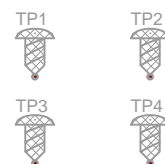
MOS、电机温度



JLC客编

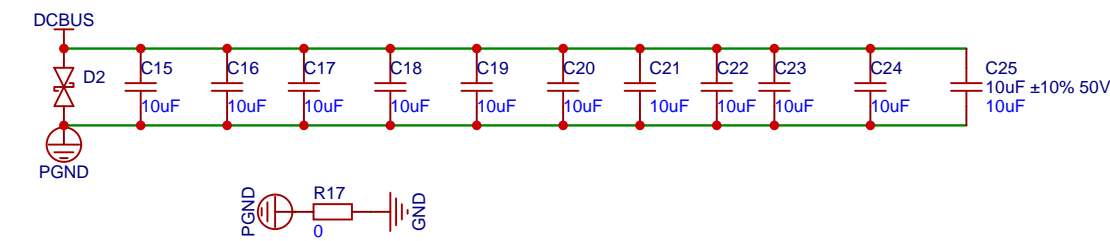


M2安装孔

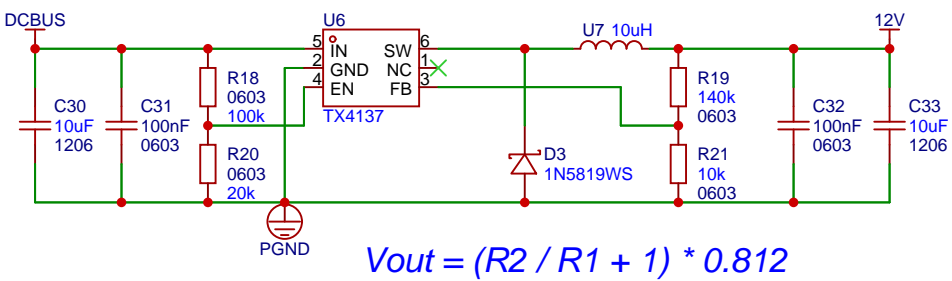


原理图	Schematic2_10			更新日期	2025-01-12
				创建日期	2025-01-07
图页	MCU			物料编码	
绘制		xhFoc_私有			
审阅					
		版本	尺寸	页	1 共 3
 嘉立创EDA		V1.0	A4	嘉立创EDA	

电源输入

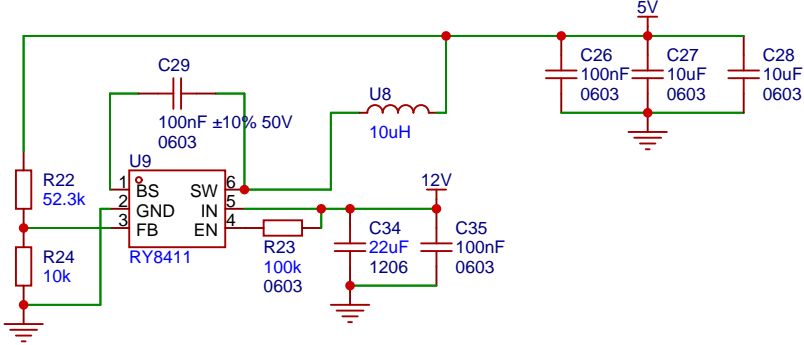


DCBUS -> 12V

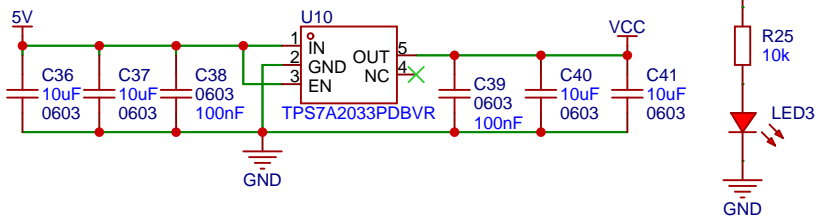


$$V_{out} = (R2 / R1 + 1) * 0.812$$

12V -> 5V

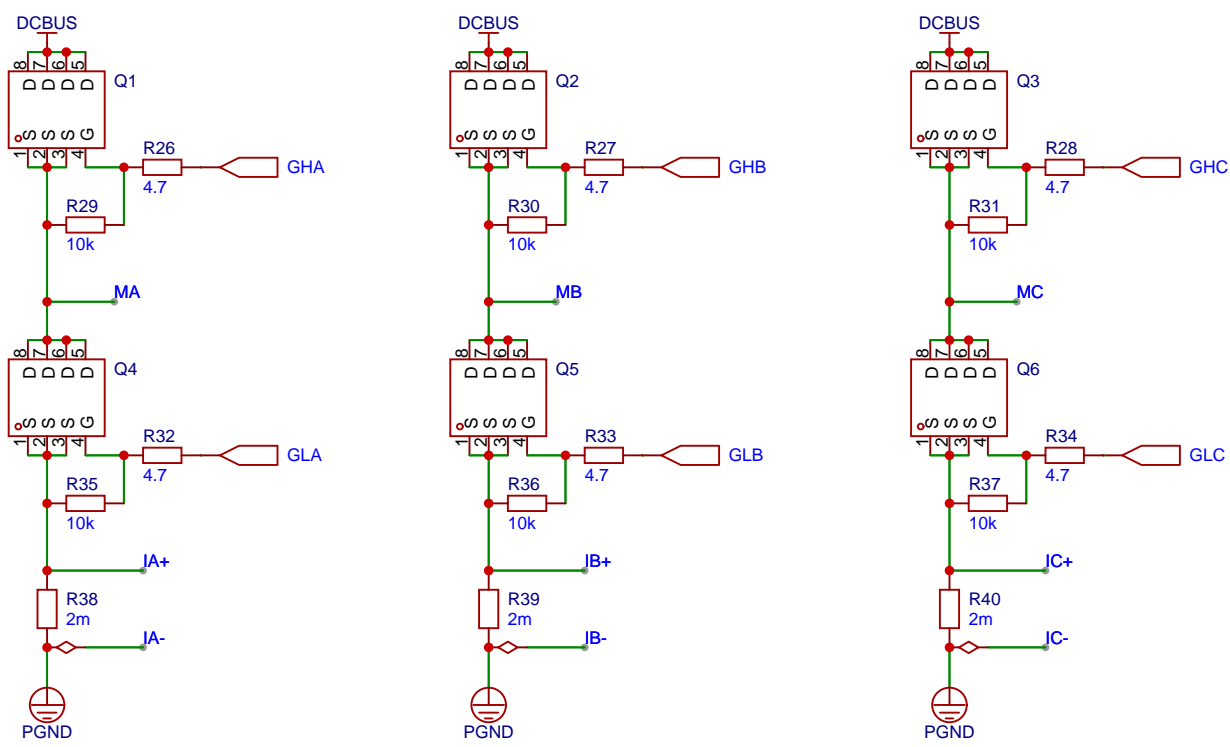


5V -> VCC

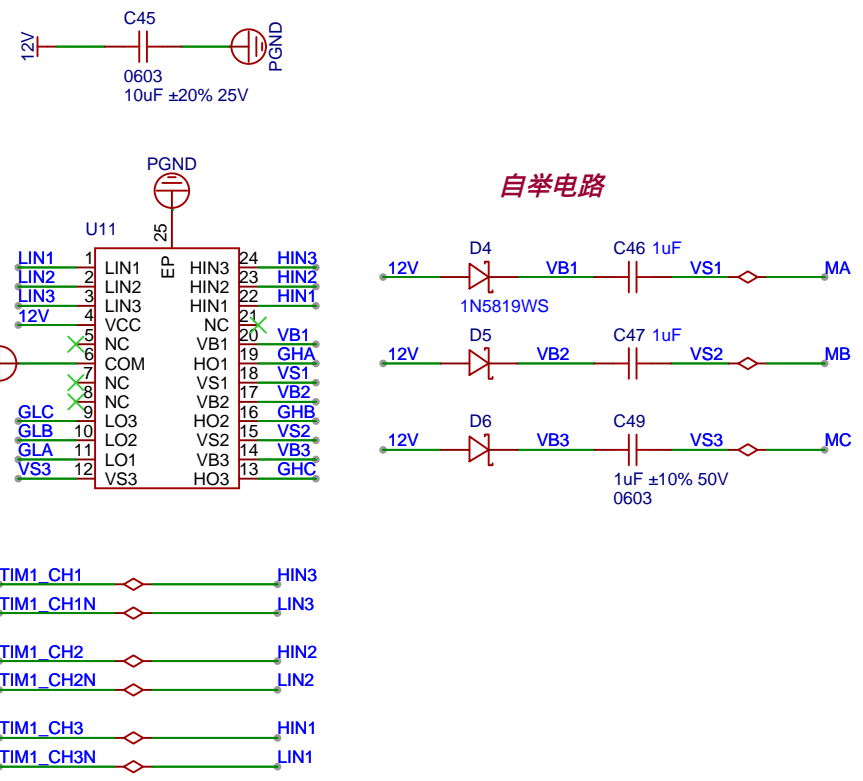


原理图	Schematic2_10		更新日期	2025-01-12
图页	POWER		创建日期	2025-01-07
绘制	xhFoc_私有			
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嘉立创EDA		V1.0	A4	嘉立创EDA

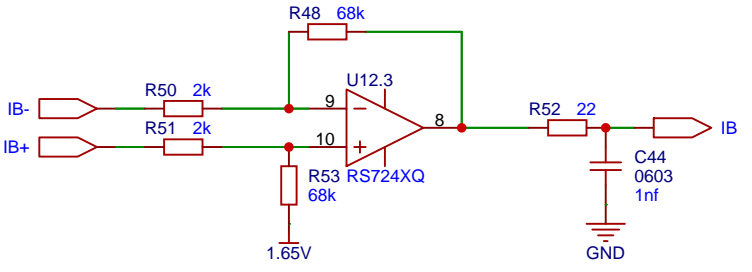
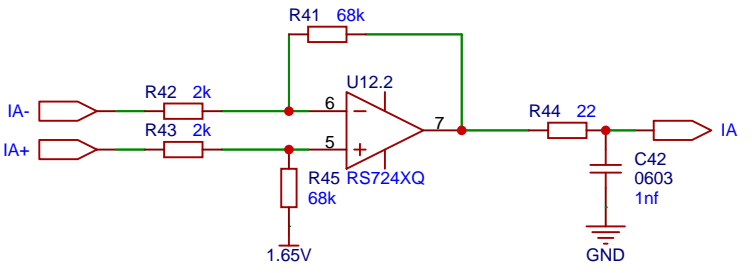
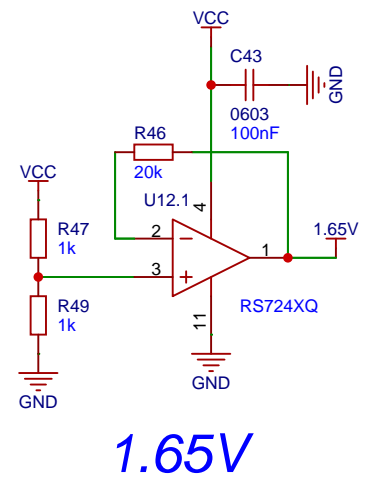
三相半桥



栅极驱动器



相电流采集



原理图	Schematic2_10		更新日期	2025-01-11
			创建日期	2025-01-07
图页	DRIVER		物料编码	
绘制	xhFoc_私有			
审阅				
		版本	尺寸	页 3 共 3
嘉立创EDA		V1.0	A4	嘉立创EDA