

ECE 510 : Emerging Functional Verification Methods
Week 7 Assignment (Assignment No : 3)
Veloce Solo – TBX Emulation Mode

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Task 1

[40]

Estimated Time – 45 minutes

Read -

1. Veloce_TBX_Mode_Intro
2. Veloce_TBX_Mode_Flow

Read the source code of following examples, also read through Makefile and understand flow

Deliverables-

1. Run Booth_DPI example in Puresim and Veloce mode. Change the C code to produce test cases ranging from -100 to -50, try the change in puresim first.
Include either screenshot or transcript text file of your result.
2. Run Booth_TBX example in Puresim and Veloce mode. Include transcript for Veloce mode.
3. Optional – Run Booth_BFM and Booth_BFM_vi examples

Task 2

Estimated Time – 2 hours

[60]

Use simple_alu from last assignment. Choose any of the methods to achieve TBX mode. Develop new project for this. Your testbench should generate each type of opcode.

If you are familiar with SystemVerilog, you may choose BFM method. In this case, you will also have to write interface and change design to use this interface.

Include tar.gz of entire project folder.