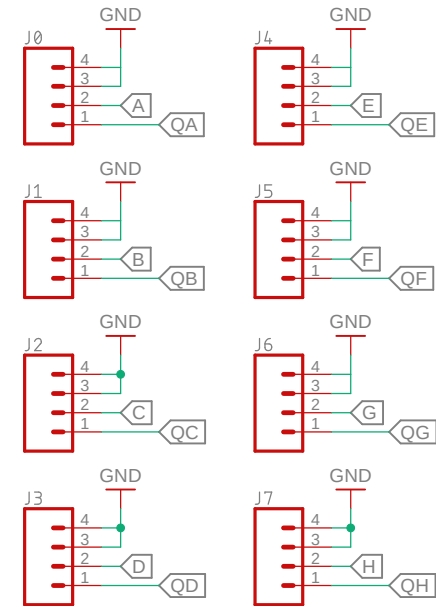Adding RN1 and RN2 pullup resistors will pull all SN74HC165N inputs HIGH
and is recommended for effective operation, but not required.

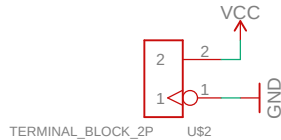Shift register pins can remain disconnected when unused.

All shift register clocks are tied together to minimize the control pins required.

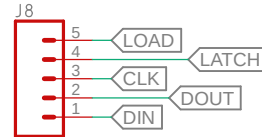Shift registers are always active, and all resets and inhibitors are disabled.
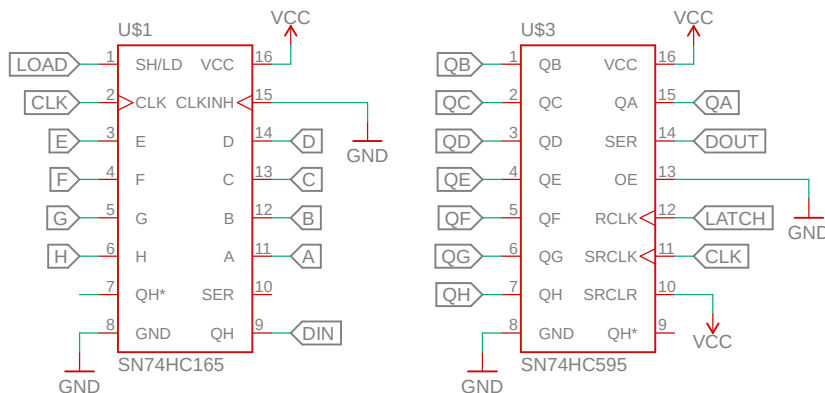
Shift Register IO Pins

Power

Shift Register Control Pins

Shift Registers

SN74HC165N Pull Up Resistor

TITLE: Shift_Register_IO_Breakout

Design by: Adam Billingsley

REV: v1.0

Date: not saved!

Sheet: 1/1