

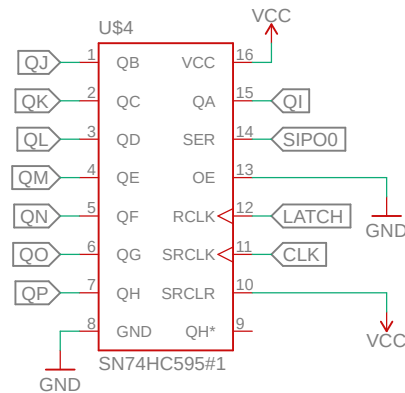
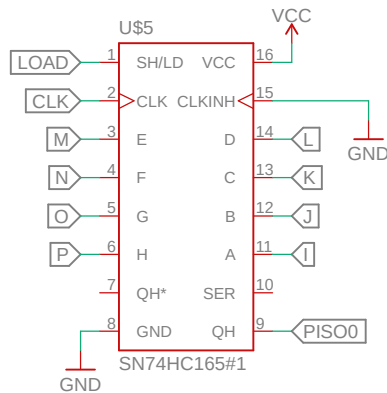
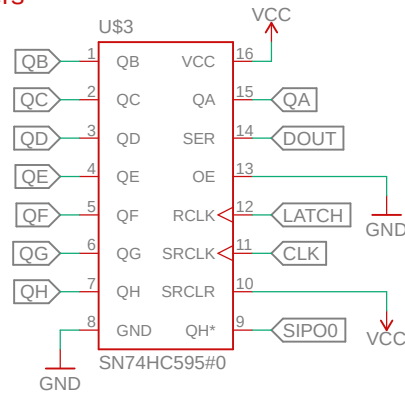
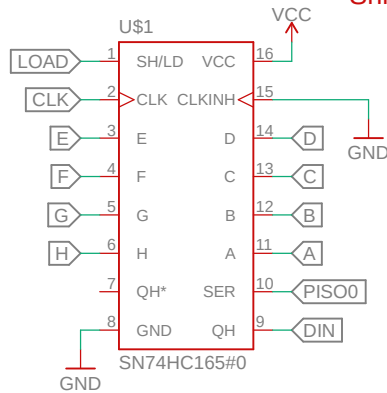
Adding RN1 and RN2 pullup resistors will pull all SN74HC165N inputs HIGH and is recommended for effective operation, but not required.

Shift register pins can remain disconnected when unused.

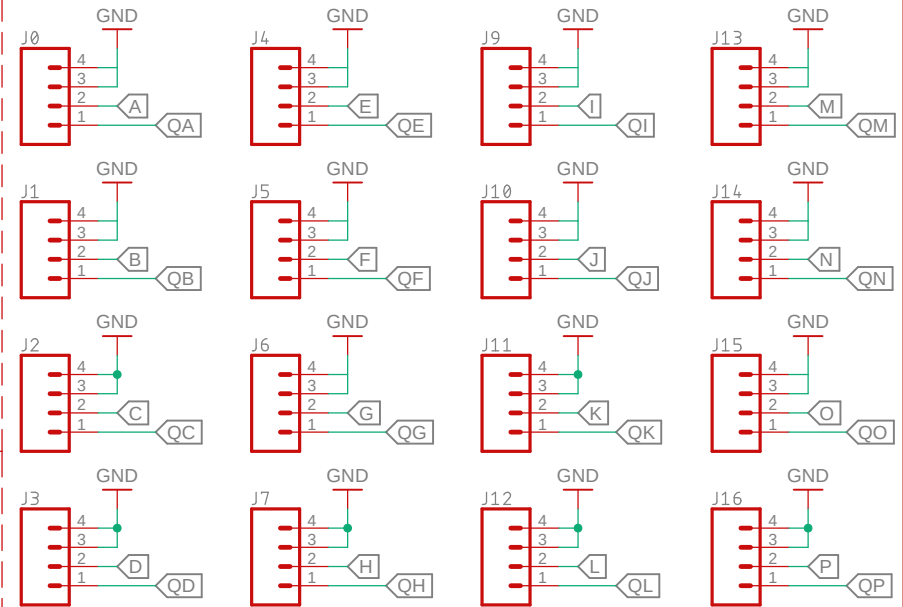
All shift register clocks are tied together to minimize the control pins required.

Shift registers are always active, and all resets and inhibitors are disabled.

Shift Registers



Shift Register IO Pins



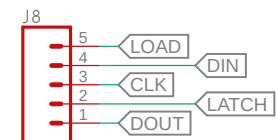
SN74HC165N Pullup Resistors



Power



Shift Register Control Pins



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TITLE: Shift_Register_2IO_Breakout

Design by: Adam Billingsley

REV:
v1.0

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Sheet: 1/1