Adding RN1 and RN2 pullup resistors will pull all SN74HC165N inputs HIGH and is recommended for effective operation, but not required.

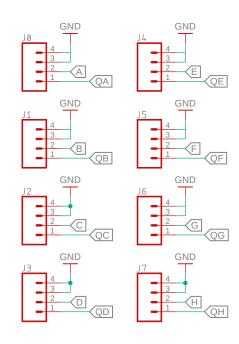
Shift register pins can remain disconnected when unused.

All shift register clocks are tied together to minimize the control pins required.

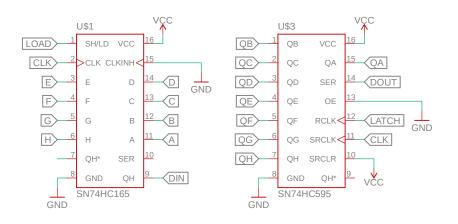
Shift registers are always active, and all resets and inhibitors are disabled.

Power Shift Register Control Pins Shift Register Control Pins TERMINAL_BLOCK_2P US2

Shift Register IO Pins



Shift Registers



SN74HC165N Pull Up Resistor



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TITLE: Shift_Register_IO_Breakout

Design by: Adam Billingsley

v1.0

REV:

Date: not saved!

Sheet: 1/1