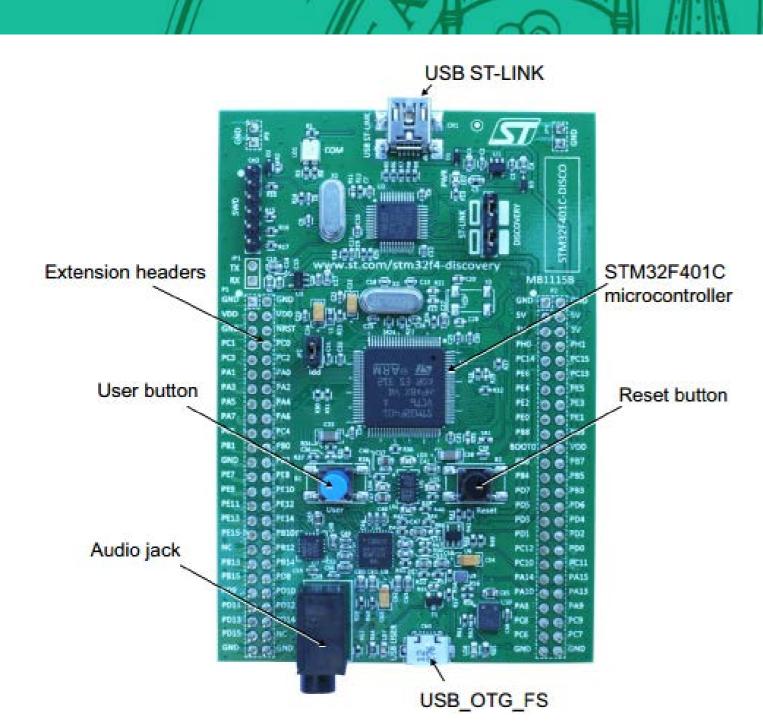


Laboratory Lesson 4:
-Timers

### Course Organization

- Hands-on session LAB1 Thursday 15.00 19.00
- Prof Benini Friday 9.00 11.00 room 5.5
- Lab is available **Friday 11.00 13.00**
- Check website for announcements, course material:
  - http://www-micrel.deis.unibo.it/LABARCH
- Final Exam:
  - Homeworks (to be checked weekly)
  - Final project
  - Final discussion (homeworks + final project)

### STM32F401 Discovery Kit



### References:

- STM32F401xB STM32F401xC datasheet
- STM32F40xxx advanced ARM®-based 32-bit MCUs reference manual (RM0344)
  - Discovery kit for STM32F401 line (UM1669)
- Getting started with STM32F401 Discovery software development tools (UM1671)



### #6 Timers

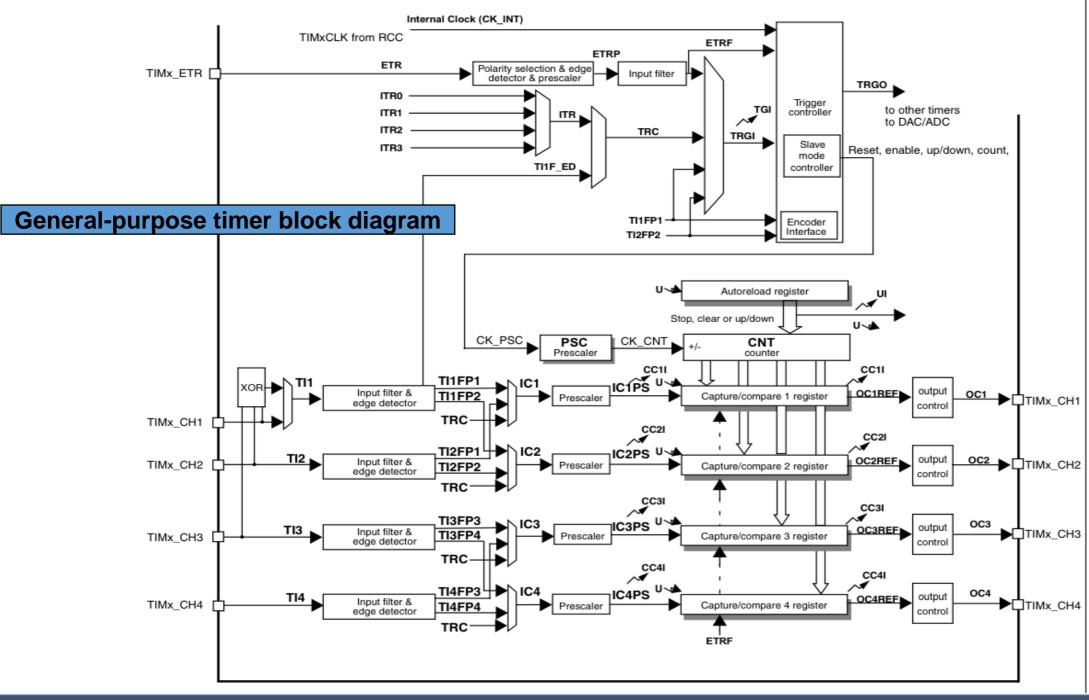
# Timers

- The general-purpose timers consist of a **16-bit auto-reload counter** driven by a programmable prescaler.
- They may be used for a variety of purposes, including measuring the pulse lengths of input signals (input capture) or generating output waveforms (PWM).
- Pulse lengths and waveform periods can be modulated from a few microseconds to several milliseconds using the timer prescaler and the RCC clock controller prescalers.



- General-purpose TIMx timer features include:
  - 16-bit up, down, up/down auto-reload counter.
  - 16-bit programmable prescaler used to divide (also "on the fly") the counter clock frequency by any factor between 1 and 65535.
  - **Up to 4 independent channels** for:
    - Input capture
    - Output compare
    - PWM generation (Edge- and Center-aligned modes)
    - One-pulse mode output

### Timers



### Timers Internal Clock (CK\_INT) TIMxCLK from RCC ETRF **ETRP** ETR Polarity selection & edge detector & prescaler TIMx\_ETR [ Input filter TRGO ITR0 ⊤GI to other timers to DAC/ADC ITR1 ITR2 TRC TRGI ITR3 Reset, enable, up/down, count, TI1F\_ED controller PATH: TimerX - Channel 1 - output compare TI1FP1 Encoder Interface TI2FP2 Autoreload register Stop, clear or up/down CK\_PSC PSC CK\_CNT CNT TI1FP1 Input filter & edge detector OC1REF output TI1FP2 TRC-CC2I TIMx\_CH1 CC2I TI2FP1 IC2PS U output OC2 → ☐TIMx\_CH2 Input filter & edge detector OC2REF TIMx\_CH2 [ TI2FP2 Prescaler Capture/compare 2 register control TRC. CC3I IC3PS U output OC3REF Input filter & -►TIMx\_CH3

Prescaler

IC4PS U

Capture/compare 3 register

Capture/compare 4 register

ETRF

CC4I

OC4REF

OC4

→ □TIMx\_CH4

output

TI3FP4

TRC-

TI4FP3

TI4FP4 TRC

edge detector

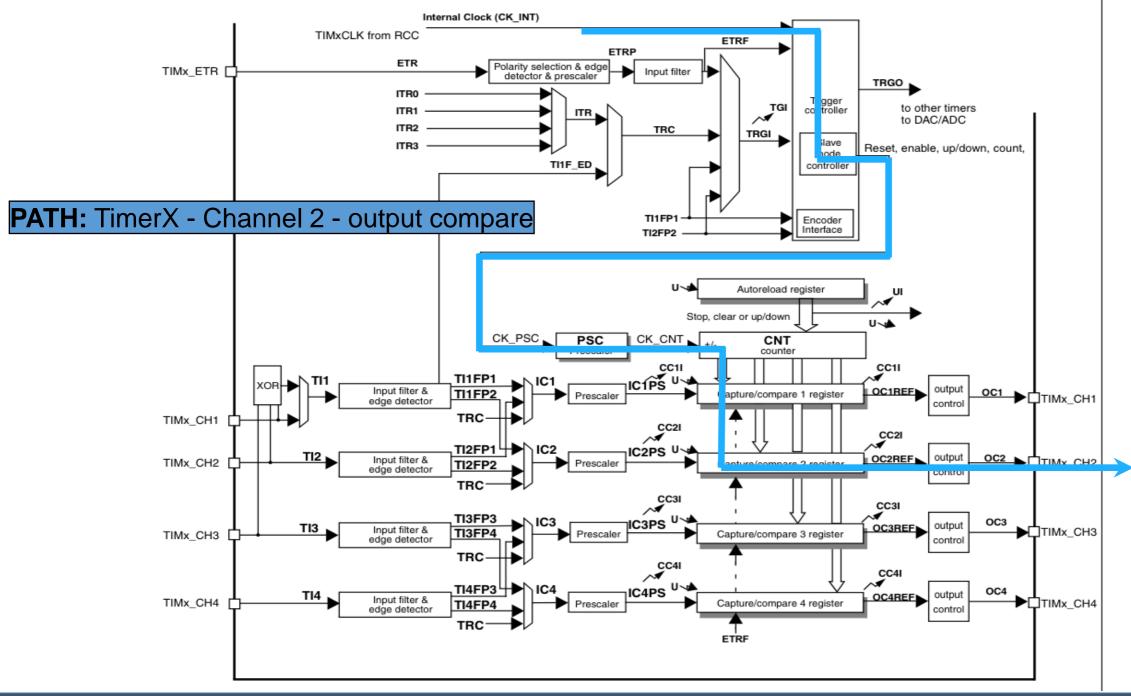
Input filter & edge detector

TI4

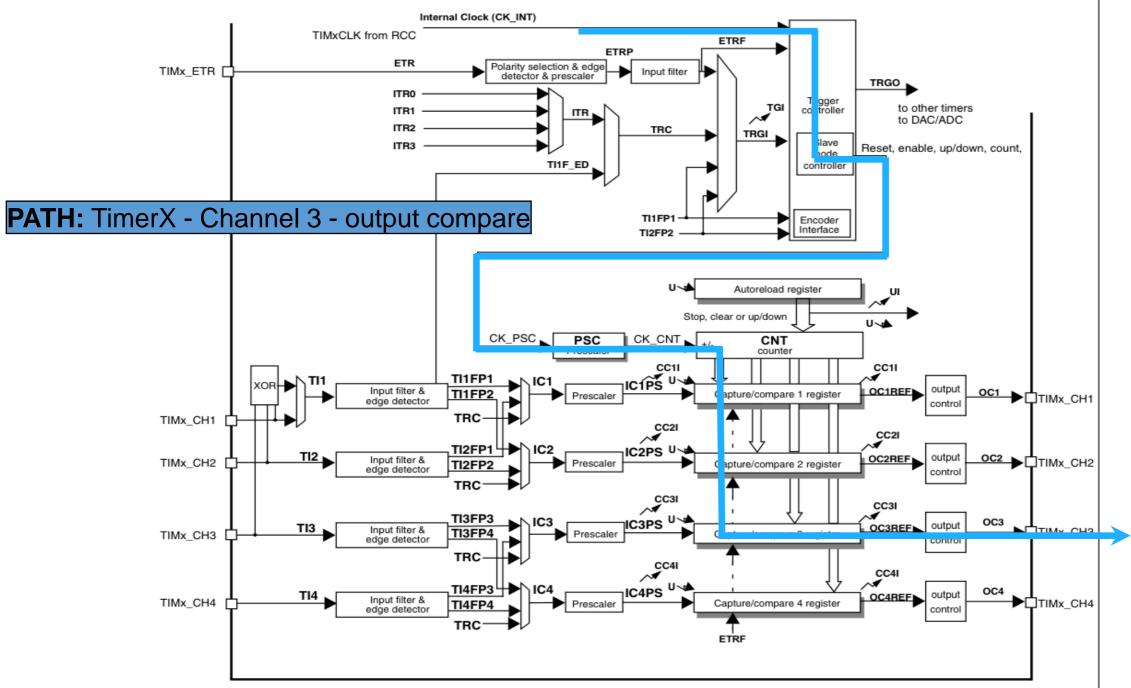
TIMx\_CH3

TIMx\_CH4

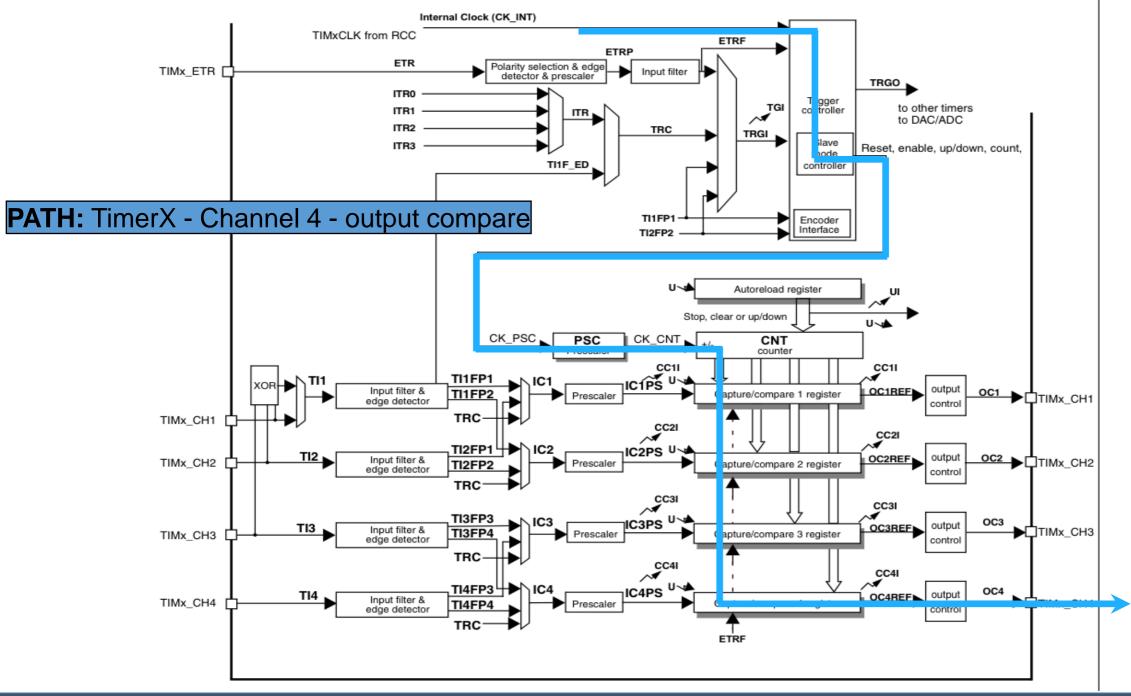
# Timers Internal Clock (CK\_INT) Timx\_ETR Tim



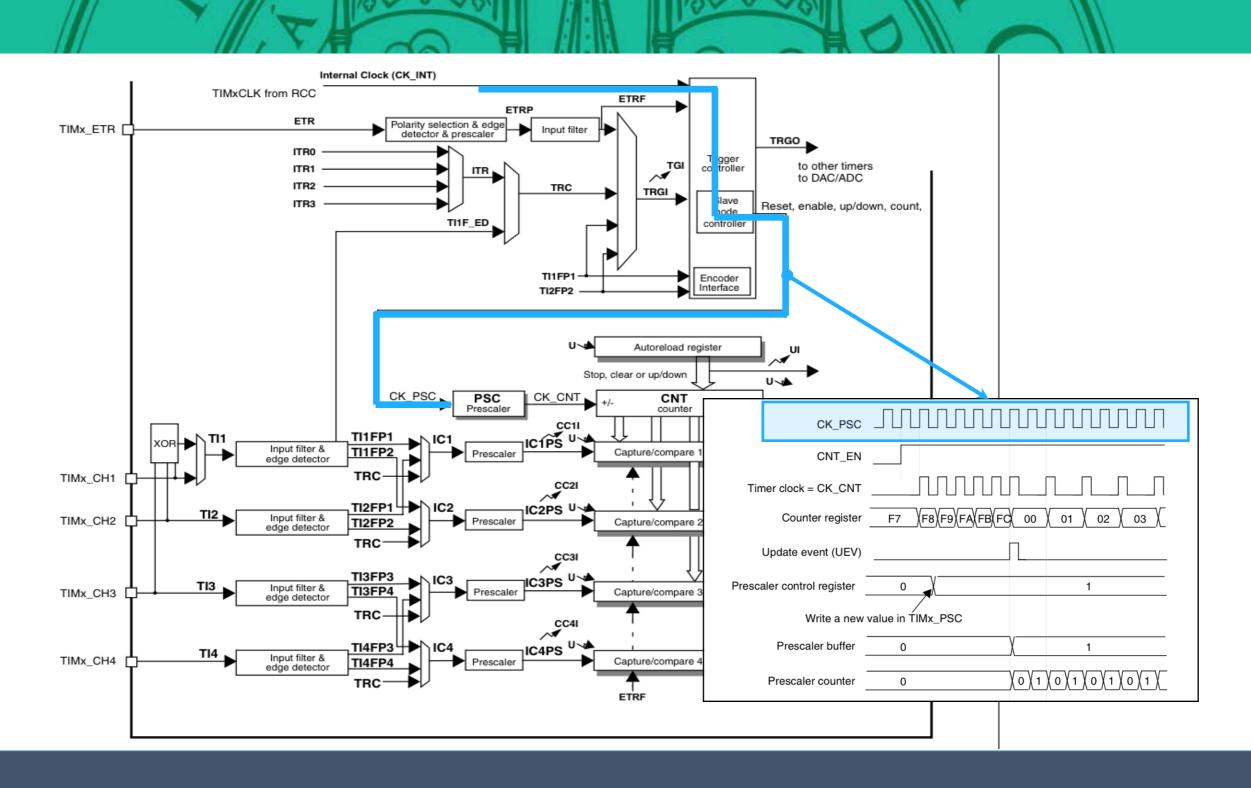
# Timers Internal Clock (CK\_INT) TIMX\_ETR INTERNAL

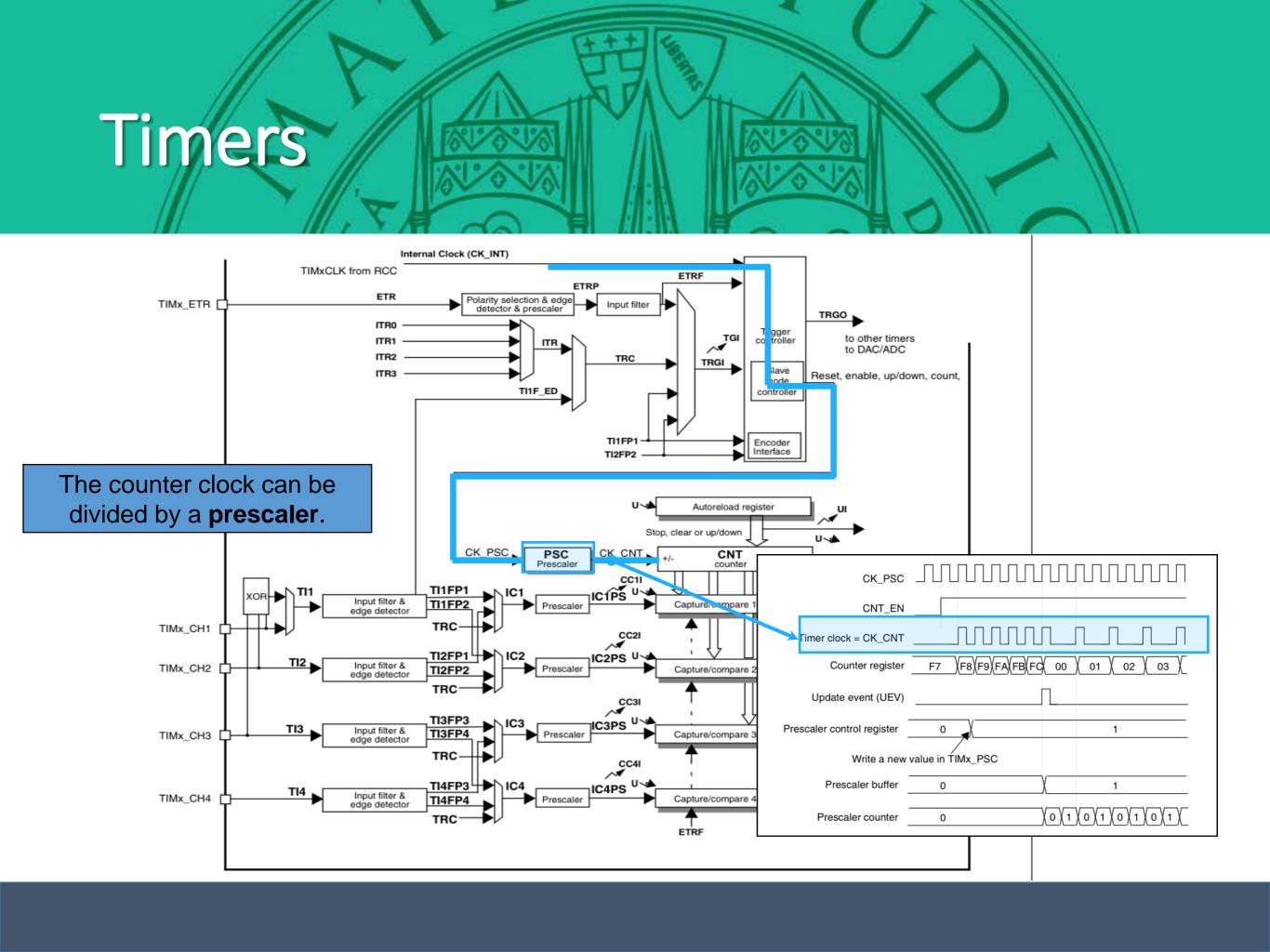


# Timers Internal Clock (CK\_INT) TiMxCLK from RCC ETR Polarity selection & edge detector & prescaler input filter TRGO



### Timers





### Timers Internal Clock (CK\_INT) TIMxCLK from RCC ETR Polarity selection & edge detector & prescaler TIMx\_ETR [ TRGO ITR0 τGI to other timers to DAC/ADC ITR1 TRGI ITR3 Reset, enable, up/down, count, TI1F\_ED controller The main block of the programmable timer is a 16-bit Encoder Interface counter with its related autoreload register. The counter can Autoreload register count up, down or both up and down. PSC Prescaler CNT Input filter & edge detector OC1 → TIMx\_CH1 OC1REF Capture/compare 1 register TIMx\_CH1 [ CNT\_EN CNT Timer clock = CK\_CNT (32)(33)(34)(35)(36)(00)(01)(02)(03)(04)(05)(06)(07) Counter register Counter overflow CK\_CNT Update event (UEV) Update interrupt flag (UIF)

### Timers Internal Clock (CK\_INT) TIMxCLK from RCC **ETR** Polarity selection & edge detector & prescaler TIMx\_ETR [ TRGO ITR0 TGI to other timers to DAC/ADC ITR1 TRGI In upcounting mode, the counter Reset, enable, up/down, count, TI1F\_ED controller counts from 0 to the auto-reload value (content of the TIMx\_ARR Encoder Interface TI2FP2 register), then restarts from 0 and generates a counter overflow Autoreload register event. op, clear or up/down CNT OC1REF output Capture/compare 1 register CNT\_EN Timer clock = CK\_CNT **Timer Interrupt** Capture/com 32 33 34 35 36 00 01 02 03 04 05 06 07 Autoreload Counter register Register Counter overflow Capture/com **CNT** Update event (UEV) CC4I Update interrupt flag (UIF) Capture/com CK\_CNT Auto-reload register 36 Write a new value in TIMx\_ARR

### Timers Internal Clock (CK\_INT) TIMxCLK from RCC ETRF **ETRP** ETR Polarity selection & edge detector & prescaler TIMx\_ETR [ Input filter TRGO ITR0 TGI → to other timers to DAC/ADC ITR1 TRC TRGI Reset, enable, up/down, count, Output compare mode: This TI1F\_ED controller function is used to control an output waveform or indicating Encoder Interface TI2FP2 when a period of time has elapsed. Autoreload register Stop, clear or up/down CK\_PSC PSC Prescaler CK CNT . CNT CC1I TI1FP1 Input filter & edge detector OC1REE Capture/compare 1 register Prescaler Write B201h in the CC1R register C2P3 U **Timer Interrupt** Autoreload B200 B201 TIMx\_CNT 0039 003A 003B Register TIMx\_CCR1 003A B201 OC1REF=OC1 CNT C4PS U OC1 Match detected on CCR1 CK\_CNT Interrupt generated if enabled

### Timers Internal Clock (CK\_INT) TIMxCLK from RCC **ETR** Polarity selection & edge detector & prescaler TIMx\_ETR [ TRGO ITR0 τGI to other timers to DAC/ADC ITR1 TRC TRGI Reset, enable, up/down, count, Output compare mode: This TI1F\_ED controller function is used to control an Used to schedule output waveform or indicating Encoder Interface TI2FP2 periodic events when a period of time has elapsed. Autoreload register Stop, clear or up/down PSC CK CNT . CNT CC1I Input filter & edge detector OC1REE Capture/compare 1 register Prescaler Write B201h in the CC1R register **Timer Interrupt** Autoreload B200 B201 TIMx\_CNT 0039 003A 003B Register TIMx\_CCR1 003A B201 OC1REF=OC1 CNT C4PS L OC1 Match detected on CCR1 CK\_CNT Interrupt generated if enabled

### Timers (What)

- I want a LED blinking at a given frequency using a timer.
  - We need to setup the GPIO port and pin the LED is connected to
    - → We already know how to do that
  - Since we are going to use interrupts generated by timers we need to setup NVIC
    - → The IRQChannel for TIMER3 is TIM3 IRQn
    - ➡The ISR is void TIM2\_IRQHandler(void)
  - We need a generic timer because we want the LED blinking at a fixed frequency
  - We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

We need to setup the **GPIO** port and pin the LED is connected to:

```
void LEDs_Configuration(void)
GPIO_InitTypeDef GPIO_InitStructure;
  /* Enable the GPIO_LED Clock */
  RCC_AHB1PeriphClockCmd(RCC_AHB1Periph_GPIOD, ENABLE);
  /* Configure the GPIO_LED pin */
  GPIO_InitStructure.GPIO_Pin = GPIO_Pin_12;
  GPIO_InitStructure.GPIO_Mode = GPIO_Mode_OUT;
  GPIO_InitStructure.GPIO_OType = GPIO_OType_PP;
  GPIO_InitStructure.GPIO_PuPd = GPIO_PuPd_UP;
  GPIO InitStructure.GPIO Speed = GPIO Speed 50MHz;
  GPIO Init(GPIOD, &GPIO InitStructure);
```



Since we are going to use interrupts generated by timers we need to setup **NVIC** 

```
void NVIC_Configuration(void)
{
    NVIC_InitTypeDef NVIC_InitStructure; /* TIM3 clock enable */

    /* Enable the TIM3 gloabal Interrupt */
    NVIC_InitStructure.NVIC_IRQChannel = TIM3_IRQn;
    NVIC_InitStructure.NVIC_IRQChannelPreemptionPriority = 0;
    NVIC_InitStructure.NVIC_IRQChannelSubPriority = 1;
    NVIC_InitStructure.NVIC_IRQChannelCmd = ENABLE;

    NVIC_Init(&NVIC_InitStructure);
}
```

We need a **generic timer** because we want the LED blinking at a fixed frequency:

uint16\_t PrescalerValue = 0; TIM\_TimeBaseInitTypeDef TIM\_TimeBaseStructure; As usual a struct is used for the configuration of the peripheral



uint16\_t PrescalerValue = 0; TIM\_TimeBaseInitTypeDef TIM\_TimeBaseStructure;

Clock enable for the TIMER3

/\* Enable the TIM3 gloabal Interrupt \*/

RCC\_APB1PeriphClockCmd(RCC\_APB1Periph\_TIM3, ENABLE);



```
uint16_t PrescalerValue = 0;
TIM_TimeBaseInitTypeDef TIM_TimeBaseStructure;
```

/\* Enable the TIM3 gloabal Interrupt \*/

RCC\_APB1PeriphClockCmd(RCC\_APB1Periph\_TIM3, ENABLE);

TIM\_TimeBaseStructure.TIM\_Period = 65535; // 2<sup>16</sup>

upcounting mode



```
uint16_t PrescalerValue = 0;
TIM_TimeBaseInitTypeDef TIM_TimeBaseStructure;

/* Enable the TIM3 gloabal Interrupt */

RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);

TIM_TimeBaseStructure.TIM_Period = 65535; // 2<sup>16</sup>
TIM_TimeBaseStructure.TIM_Prescaler = 0;
TIM_TimeBaseStructure.TIM_ClockDivision = 0;
TIM_TimeBaseStructure.TIM_CounterMode = TIM_CounterMode_Up;
```

upcounting mode



```
uint16_t PrescalerValue = 0;
TIM_TimeBaseInitTypeDef TIM_TimeBaseStructure;
/* Enable the TIM3 gloabal Interrupt */
RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);
TIM_TimeBaseStructure.TIM_Period = 65535;
TIM_TimeBaseStructure.TIM_Prescaler = 0;
TIM_TimeBaseStructure.TIM_ClockDivision = 0;
TIM_TimeBaseStructure.TIM_CounterMode = TIM_CounterMode_Up;
TIM_TimeBaseInit(TIM3, &TIM_TimeBaseStructure); /* Prescaler configuration */
```

As usual the init routine



```
uint16_t PrescalerValue = 0;
TIM_TimeBaseInitTypeDef TIM_TimeBaseStructure;

/* Enable the TIM3 gloabal Interrupt */

RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);

TIM_TimeBaseStructure.TIM_Period = 65535;

TIM_TimeBaseStructure.TIM_Prescaler = 0;

TIM_TimeBaseStructure.TIM_ClockDivision = 0;

TIM_TimeBaseStructure.TIM_CounterMode = TIM_CounterMode_Up;

TIM_TimeBaseInit(TIM3, &TIM_TimeBaseStructure); /* Prescaler configuration */
```

As usual the init routine

**TIM\_PrescalerConfig**(TIM3, **PrescalerValue**, TIM\_PSCReloadMode\_Immediate);



### **Prescaler Value:**

To set the <u>prescaler</u> we use the formula:

Prescaler = ((SystemCoreClock / 2) Fx) - 1

where Fx is the counter clock of the TIMER (CK\_CNT) we want.

/\* Compute the prescaler value \*/

PrescalerValue = (uint16\_t) ((SystemCoreClock / 2) / 500000) - 1; // 500 KHz

This value has to be set before calling TIM\_PrescalerConfig function



• We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

TIM\_OCInitTypeDef TIM\_OCInitStructure;

As usual a struct is used for the configuration of the peripheral

• We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;
```

TIM\_OCInitStructure.TIM\_OCMode = TIM\_OCMode\_Timing;

The comparison between the output compare register and the counter has no effect on the outputs. (this mode is used to generate a timing base).

We are interested in interrupt not in output waveform.

We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;

TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;
TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;
```

OC1 signal is active high on the corresponding output pin

We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;
TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;
TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;

TIM_OCInitStructure.TIM_Pulse = 0;
```

The compare register is set to 0.

We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;

TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;

TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;

TIM_OCInitStructure.TIM_Pulse = 0;

TIM_OC1Init(TIM3, &TIM_OCInitStructure);
```

Init as usual

We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;

TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;

TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;

TIM_OCInitStructure.TIM_Pulse = 0;

TIM_OC1Init(TIM3, &TIM_OCInitStructure);

TIM_OC1PreloadConfig(TIM3, TIM_OCPreload_Disable);
```

The compare register can be written at anytime, the new value is taken in account immediately

We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;

TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;
TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;

TIM_OCInitStructure.TIM_Pulse = 0;

TIM_OC1Init(TIM3, &TIM_OCInitStructure);
TIM_OC1PreloadConfig(TIM3, TIM_OCPreload_Disable);
TIM_ITConfig(TIM3, TIM_IT_CC1, ENABLE);
```

We are interested in the **interrupt** of the CHANNEL 1 of the TIMER3

• We use the TIM3\_CH1 (Timer 3 channel 1) in output compare mode

```
TIM_OCInitTypeDef TIM_OCInitStructure;

TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;

TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;

TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;

TIM_OCInitStructure.TIM_Pulse = 0;

TIM_OC1Init(TIM3, &TIM_OCInitStructure);

TIM_OC1PreloadConfig(TIM3, TIM_OCPreload_Disable);

TIM_ITConfig(TIM3, TIM_IT_CC1, ENABLE);

TIM_Cmd(TIM3, ENABLE);

TIMER2 enabled
```

Handle the interrupt

```
void TIM3_IRQHandler(void)
{
    if (TIM_GetITStatus(TIM3, TIM_IT_CC1) != RESET)
    {
        TIM_ClearITPendingBit(TIM3, TIM_IT_CC1);
        // Turn on the LED
}
```

- 1. Check the flags to see what channel the interrupt is related to
- Clear the flag
- 3. Turn on/off the LED

### Timers (Code:Main)

```
#include "stm32f4xx.h"
#include "stm32f401 discovery.h"
void NVIC_Configuration(void)
     NVIC_InitTypeDef NVIC_InitStructure; /* TIM3 clock enable */
     RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);
     NVIC_InitStructure.NVIC_IRQChannel = TIM3_IRQn;
     NVIC_InitStructure.NVIC_IRQChannelPreemptionPriority = 0;
     NVIC_InitStructure.NVIC_IRQChannelSubPriority = 1;
     NVIC_InitStructure.NVIC_IRQChannelCmd = ENABLE;
     NVIC_Init(&NVIC_InitStructure);
void LEDs_Configuration(void)
    GPIO_InitTypeDef GPIO_InitStructure;
    RCC_AHB1PeriphClockCmd(RCC_AHB1Periph_GPIOD, ENABLE);
    GPIO_InitStructure.GPIO_Pin = GPIO_Pin_12;
    GPIO_InitStructure.GPIO_Mode = GPIO_Mode_OUT;
    GPIO InitStructure.GPIO OType = GPIO OType PP;
    GPIO_InitStructure.GPIO_PuPd = GPIO_PuPd_UP;
    GPIO InitStructure.GPIO Speed = GPIO Speed 50MHz;
    GPIO_Init(GPIOD, &GPIO_InitStructure);
```

### Timers (Code:Main)

```
int main(void)
    TIM TimeBaseInitTypeDef TIM TimeBaseStructure;
    TIM OCInitTypeDef TIM OCInitStructure;
    uint16 t PrescalerValue = 0:
    LEDs_Configuration();
    NVIC Configuration();
    RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);
    /* Compute the prescaler value */
    PrescalerValue = (uint16 t) ((SystemCoreClock /2)/ 500000) - 1;
    /* Time base configuration */
    TIM_TimeBaseStructure.TIM_Period = 65535;
    TIM_TimeBaseStructure.TIM_Prescaler = 0;
    TIM_TimeBaseStructure.TIM_ClockDivision = 0;
    TIM TimeBaseStructure.TIM CounterMode = TIM CounterMode Up;
     TIM_TimeBaseInit(TIM3, &TIM_TimeBaseStructure);
    TIM_PrescalerConfig(TIM3, PrescalerValue, TIM_PSCReloadMode_Immediate);
    /* Output Compare Timing Mode configuration: Channel1 */
    TIM_OCInitStructure.TIM_OCMode = TIM_OCMode_Timing;
    TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;
    TIM_OCInitStructure.TIM_Pulse = 0;
    TIM_OCInitStructure.TIM_OCPolarity = TIM_OCPolarity_High;
     TIM_OC1Init(TIM2, &TIM_OCInitStructure);
    TIM_OC1PreloadConfig(TIM3, TIM_OCPreload_Disable);
    TIM_ITConfig(TIM3, TIM_IT_CC1, ENABLE);
    /* TIM2 enable counter */
    TIM_Cmd(TIM3, ENABLE);
    while(1);
    return(0);
```

### Timers (Code:II)

### stm32f4xx\_it.c

### Timers (Esercizi)

- 1. Fai lampeggiare un LED a 2Hz, ed un'altro a 3Hz
  - → Tip: usa TIM\_GetCapture1() e TIM\_SetCompare1()
  - →Tip: devi utilizzare due differenti canali
- 2. Utilizza un bottone per modificare la frequenza di lampeggio di un LED (usando ovviamente i timers).
- 3. Genera un onda quadra a 500Hz in output da un TIM2\_CH1 pin (controlla con l'oscilloscopio) (opzionale)
  - Tips: Dovreste usare il Toggle del Output Compare mode
  - →STM32F4xx Standard Peripherals Library,

### Timers (Domande)

- 1. Quali sono gli altri possibili modi per configurare il TIM\_OCMode, nel campo di TIM\_OCInitTypeDef **structure** (guardare anche I manuali messi a disposizione)
- 2. Come funziona la modalità TIM\_CounterMode\_CenterAligned in comparazione alla modalità TIM\_CounterMode\_Up) ?
- 4. Cosa accade se omettiamo TIM\_OC1PreloadConfig(...) ?
- 5. Che cos'è ClockDivision?