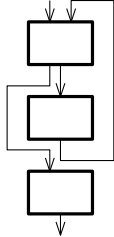
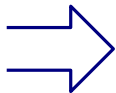
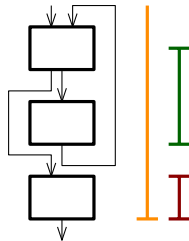


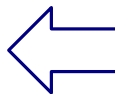
Block Layout



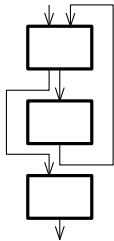
Lifetime Analysis



Register Assignment



Code Generation



@11 \mapsto %rax

@25 \mapsto %r9

@31 \mapsto [-8]