Block Layout

Code Generation
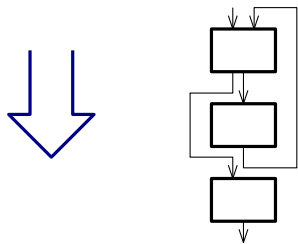
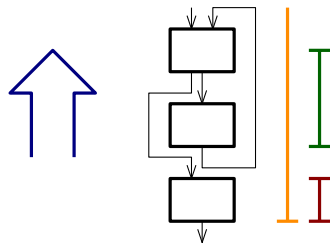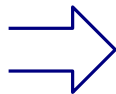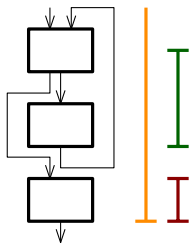**Lifetime Analysis**

@11 $\mapsto$ `%rax`

@25 $\mapsto$ `%r9`

@31 $\mapsto$ `[-8]`

Register Assignment