

# Bare Demo of IEEEtran.cls for Conferences

Michael Shell  
School of Electrical and  
Computer Engineering  
Georgia Institute of Technology  
Atlanta, Georgia 30332-0250

Email: <http://www.michaelshell.org/contact.html>

Homer Simpson  
Twentieth Century Fox  
Springfield, USA  
Email: [homer@thesimpsons.com](mailto:homer@thesimpsons.com)

James Kirk  
and Montgomery Scott  
Starfleet Academy  
San Francisco, California 96678-2391  
Telephone: (800) 555-1212  
Fax: (888) 555-1212

*Abstract*—The abstract goes here.

## I. ACCESS RECENCY

## II. TECHNOLOGY

*A. DRAM stuff*

*B. Refresh*

### III. INTELLIGENT REFRESH TECHNIQUES

*A. Access Recency*

*B. Retention*

*C. Tolerance*

*D. Validity*

#### IV. ACCESS RECENCY

## V. RETENTION

[1]

## VI. TOLERANCE

## VII. VALIDITY



## VIII. TECHNIQUE COMBINATION

## IX. EVALUATION

## X. CONCLUSION

## XI. CONCLUSION

The conclusion goes here. [2]

## ACKNOWLEDGMENT

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## REFERENCES

- [1] Z. Z. Y. B. M. C. Zehan Cui, Sally A. McKee, "Dtail: A flexible approach to dram refresh management," in *Proc. ACM International Conference on Supercomputing (ICS'14)*, Jun. 2014, pp. 290–294.
- [2] —, "Dtail: A flexible approach to dram refresh management," *IEEE Electron Device Lett.*, vol. 20, pp. 569–571, Nov. 1999.