

# Report 4 Cuda 3D processing

Le Nhu Chu Hiep

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## 1 Implementation

- Defining "grid size" and "block size" as dim3
- Redefining grayScale function to identify array identity from 3D index
- Run as normal

## 2 Result

### 2.1 Text result

```
-- Labwork 4 --  
USTH ICT Master 2018, Advanced Programming for HPC.  
Warming up...  
Starting labwork 4  
labwork 4 ellapsed 143.9ms
```

```
-- Labwork 3 --  
USTH ICT Master 2018, Advanced Programming for HPC.  
Warming up...  
Starting labwork 3  
blockSize: 32  
labwork 3 ellapsed 4243.4ms
```

### 2.2 Figure result



Figure 1: Eiffel picture