

**Real-time Video Alignment and Fusion Using Feature Detection on FPGA
Devices**

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Robert Haywood Taglang

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Abstract

Real-time Video Alignment and Fusion Using Feature Detection on FPGA Devices

Robert Haywood Taglang

Prawat Nagvajara, Ph.D.

Video fusion functions as a way to combine the important or useful parts of two or more sequences of images. The scenario presented is the use of Laplacian fusion to produce a single video composed of the fields of view of two cameras whose areas of focus differ substantially. This is not a useful real-time strategy unless the frames can be aligned. This thesis presents a system for detecting features using an FPGA implementation of SURF (Speeded-Up Robust Features), and aligning video streams by applying a transform generated from the key features.

1 Background

1.1 Introduction

The fusion of data from two or more sensors has been well-researched [8] [6], though these approaches typically discuss the process of fusing images which have been pre-aligned. Pre-computed transforms used to align the frames of two cameras are not robust to variations. Some approaches have made use of additional hardware sensors in order to correct against these variations [4]. The approach presented in this thesis seeks to perform this correction completely in hardware using feature detection on a FPGA.

The design choice to use a FPGA rather than a GPU or some other software based approach was made due to the advantages gained from operating with embedded hardware, namely higher portability and lower overall power consumption.

1.2 Laplacian Fusion

Laplacian pyramids of images have their origin as a strategy for image encoding [3]. A gaussian blur is applied to the image, and the image is downsampled to half of its original size. This process can be repeated on the resulting image to create a sequence of images representing the original in different scale spaces. This sequence of blurred and downsampled images is known as a Gaussian pyramid. An illustration of a Guassian pyramid for an example image can be seen in Figure 1.

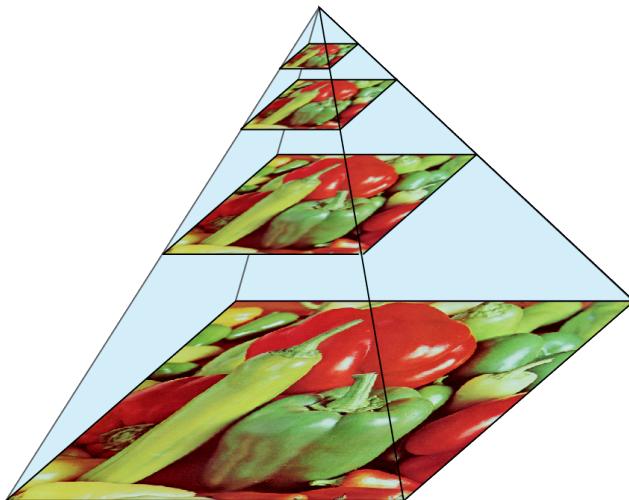


Figure 1: Gaussian pyramid of an example image

The Laplacian pyramid is one which can be used for reconstruction of the original image.

At each level above the lowest level of the Gaussian pyramid, the level below is upsampled to match the scale of the current level. The difference between the upsampled image and the current scale level image is known as the Laplacian of the image. The sum of the upsampled lower level and the Laplacian is the original image. At a single level, the Laplacian can be thought of as the error introduced by applying a Gaussian and Box filter. A diagram illustrating a Laplacian pyramid can be seen in Figure 2

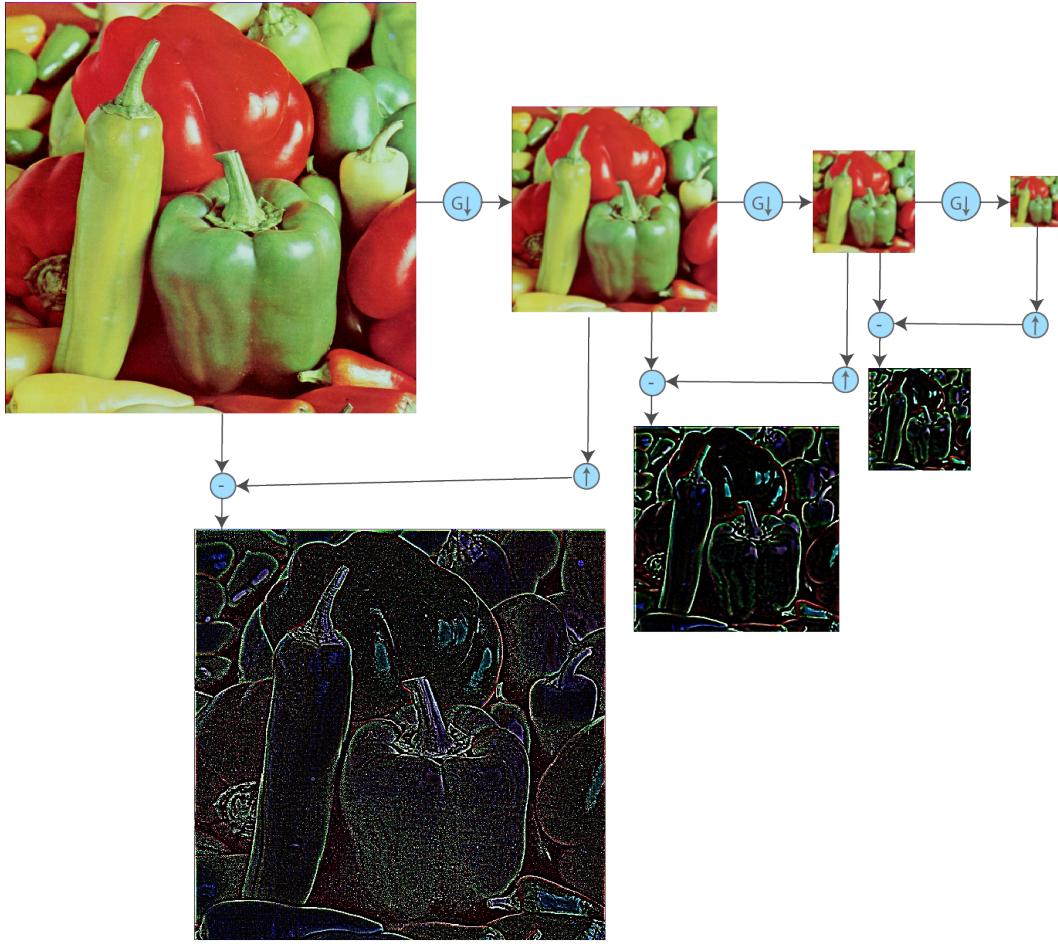


Figure 2: Laplacian pyramid of an example image

The property of the Laplacian that makes it ideal for fusion is its ability to capture the high frequency components in an image through the use of very simple kernel operators that are easily implemented in hardware. The difference between a blurred image and the original will have higher magnitude in the areas where the image was sharpest.

The fusion of two images can be thought of as a function of the two images X and Y of dimension $M \times N$ where $Z = f(X, Y)$, a single image of dimension $M \times N$. A naive approach to fusion would be to compute the Laplacians and use their magnitudes to select

a pixel from either X or Y as shown in Equation 1.

$$Z(i, j) = \begin{cases} X(i, j) & |L(X(i, j))| \geq |L(Y(i, j))| \\ Y(i, j) & otherwise \end{cases} \quad (1)$$

This approach does not account for variations in colorspace between the two images. Consider the images in Figure 3. The more saturated image will likely have a higher valued Laplacian in some parts simply because it is brighter, therefore having higher magnitudes at individual pixels. This approach also will not facilitate smooth stitching of the images. Contiguous regions of selection from one image will be adjacent to regions from the other with no transition, producing a grainy effect at areas of high frequency. The result of this naive fusion can be seen in Figure 4a which exhibits the flaws of this approach.

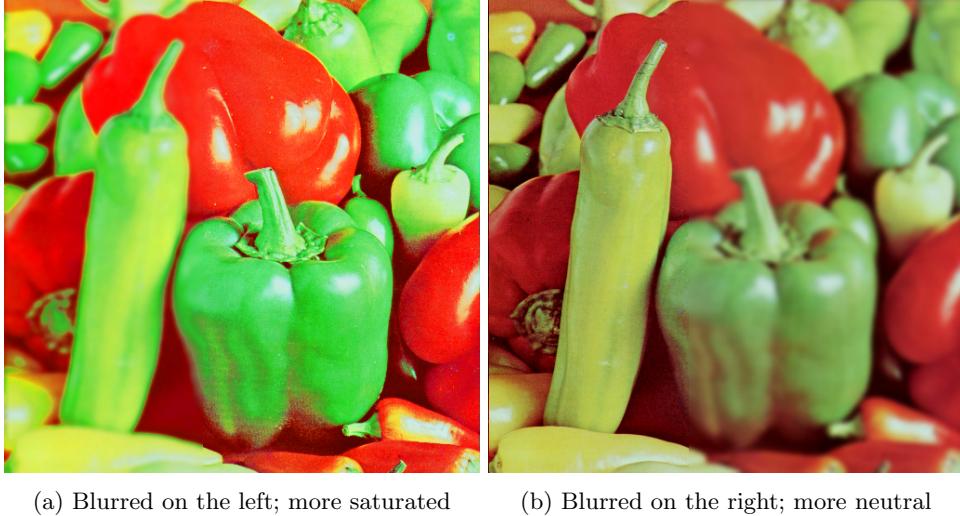


Figure 3: Two images of the same scene with variations in sharpness and colorspace

A more correct approach would involve using the Laplacian in a weighted sum to combine the pixels of the images, rather than simply selecting them, as shown in Equation 2.

$$Z(i, j) = \frac{|L(X(i, j))|}{|L(X(i, j))| + |L(Y(i, j))|} \cdot X(i, j) + \frac{|L(Y(i, j))|}{|L(X(i, j))| + |L(Y(i, j))|} \cdot Y(i, j) \quad (2)$$

The result of this weighted sum approach can be seen in Figure 4b which exhibits a reduction in graininess from the naive approach.



(a) Fusion using the naive approach

(b) Fusion using weighted sum

Figure 4: Fusion of the images in Figure 3 using a naive selection and weighted sum approach

1.3 Speeded-up Robust Features (SURF)

The generation of features for use as marker points in alignment utilizes the SURF algorithm from Bay et al [2]. SURF is composed of two parts: a discrete approximation for computing Hessian determinants, and the generation of rotation invariant feature descriptors for detected feature points.

SURF is typically used for its applications in object recognition, where the feature descriptor is used to facilitate a match between what is observed and some known set of feature points and descriptors. The descriptor largely serves as a way of discriminating against false positives. In terms of using SURF for fusion, the detected feature points will be matched across two images with the assumption that the subject is the same and that the images do contain spatially coherent matches. Given this assumption, it can be concluded that the feature descriptor is not necessary for alignment, only the feature points computed using Hessian determinants.

1.3.1 Computation of Hessian Determinants

The Hessian determinant is the determinant of a Hessian matrix, which is a matrix composed of the spatial partial second derivatives of a function $f : \mathbb{R}^n \rightarrow \mathbb{R}$. It is of the general form shown in Equation 3. In the image domain, $f : \mathbb{R}^2 \rightarrow \mathbb{R}$. The particular form of the Hessian matrix in \mathbb{R}^2 with dimensions x_1 and x_2 is shown in Equation 4.

$$H = \begin{bmatrix} \frac{\partial^2 f}{\partial x_1^2} & \frac{\partial^2 f}{\partial x_1 x_2} & \dots & \frac{\partial^2 f}{\partial x_1 \partial x_n} \\ \frac{\partial^2 f}{\partial x_2 \partial x_1} & \frac{\partial^2 f}{\partial x_2^2} & \dots & \frac{\partial^2 f}{\partial x_2 \partial x_2} \\ \vdots & \vdots & \ddots & \vdots \\ \frac{\partial^2 f}{\partial x_n \partial x_1} & \frac{\partial^2 f}{\partial x_n \partial x_2} & \dots & \frac{\partial^2 f}{\partial x_n^2} \end{bmatrix} \quad (3)$$

$$H = \begin{bmatrix} \frac{\partial^2 f}{\partial x_1^2} & \frac{\partial^2 f}{\partial x_1 x_2} \\ \frac{\partial^2 f}{\partial x_2 x_1} & \frac{\partial^2 f}{\partial x_2^2} \end{bmatrix} \quad (4)$$

The second order derivative used can be computed via convolution of the Gaussian second order derivative at any point, x , in the image. The formulas for the Gaussian second order derivative for each partial with respect to x_1^2 , $x_1 x_2$ and x_2^2 can be seen in Equations 5, 6, and 7 respectively.

$$\frac{\partial^2 G(x_1, x_2, \sigma)}{\partial^2 x_1} = (-1 + \frac{x_1^2}{\sigma^2}) \frac{e^{-\frac{x_1^2+x_2^2}{2\sigma^2}}}{2\pi\sigma^4} \quad (5)$$

$$\frac{\partial^2 G(x_1, x_2, \sigma)}{\partial x_1 x_2} = \frac{x_1 x_2}{2\pi\sigma^6} e^{-\frac{x_1^2+x_2^2}{2\sigma^2}} \quad (6)$$

$$\frac{\partial^2 G(x_1, x_2, \sigma)}{\partial^2 x_2} = (-1 + \frac{x_2^2}{\sigma^2}) \frac{e^{-\frac{x_1^2+x_2^2}{2\sigma^2}}}{2\pi\sigma^4} \quad (7)$$

3D surface plots of these equations where $\sigma = 1$ can be seen in Figure 5. In order to compute these functions quickly, SURF approximates them with cropped, discrete, kernels.

The Gaussian second order derivatives can be approximated as 9×9 kernels with $\sigma = 1.2$. These kernels can be seen in Figure 6. By adjusting σ , Hessian determinants can be computed in different scale spaces. This is a concept that SURF draws from the Scale-Invariant Feature Transform (SIFT) from Lowe et al [7].

By detecting features in different scale spaces, SIFT and by proxy, SURF are robust to changes in scale. SIFT accomplished this by downsampling the image to detect at lower order scale spaces. SURF improved on this approach for speed by instead scaling the kernel.

Another speed optimization presented in SURF takes advantage of the form of the discrete kernels. Since the approximated kernels are composed of rectangles of constant value, they can be decomposed into a set of box filters. Box filters can be computed quickly with the use of integral images. The general form of the integral image of an $M \times N$ image I is

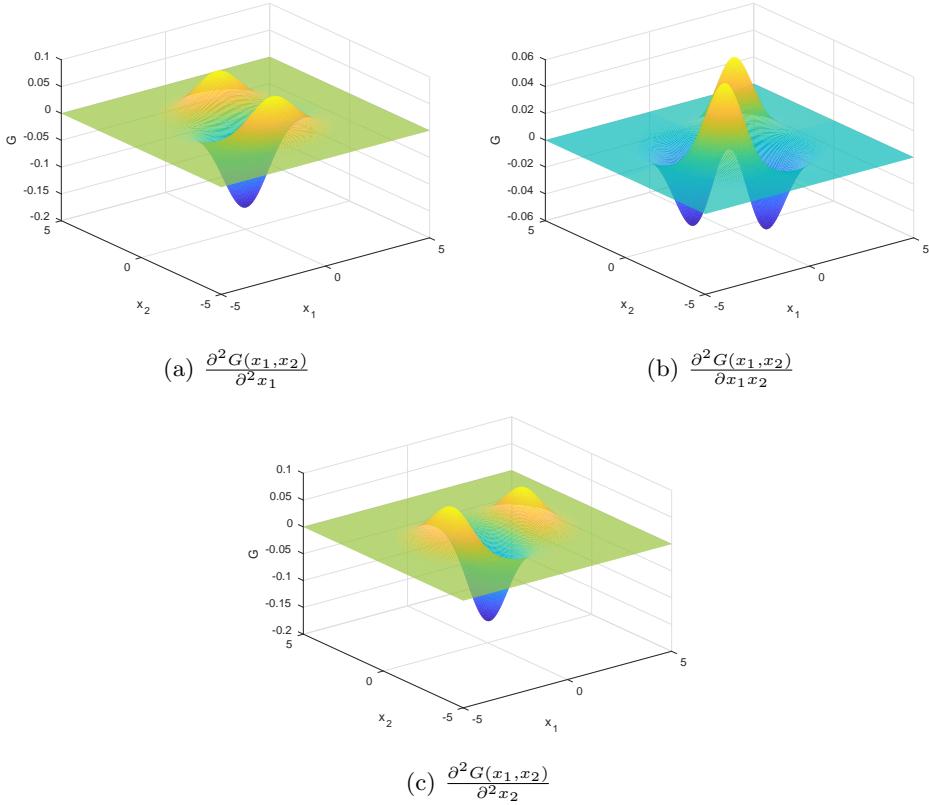


Figure 5: 3D surface plots of the Gaussian second order derivative functions where $\sigma = 1$

shown in Equation 8.

$$\int I = \begin{bmatrix} I(0,0) & \sum_{m=0}^1 I(0,m) & \dots & \sum_{m=0}^M I(0,m) \\ \sum_{n=0}^1 I(n,0) & \sum_{n=0}^1 \sum_{m=0}^1 I(n,m) & & \vdots \\ \vdots & & \ddots & \vdots \\ \sum_{n=0}^N I(n,0) & \dots & \dots & \sum_{n=0}^N \sum_{m=0}^M I(n,m) \end{bmatrix} \quad (8)$$

Integral images decrease the computational complexity of finding the sum of an area in the input image. The computational complexity of strict kernel convolution at each point scales with the size of the kernel and is of the order $O(N^2)$ where the kernel is $N \times N$. A box filter can be decomposed into finding the sum of an area in the image and scaling it. Finding the sum from the integral image can be performed in $O(1)$ as shown in Equation 9.

$$\sum_{n=w}^y \sum_{m=x}^z = \int I(y, z) - \int I(w-1, z) - \int I(y, x-1) + \int I(w-1, x-1) \quad (9)$$

This makes the computation of the Hessian matrix scale only in terms of the image size,

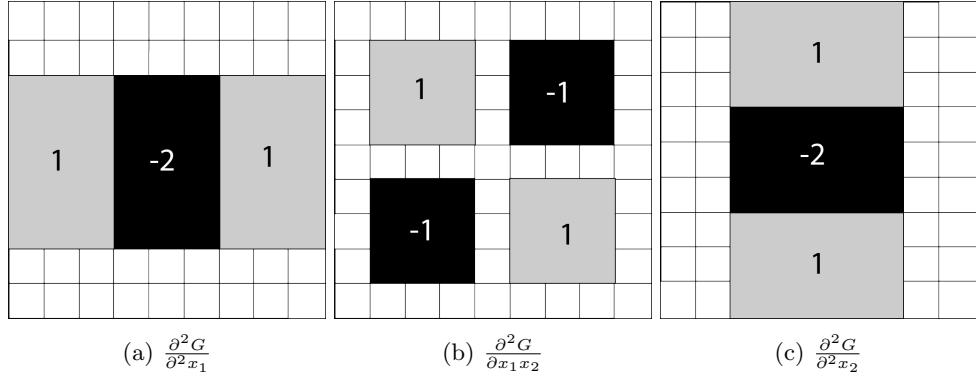


Figure 6: 9×9 discrete approximations of the Gaussian second order derivative with $\sigma = 1.2$

yielding no additional penalty for operating on different scale spaces.

1.3.2 SURF Implementations for FPGA Devices

The relatively low computational complexity makes SURF a popular choice for FPGA applications. Battezzati et al. present an architecture using accumulators for computing the integral image pipelined through the Hessian computation and storing detected feature points in a FIFO cache [1]. These are matched against a stored set of feature points. Chen et al. present improvements on this approach by parallelizing the computation of different scale spaces [5]. The implementation in this thesis follows these approaches with some additional improvements for speed based on the use case of matching against another image rather than a stored set of features.

1.4 Iterative Closest Point Algorithm

1.5 Singular Value Decomposition (SVD)

1.5.1 SVD Implementations for FPGA Devices

2 Implementation

2.1 Streaming Kernel Operators

2.1.1 Hessian Kernel with Accumulator

2.1.2 Box Kernel Approximation of Single Level Laplacian Pyramid

2.2 Computation of Transform from Detected Features

2.3 Application of Transform to Real-Time Data

3 Results

References

- [1] N. Battezzati, S. Colazzo, M. Maffione, and L. Senepa. SURF algorithm in FPGA: A novel architecture for high demanding industrial applications. In *2012 Design, Automation Test in Europe Conference Exhibition (DATE)*, pages 161–162, March 2012.
- [2] Herbert Bay, Tinne Tuytelaars, and Luc Van Gool. Surf: Speeded up robust features. *Computer vision–ECCV 2006*, pages 404–417, 2006.
- [3] P. Burt and E. Adelson. The Laplacian Pyramid as a Compact Image Code. *IEEE Transactions on Communications*, 31(4):532–540, April 1983.
- [4] S. Chappell, A. Macarthur, D. Preston, D. Olmstead, B. Flint, and C. Sullivan. Exploiting Real-time FPGA Based Adaptive Systems Technology for Real-time Sensor Fusion in Next Generation Automotive Safety Systems. In *The IEE Seminar on Target Tracking: Algorithms and Applications 2006 (Ref. No. 2006/11359)*, pages 61–68, March 2006.
- [5] W. Chen, S. Ding, Z. Chai, D. He, W. Zhang, G. Zhang, Q. Peng, and W. Luo. FPGA-Based Parallel Implementation of SURF Algorithm. In *2016 IEEE 22nd International Conference on Parallel and Distributed Systems (ICPADS)*, pages 308–315, December 2016.
- [6] Hui Li, B. S. Manjunath, and S. K. Mitra. Multi-sensor image fusion using the wavelet transform. In *Proceedings of 1st International Conference on Image Processing*, volume 1, pages 51–55 vol.1, November 1994.
- [7] David G. Lowe. Distinctive image features from scale-invariant keypoints. *International journal of computer vision*, 60(2):91–110, 2004.
- [8] Wencheng Wang and Faliang Chang. A Multi-focus Image Fusion Method Based on Laplacian Pyramid. *Journal of Computers*, 6(12), December 2011.