```
           0: j main
it_handler 4: lw $t0, 96($zero)
           8: lw $t1, 0($t0)
          12: lw $t0, 104($zero)
          16: sw $t1, 0($t0)
          20: eret
main      24: lw $s0, 108($zero)
          28: mtc0 $12, $s0
          32: lw $s0, 100($zero)
          36: lw $s1, 112($zero)
          40: sw $s1, 0($s0)
loop      44: lw $s0, 104($zero)
          48: lw $s1, 116($zero)
          52: sw $s1, 0($s0)
          56: lw $s1, 120($zero)
          60: sw $s1, 0($s0)
          64: lw $s1, 124($zero)
          68: sw $s1, 0($s0)
          72: sw $s1, 0($s0)
          76: lw $s1, 128($zero)
          80: sw $s1, 0($s0)
          84: lw $s1, 132($zero)
          88: sw $s1, 0($s0)
          92: j loop
data      96: 0xfffffffe ; keyboard data address
         100: 0xffffffff ; keyboard control & status address
         104: 0xfffffffd ; tty address
         108: 0x00000001 ; to initialize status register in supervisor mode with interrupts enable
         112: 0x00000002 ; to allow interrupt request from keyboard controler
         116: 0x00000048 ; 'H'
         120: 0x00000045 ; 'E'
         124: 0x0000004c ; 'L'
         128: 0x0000004f ; 'O'
         132: 0x0000000a ; '\n'

           0: 0x08000006
           4: 0x8c080060
           8: 0x8d090000
          12: 0x8c080068
          16: 0xad090000
          20: 0x82000018
          24: 0x8c11006c
          28: 0x40916000
          32: 0x8c110064
          36: 0x8c120070
          40: 0xae320000
          44: 0x8c110068
          48: 0x8c120074
          52: 0xae320000
          56: 0x8c120078
          60: 0xae320000
          64: 0x8c12007c
          68: 0xae320000
          72: 0xae320000
          76: 0x8c120080
          80: 0xae320000
          84: 0x8c120084
          88: 0xae320000
          92: 0x0800000b
          96: 0xfffffffe
         100: 0xffffffff
         104: 0xfffffffd
```

```
108: 0x00000001
112: 0x00000002
116: 0x00000048
120: 0x00000045
124: 0x0000004c
128: 0x0000004f
132: 0x0000000a
```