SNES F411 CIC Pinout by [Jonathon W. Donaldson (jwdonal)](#) (special thanks to [Martin Korth (nocash)](#))

Designator	Name	Desc	SOP65P780-18M	Type	Owner	Show	Number	Name
1	P00	Data (This is an output immediately following reset for the initial random seed transfer, later on however the direction switches randomly between input/output)	1	I/O	1	True	True	P00
2	P01	Data (This is an input immediately following reset for the initial random seed transfer, later on however the direction switches randomly between input/output)	2	I/O	1	True	True	P01
3	P02	Random	3	Input	1	True	True	P02
4	P03	Mode (VCC=Console/Lock, GND=Cartridge/Key)	4	Input	1	True	True	P03
5	NC		5	Passive	1	True	True	NC
6	CL2	Not Connected	6	Passive	1	True	True	CL2
7	CL1	Clock (3.072MHz) (from APU)	7	Input	1	True	True	CL1
8	RES	From Reset button	8	Input	1	True	True	RES
9	GND	Supply	9	Power	1	True	True	GND
10	P10	/RESET (to CPU, PPU, APU, etc)	10	Output	1	True	True	P10
11	P11	START	11	Output	1	True	True	P11
12	P12	NC	12	Passive	1	True	True	P12
13	P13	NC	13	Passive	1	True	True	P13
14	NC		14	Passive	1	True	True	NC
15	P20	NC	15	Passive	1	True	True	P20
16	P21	NC	16	Passive	1	True	True	P21
17	P22	NC	17	Passive	1	True	True	P22
18	VCC	Supply	18	Passive	1	True	True	VCC