SNES Cartridge Slot Pinout by [Jonathon W. Donaldson (jwdonal)](#) (special thanks to [Martin Korth (nocash)](#))

Designator	Name	Desc	Type	Owner	Show	Number	Name
1	MCK	Master Clock (21.47727MHz) (used by SGB1 and MarioChip1)	Input	1	True	True	MCK
2	EXPAND	Used to let cartridge know if something is connected to the expansion port (10k Pull-up) (from EXT port)	Passive	1	True	True	EXPAND
3	PA6	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA6
4	P\A\R\D\	5A22 B-Bus Read Strobe (Active-Low)	Input	1	True	True	P\A\R\D\
5	GND	Supply	Power	1	True	True	GND
6	CA11	5A22 A-Bus Address	Input	1	True	True	CA11
7	CA10	5A22 A-Bus Address	Input	1	True	True	CA10
8	CA9	5A22 A-Bus Address	Input	1	True	True	CA9
9	CA8	5A22 A-Bus Address	Input	1	True	True	CA8
10	CA7	5A22 A-Bus Address	Input	1	True	True	CA7
11	CA6	5A22 A-Bus Address	Input	1	True	True	CA6
12	CA5	5A22 A-Bus Address	Input	1	True	True	CA5
13	CA4	5A22 A-Bus Address	Input	1	True	True	CA4
14	CA3	5A22 A-Bus Address	Input	1	True	True	CA3
15	CA2	5A22 A-Bus Address	Input	1	True	True	CA2
16	CA1	5A22 A-Bus Address	Input	1	True	True	CA1
17	CA0	5A22 A-Bus Address	Input	1	True	True	CA0
18	I\R\Q\	5A22 Interrupt Request (Active-Low)	Output	1	True	True	I\R\Q\
19	D0	5A22 Data Bus	I/O	1	True	True	D0
20	D1	5A22 Data Bus	I/O	1	True	True	D1
21	D2	5A22 Data Bus	I/O	1	True	True	D2
22	D3	5A22 Data Bus	I/O	1	True	True	D3
23	C\P\U\R\D\	5A22 A-Bus Read Strobe (Active-Low)	Input	1	True	True	C\P\U\R\D\
24	CIC0	Lockout Chip	I/O	1	True	True	CIC0
25	CIC2	Lockout Chip	Input	1	True	True	CIC2
26	R\E\S\E\T\	CPU Reset (Active-Low) (from console)	Input	1	True	True	R\E\S\E\T\
27	+5V	Supply	Power	1	True	True	+5V
28	PA0	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA0
29	PA2	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA2
30	PA4	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA4
31	SOUND-L	Cartridge Left Analog Audio (to console mixer)	Output	1	True	True	SOUND-L
32	R\A\M\S\E\L\	WRAM Select (from 5A22)	Input	1	True	True	R\A\M\S\E\L\
33	REFRESH	DRAM Refresh (from 5A22)	Input	1	True	True	REFRESH
34	PA7	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA7

Designator	Name	Desc	Type	Owner	Show	Number	Name
35	P\A\W\R\	5A22 B-Bus Write Strobe (Active-Low)	Input	1	True	True	P\A\W\R\
36	GND	Supply	Power	1	True	True	GND
37	CA12	5A22 A-Bus Address	Input	1	True	True	CA12
38	CA13	5A22 A-Bus Address	Input	1	True	True	CA13
39	CA14	5A22 A-Bus Address	Input	1	True	True	CA14
40	CA15	5A22 A-Bus Address	Input	1	True	True	CA15
41	CA16	5A22 A-Bus Address	Input	1	True	True	CA16
42	CA17	5A22 A-Bus Address	Input	1	True	True	CA17
43	CA18	5A22 A-Bus Address	Input	1	True	True	CA18
44	CA19	5A22 A-Bus Address	Input	1	True	True	CA19
45	CA20	5A22 A-Bus Address	Input	1	True	True	CA20
46	CA21	5A22 A-Bus Address	Input	1	True	True	CA21
47	CA22	5A22 A-Bus Address	Input	1	True	True	CA22
48	CA23	5A22 A-Bus Address	Input	1	True	True	CA23
49	R\O\M\S\E\L\	ROM Select (from 5A22)	Input	1	True	True	R\O\M\S\E\L\
50	D4	5A22 Data Bus	I/O	1	True	True	D4
51	D5	5A22 Data Bus	I/O	1	True	True	D5
52	D6	5A22 Data Bus	I/O	1	True	True	D6
53	D7	5A22 Data Bus	I/O	1	True	True	D7
54	C\P\U\W\R\	5A22 A-Bus Write Strobe (Active-Low)	Input	1	True	True	C\P\U\W\R\
55	CIC1	Lockout Chip	I/O	1	True	True	CIC1
56	CIC3	Lockout Chip (3.072MHz)	Output	1	True	True	CIC3
57	SYSCK	5A22 System Clock (most likely the current memory access cycle clock, i.e. 3.58MHz, 2.68MHz, or 1.79MHz)	Input	1	True	True	SYSCK
58	+5V	Supply	Power	1	True	True	+5V
59	PA1	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA1
60	PA3	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA3
61	PA5	5A22 B-Bus Address ($2100-$21FF range in banks $00-$3F/$80-$BF)	Input	1	True	True	PA5
62	SOUND-R	Cartridge Right Analog Audio (to console mixer)	Output	1	True	True	SOUND-R