SNES 32Kx8 SRAM Pinout by [Jonathon W. Donaldson (jwdonal)](#) (special thanks to [Martin Korth (nocash)](#))

Designator	Name	Desc	S21_M	Type	Owner	Show	Number	Name
1	A14	Address Bus (from S-DSP)	1	Input	1	True	True	A14
2	A12	Address Bus (from S-DSP)	2	Input	1	True	True	A12
3	A7	Address Bus (from S-DSP)	3	Input	1	True	True	A7
4	A6	Address Bus (from S-DSP)	4	Input	1	True	True	A6
5	A5	Address Bus (from S-DSP)	5	Input	1	True	True	A5
6	A4	Address Bus (from S-DSP)	6	Input	1	True	True	A4
7	A3	Address Bus (from S-DSP)	7	Input	1	True	True	A3
8	A2	Address Bus (from S-DSP)	8	Input	1	True	True	A2
9	A1	Address Bus (from S-DSP)	9	Input	1	True	True	A1
10	A0	Address Bus (from S-DSP)	10	Input	1	True	True	A0
11	D0	Data Bus (from S-DSP)	11	I/O	1	True	True	D0
12	D1	Data Bus (from S-DSP)	12	I/O	1	True	True	D1
13	D2	Data Bus (from S-DSP)	13	I/O	1	True	True	D2
14	GND	Supply	14	Power	1	True	True	GND
15	D3	Data Bus (from S-DSP)	15	I/O	1	True	True	D3
16	D4	Data Bus (from S-DSP)	16	I/O	1	True	True	D4
17	D5	Data Bus (from S-DSP)	17	I/O	1	True	True	D5
18	D6	Data Bus (from S-DSP)	18	I/O	1	True	True	D6
19	D7	Data Bus (from S-DSP)	19	I/O	1	True	True	D7
20	C\S\	Chip-Select (from S-DSP)	20	Input	1	True	True	C\S\
21	A10	Address Bus (from S-DSP)	21	Input	1	True	True	A10
22	O\E\	Output-Enable (from S-DSP)	22	Input	1	True	True	O\E\
23	A11	Address Bus (from S-DSP)	23	Input	1	True	True	A11
24	A9	Address Bus (from S-DSP)	24	Input	1	True	True	A9
25	A8	Address Bus (from S-DSP)	25	Input	1	True	True	A8
26	A13	Address Bus (from S-DSP)	26	Input	1	True	True	A13
27	W\E\	Write-Enable (from S-DSP)	27	Input	1	True	True	W\E\
28	VCC	Supply	28	Power	1	True	True	VCC