INFO: [VRFC 10-2263] Analyzing Verilog file "J:/Documents/323\_lab9/323\_lab9.srcs/sources\_1/new/mips\_final.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module mips\_final

WARNING: [VRFC 10-526] concatenation with unsized literal; will interpret as 32 bits [J:/Documents/323\_lab9/323\_lab9.srcs/sources\_1/new/mips\_final.v:163]

WARNING: [VRFC 10-756] identifier jump\_EX is used before its declaration [J:/Documents/323\_lab9/323\_lab9.srcs/sources\_1/new/mips\_final.v:247]

WARNING: [VRFC 10-756] identifier jump\_EX is used before its declaration [J:/Documents/323\_lab9/323\_lab9.srcs/sources\_1/new/mips\_final.v:248]

WARNING: [VRFC 10-756] identifier jump\_EX is used before its declaration [J:/Documents/323\_lab9/323\_lab9.srcs/sources\_1/new/mips\_final.v:249]

INFO: [VRFC 10-2263] Analyzing Verilog file "J:/Downloads/tb\_generic\_mips.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module tb\_generic\_pipeline

INFO: [VRFC 10-2263] Analyzing Verilog file "J:/Documents/323\_lab9/323\_lab9.sim/sim\_1/behav/glbl.v" into library xil\_defaultlib

INFO: [VRFC 10-311] analyzing module glbl