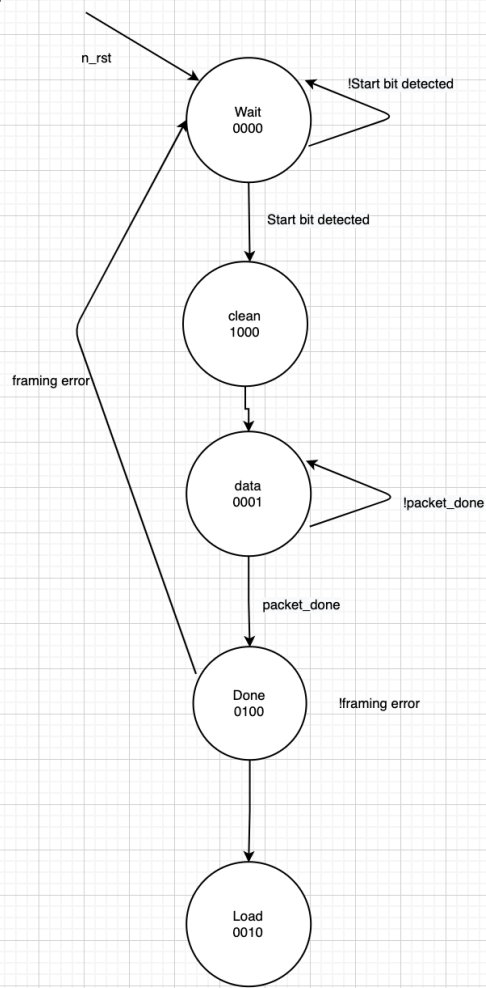


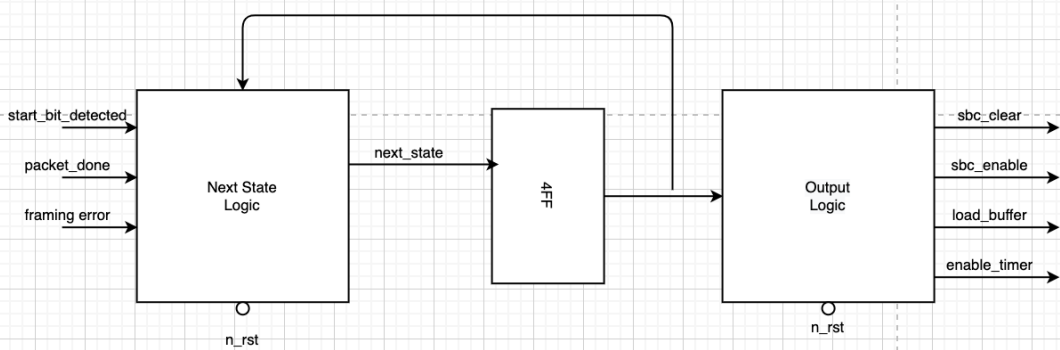
Laula Huang

State Transition diagram

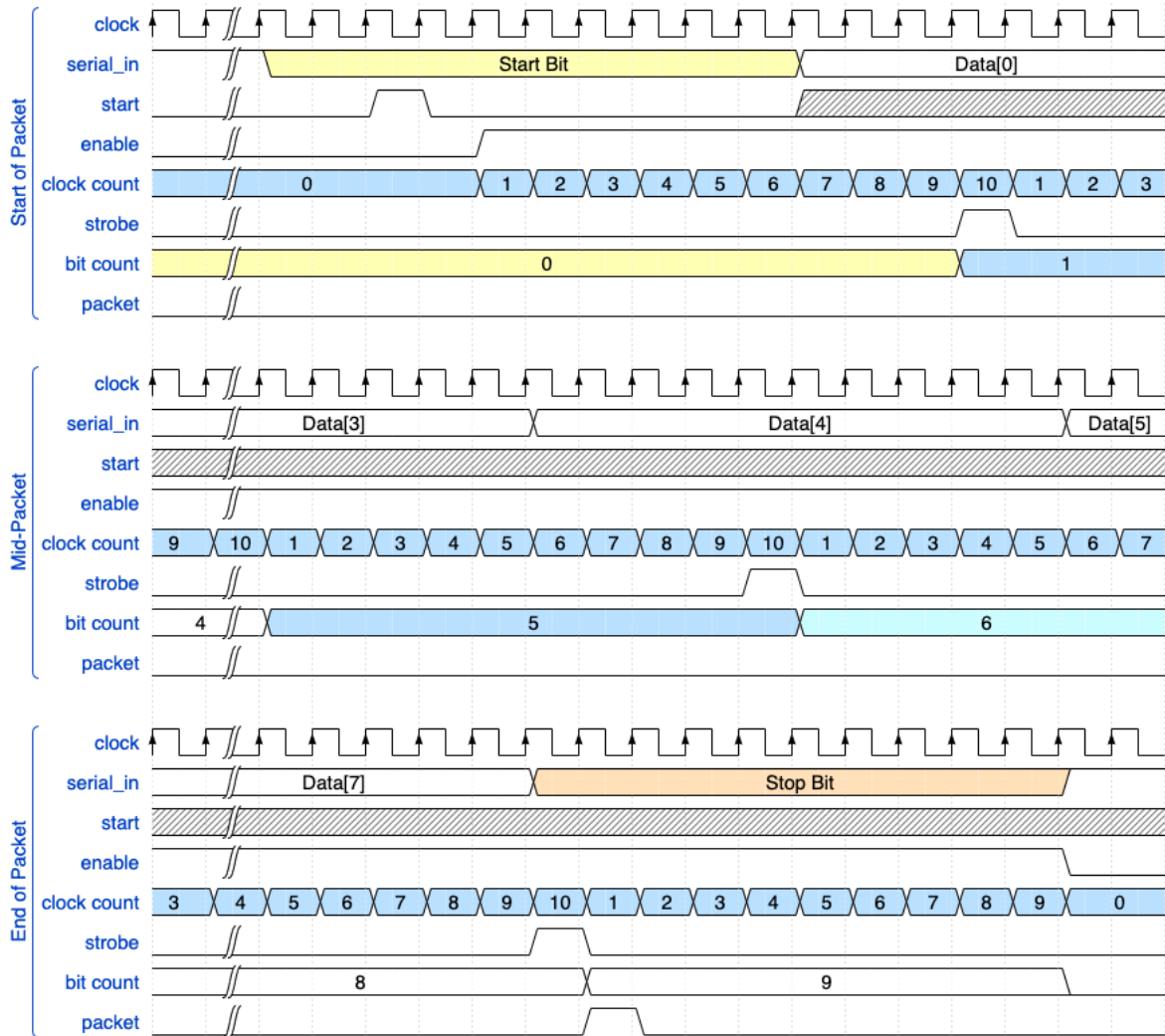
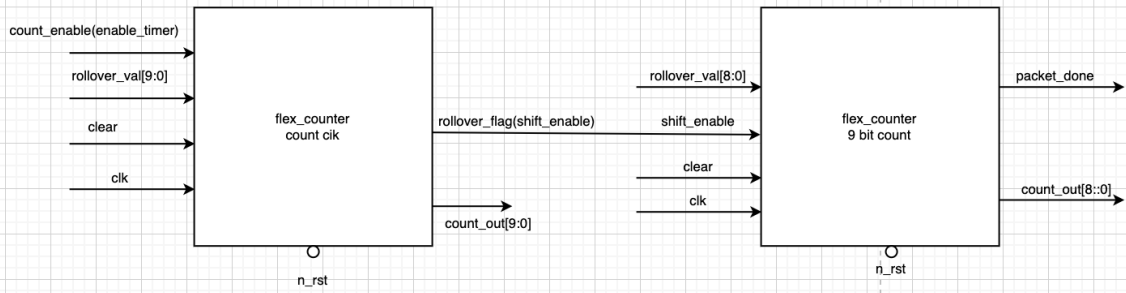


Output = [sbc_clear, sbc_enable, load_buffer, enable_timer]

RTL diagram



Block diagram



```

{ signal: [
  ['Start of Packet',
    {name: 'clock', wave: 'P|.....'},
    {name: 'serial_in', wave: '1|3.....2.....', data:['Start Bit','Data[0]']},
    {name: 'start', wave: '0...10.....x.....'},
    {name: 'enable', wave: '0|...1.....'},
    {name: 'clock count', wave: '5|...555555555555', data:['0','1','2','3','4','5','6','7','8','9','10','1','2','3']},
    {name: 'strobe', wave: '0|.....10.....'},
    {name: 'bit count', wave: '3|.....5.....', data:['0','1']},
    {name: 'packet', wave: '0|.....'}]],
  [],
  ['Mid-Packet',
    {name: 'clock', wave: 'P|.....'},
    {name: 'serial_in', wave: '2|.....2.....2.....', data:['Data[3]','Data[4]','Data[5]']},
    {name: 'start', wave: 'x|.....'},
    {name: 'enable', wave: '1|.....'},
    {name: 'clock count', wave: '5555555555555555', data:['9','10','1','2','3','4','5','6','7','8','9','10','1','2','3','4','5','6','7']},
    {name: 'strobe', wave: '0|.....10.....'},
    {name: 'bit count', wave: '2|5.....6.....', data:['4','5','6']},
    {name: 'packet', wave: '0|.....'}]],
  [],
  ['End of Packet',
    {name: 'clock', wave: 'P|.....'},
    {name: 'serial_in', wave: '2|.....4.....1.', data:['Data[7]','Stop Bit']},
    {name: 'start', wave: 'x|.....'},
    {name: 'enable', wave: '1|.....0.'},
    {name: 'clock count', wave: '5555555555555555', data:['3','4','5','6','7','8','9','10','1','2','3','4','5','6','7','8','9','0']},
    {name: 'strobe', wave: '0|.....10.....'},
    {name: 'bit count', wave: '2|.....2.....0.', data:['8','9']},
    {name: 'packet', wave: '0|.....10.....'}]]
}

```