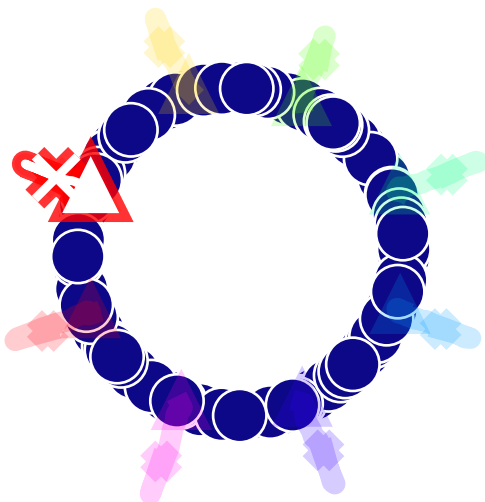


MemoryPro

Response dynamics

MemoryAnti
Memory State PC2



MemoryPro
Memory State PC1