

BC807 ... BC808 **SMD General Purpose PNP Transistors SMD Universal-PNP-Transistoren**

= -800 mA $h_{FE} \sim 160/250/400 P_{tot} = 310 \text{ mW}$

 $V_{CES} = -30 ... -50 V$

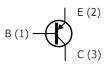
 $T_{jmax} = 150$ °C

Version 2021-07-05

SOT-23 TO-236



SPICE Model & STEP File 1)



Marking Code See below | Siehe unten

HS Code 85412100

Typical Applications

Signal processing Switching **Amplification** Commercial grade Suffix -Q: AEC-Q101 compliant 1) Suffix -AQ: in AEC-Q101 qualification 1)

Features

General Purpose Three current gain groups Compliant to RoHS (w/o exemp.), REACH, Conflict Minerals 1)

Mechanical Data 1)

Taped and reeled Weight approx. Case material Solder & assembly conditions **Typische Anwendungen**

Signalverarbeitung Schalten Verstärken Standardausführung 1) Suffix -Q: AEC-Q101 konform 1) Suffix -AQ: in AEC-Q101 Qualifizierung 1)

> **Besonderheiten** Universell anwendbar Drei Stromverstärkungsklassen Konform zu RoHS (ohne Ausn.),

REACH, Konfliktmineralien 1

Mechanische Daten 1)

3000 / 7" Gegurtet auf Rolle 0.01 g Gewicht ca.

UL 94V-0 Gehäusematerial

260°C/10s Löt- und Einbaubedingungen

MSL = 1

1	Complementary NPN transistors			
Mar	Komplementäre NPN-Transistoren			
BC807-16 = 5A or 5CR BC807-25/-Q = 5B or 5CS BC807-25-AQ = 5CS BC807-40/-Q = 5C or 5CT BC807-40-AQ = 5CT	BC808-16 = 5E or 5CR BC808-25 = 5F or 5CS BC808-40 = 5G or 5CT	BC817 BC818		

Maximum ratings 2)

Grenzwerte 2)

			BC807	BC808	
Collector-Emitter-voltage – Kollektor-Emitter-Spannung	E-B short	- V _{CES}	50 V	30 V	
Collector-Emitter-voltage – Kollektor-Emitter-Spannung	B open	- V _{CEO}	45 V	25 V	
Emitter-Base-voltage – Emitter-Basis-Spannung	C open	- V _{EBO}	5 V		
Power dissipation – Verlustleistung		P _{tot}	310 mW ³)		
Collector current – Kollektorstrom	DC	- I _C	800 mA		
Peak Collector current – Kollektor-Spitzenstrom		- I _{CM}	1 A		
Peak Base current – Basis-Spitzenstrom			200 mA		
Junction temperature – Sperrschichttemperatur Storage temperature – Lagerungstemperatur		T _j T _s	-55+150°C -55+150°C		

Please note the <u>detailed information on our website</u> or at the beginning of the data book Bitte beachten Sie die detaillierten Hinweise auf unserer Internetseite bzw. am Anfang des Datenbuches

 T_A = 25°C, unless otherwise specified – T_A = 25°C, wenn nicht anders angegeben

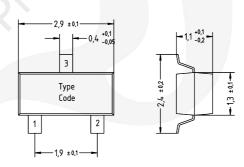
Valid, if leads are kept at ambient temperature at a distance of 2 mm from case Gültig wenn die Anschlussdrähte in 2 mm Abstand vom Gehäuse auf Umgebungstemperatur gehalten werden



Characteristics Kennwerte

	T _j = 25°C	Min.	Тур.	Max.	
DC current gain – Kollektor-Basis-Stromverhältnis ¹)					
$\label{eq:control_control_control} \begin{array}{c} \text{Group -1} \\ \text{- V}_{\text{CE}} = 1 \text{ V, - I}_{\text{C}} = 100 \text{ mA} \\ \text{Group -4} \\ \end{array}$	5 h _{FE}	100 160 250	- - -	250 400 630	
- $V_{CE} = 1 \text{ V, - } I_C = 500 \text{ mA}$	h _{FE}	40	_	_	
Collector-Emitter saturation voltage – Kollektor-Emitter-Sättigungss	pg. ²)				
- I_C = 500 mA, - I_B = 50 mA		_	_	0.7 V	
Base-Emitter saturation voltage – Basis-Emitter-Sättigungsspannung ²)					
$- I_{C} = 500 \text{ mA}, - I_{B} = 50 \text{ mA}$		_	_	1.3 V	
Base-Emitter-voltage – Basis-Emitter-Spannung ²)					
- $V_{CE} = 1 V$, - $I_C = 500 \text{ mA}$		_	_	1.2 V	
Collector-Base cutoff current – Kollektor-Basis-Reststrom					
- $V_{CB} = 20 \text{ V, (E open)}$ - $V_{CB} = 20 \text{ V, T}_j = 125^{\circ}\text{C, (E open)}$	- I _{CB0}	(R)	- -	100 nA 5 μA	
Emitter-Base cutoff current – Emitter-Basis-Reststrom					
- V _{EB} = 4 V, (C open)	- I _{EB0}	_	_	100 nA	
Gain-Bandwidth Product – Transitfrequenz	(
- V_{CE} = 5 V, - I_C = 10 mA, f = 50 MHz		7 –	100 MHz	-	
Collector-Base Capacitance – Kollektor-Basis-Kapazität					
- V_{CB} = 10 V, - I_E = i_e = 0, f = 1 MHz	Ссво	_	12 pF	-	
Typical thermal resistance junction to ambient Typischer Wärmewiderstand Sperrschicht – Umgebung	R _{thA}	420 K/W ²)			

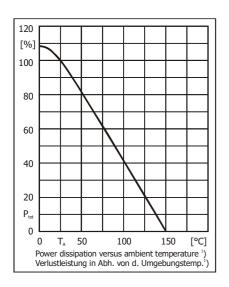
Dimensions - Maße [mm]

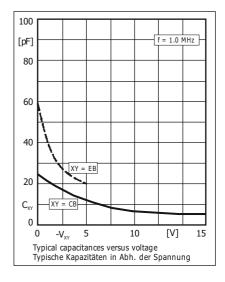


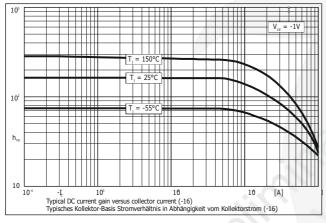
2 © Diotec Semiconductor AG http://www.diotec.com/

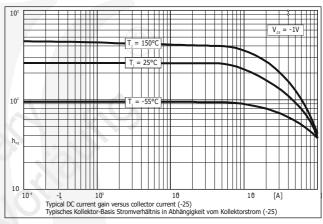
Tested with pulses t_p = 300 μ s, duty cycle \leq 2% - Gemessen mit Impulsen t_p = 300 μ s, Schaltverhältnis \leq 2% Mounted on P.C. board with 3 mm² copper pad at each terminal Montage auf Leiterplatte mit 3 mm² Kupferbelag (Lötpad) an jedem Anschluss

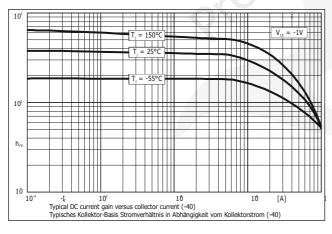


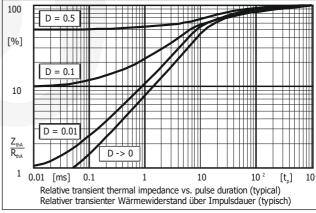












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