# **Aanjhan Ranganathan**

## **Education**

ETH Zurich Zurich, Switzerland 2010–present

École Polytechnique Fédérale de Lausanne

Master of Science, 5.62/6 2008–2010

SSN College of Engineering (Anna University)

**Doctoral Thesis** (expected to graduate in 05/2016)

title: Physical-layer Techniques for Secure Proximity Verification and Localization

supervisor: Prof. Dr. Srdjan Capkun

Bachelor of Engineering, 82/100

description: Analysed and proposed novel architectures for secure localization and proximity verification.

Designed and implemented a spoofing resilient GPS receiver.

### Master thesis

title: Snoop Filtering for Low Power Embedded Multiprocessor Systems

supervisors: Prof. Dr. Paolo lenne, Prof. Dr. Philip Brisk, Dr. Theo Kluter

description: Quantified the benefits of using cache snoop filters to reduce memory subsystem energy in

embedded multiprocessor systems.

## **Experience**

Industry.....

## **Robert Bosch Engineering and Business Solutions**

Bengaluru, India

Lausanne, Switzerland

Chennai, India

2001-2005

Senior Software Engineer

June 2005-June 2008

Internships.....

Processor Architecture Laboratory, EPFL Lausanne, Switzerland

Research Assistant

March 2010-July 2010

Google Inc. (Summer of Code)

Contract Employee May 2009–August 2009

Teaching.....

ETH Zurich Zurich. Switzerland

Semester and Master theses supervision

September 2010-present

Locating tags using Unmanned Aerial Vehicles, Carl Olsson, Semester Thesis, 2015. Detection of Spoofing Attacks on Wireless Multilateration Systems, Patrick Leu (in co-operation with Armasuisse), Master Thesis, 2015. Detection of GPS Spoofing Attacks, Hildur Ólafsdóttir, Master Thesis, 2014. On the Security of Powerline Communication Systems, Baron Oldenburg, Semester project, 2012. Smart and Secure WiFi Pairing, Pascal Brogle, Masters Thesis, 2011.

ETH Zurich Zurich, Switzerland
Lectures (Masters) September 2010–present

Topics include secure localization, distance bounding, broadcast communication authentication techniques, GPS, Anti-jamming techniques

ETH Zurich Zurich, Switzerland

Teaching Assistant September 2010–present

Design of Digital Circuits (Bachelors), Security of Wireless Networks laboratory (Masters)

EPFL Lausanne, Switzerland

Teaching Assistant September 2009–December 2009

Hardware System Modelling (Masters), Semi-custom EDA based VLSI Design (Masters)

## **Publications**

[1] <u>Aanjhan Ranganathan</u>, Boris Danev, and Srdjan Capkun. Proximity Verification for Contactless Access Control and Authentication Systems. In *Proceedings of the 31st Annual Computer Security Applications Conference (ACSAC)*, 2015.

[2]Ramya Jayaram Masti, Devendra Rai, <u>Aanjhan Ranganathan</u>, Christian Müller, Lothar Thiele, and Srdjan Capkun. Thermal Covert Channels on Multi-core Platforms. In *Proceedings of the 24th USENIX Security Symposium (USENIX Security 15)*, 2015.

[3] Der-Yeuan Yu, Aanjhan Ranganathan, Ramya Jayaram Masti, Claudio Soriente, and Srdjan Capkun. W-SPS: Designing a Wide-Area Secure Positioning System. Cryptology ePrint Archive, Report 2015/230, 2015.

[4]Der-Yeuan Yu, <u>Aanjhan Ranganathan</u>, Thomas Locher, Srdjan Capkun, and David Basin. Short paper: Detection of GPS Spoofing Attacks in Power Grids. In *Proceedings of the 2014 ACM conference on Security and privacy in wireless & mobile networks (WiSec)*, 2014.

[5] Aanjhan Ranganathan, Boris Danev, and Srdjan Capkun. Low-power Distance Bounding. arXiv preprint arXiv:1404.4435, 2014.

[6]Nils Ole Tippenhauer, Luka Malisa, <u>Aanjhan Ranganathan</u>, and Srdjan Capkun. On limitations of friendly jamming for confidentiality. In *Proceedings of the 34th IEEE Symposium on Security and Privacy (S&P)*, 2013.

[7]Ramya Jayaram Masti, Claudio Marforio, <u>Aanjhan Ranganathan</u>, Aurélien Francillon, and Srdjan Capkun. Enabling Trusted Scheduling in Embedded Systems. In *Proceedings of the 28th Annual Computer Security Applications Conference (ACSAC)*, 2012.

[8] <u>Aanjhan Ranganathan</u>, Nils Ole Tippenhauer, Boris Škorić, Dave Singelée, and Srdjan Capkun. Design and implementation of a terrorist fraud resilient distance bounding system. In *Proceedings of the 17th European Symposium on Research in Computer Security (ESORICS)*, 2012.

[9] <u>Aanjhan Ranganathan</u>, Ali Galip Bayrak, Theo Kluter, Philip Brisk, Edoardo Charbon, and Paolo lenne. Counting stream registers: An efficient and effective snoop filter architecture. In *Proceeding of the 18th IEEE International Conference on Embedded Computer Systems (SAMOS)*, 2012.

[10] <u>Aanjhan Ranganathan</u>, Boris Danev, Aurélien Francillon, and Srdjan Capkun. Physical-layer attacks on chirp-based ranging systems. In *Proceedings of the 5th ACM conference on Security and Privacy in Wireless and Mobile Networks (WiSec)*, 2012.

[11] <u>Aanjhan Ranganathan</u>. Adaptation of Linux and \*BSD in automotive embedded software. Technical report, SAE Technical Paper, 2007.

#### **Talks**

- o Proximity Verification for Contactless Access Control and Authentication Systems: Annual Computer Security Applications Conference, Los Angeles, USA, 2015.
- o SPREE: A Spoofing Aware GPS Receiver: University of California, Los Angeles, USA, 2015.
- Thermal Covert Channels on Multicore Platforms: Usenix Security Symposium, Washington D.C., USA, 2015.
- Design and implementation of a terrorist fraud resilient distance bounding system: European Symposium on Research in Computer Security, Pisa, Italy, 2012.
- o Counting stream registers: An efficient and effective snoop filter architecture: International Conference on Embedded Computer Systems, Samos, Greece, 2012.
- Physical-layer attacks on chirp-based ranging systems: ACM Conference on Security and Privacy of Wireless and Mobile Networks, Tucson, USA, 2012.

#### **Professional and Social Activities**

- Technical Program Committee member for "Smart Medical Devices from Lab to Clinical Practice" workshop 2015, 2016
- o Reviewer for IEEE/ACM Transactions on Networking 2015
- o Reviewer for ACM Transactions on Embedded Computing Systems (TECS 2015)
- o Reviewer for IEEE Journal of Selected Topics in Signal Processing 2014
- o Reviewer for Transactions on Emerging Telecommunications Technologies 2014
- o "Free and Open Source VLSI CAD Tools" at Jawaharlal Nehru University, Delhi [http://freed.in]
- o "Icarus Verilog and Free/Open Source EDA Tools" at Indian Institute of Technology Madras.
- o "Hybrid Electric Vehicle with Intelligent Drive Switching" at IIT Bombay [Techfest 2004]
- Organized and conducted workshops for School/College students on 'Developing Leadership Qualities' as the head of the technical division of 'PEOPLE FIRST', a youth organization.
- O Developer and maintainer of GNUSim8085, a 8085 simulator. [http://gnusim8085.sourceforge.net]
- o RTEMS Contributor http://www.rtems.com
- o Active Participation in Linux demo Days Conducted by the Chennai Linux users Group in creating Awareness on FLOSS to students in both the semi-urban and urban areas.

## Computer skills

Programming Languages: C, Verilog, VHDL, Matlab, Bash, Python, LATEX

**Tools and Platforms**: NEC V850 Emulators, Cadence and Synopsys toolchains, Software Defined Radio platforms, Xilinx and Altera FPGA platforms.

#### **Awards**

Young Scientist Award: awarded by the Indian Science Monitor in 2005

Distinguished Alumni Award: SSN Institutions, Chennai, India

#### Languages

English, Tamil (Mother tongue): Fluent

**German, Hindi**: Intermediate **French, Hungarian**: Basic