Lauria Clarke

lauriaclarke.com

EDUCATION

Parsons School of Design, The New School — New York, NY
Candidate for M.F.A. in Design and Technology — May 2023

Northeastern University — Boston, MA
B.S. Electrical Engineering — May 2017
M.S. Computer Engineering – Concentration in Computer Systems and Software

SKILLS

SW Tools and Languages: C/C++, Verilog, Python, Javascript, Linux, Git, Subversion, p5.js, Arduino
HW Tools: Xilinx FPGAs, ARM Coretex series, ESP32 series, Eagle, Soldering, Oscilloscope, Logic Analyzer
Fabrication: Basic metal and woodworking, electronic systems, fish bones, pistachio shells

WORK and RESEARCH EXPERIENCE

NanoSemi / MaxLinear — Waltham, MA
Senior ASIC Design Engineer — July 2020 - present
- Part of verification team supporting development of NanoSemi / MaxLinear's linearization IP portfolio
- Contributes to test plan development and implementation
- Responsible for verification of laboratory prototyping designs

Public Invention — Medford, MA / Austin TX
Embedded Systems Engineer — March 2020 - March 2021
- Helped create and implement the electrical design for multiple iterations of an Open Source ventilator testing and monitoring device, VentMon
- Created custom circuit board for version, T0.3 which is currently used by teams around the world

Intel — Hudson, MA
SoC Design and Verification Engineer — August 2017 - June 2019
- Was part of a cross functional team that designs and verifies Intel's SSD controller chips
- Led the integration of industry standard verification technologies into group's current project
- Was involved in group-wide effort to improve workflow process efficiency and design integrity

Ed Andrews Studio — Boston, MA / Chepachet, RI
Designer — September 2013 - December 2017
- Created electrical system and software for large, site specific installation at the Boston Sculptors Gallery (December 2017)
- Created electrical system and software for sculpture with LED lighting based on real-time weather data (Exhibition in Falmouth, MA summer of 2018)

Embedded Systems Laboratory, Northeastern University — Boston, MA
Research Assistant — August 2015 - May 2017
- Part of team that designed and implemented a new neural network architecture for computer vision applications
- Participated in academic writing within the group for research publications

PUBLICATIONS

Read, R. Clarke, L. and Mulligan, G. “VentMon: An open source inline ventilator tester and monitor.” *HardwareX*, Volume 9, e00195, April 01, 2021.