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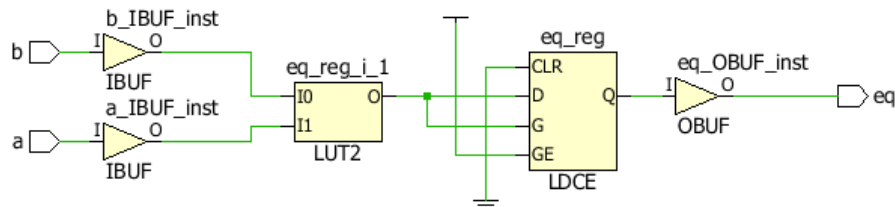
Design of Digital systems 1 TFE4141 - Assignment 1

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1 Task 1

1.1

1.2

An example where a latch is unintentionally created is when you don't assign values to all output in every branch of an if-statement.

1.3

The way to make sure that latches are not generated from combinatorial processes is to always make sure that there are no incomplete branches, or incom-

plete signal assignments.

2   Task 2

2.1