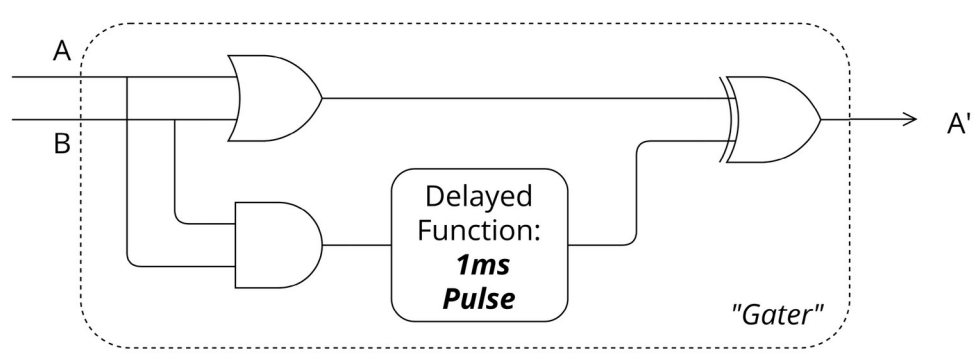
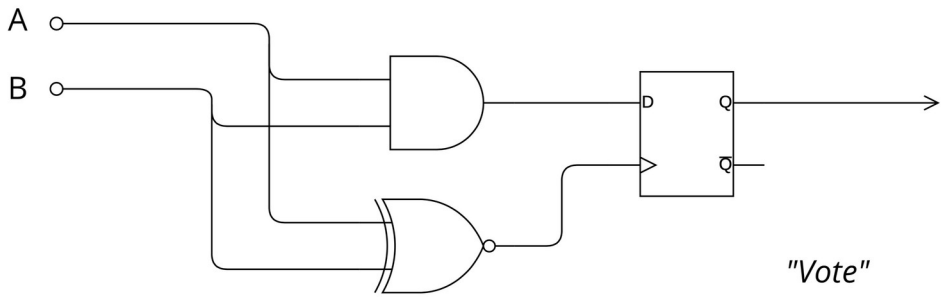


Klavis Two-Bits : Equivalent Logic Recipes on Disting NT

Category	Function	Parameter	Disting NT equivalent
Simple Logic			
	AND, NAND	N/A	Logic Algorithm
	OR, NOR	N/A	Logic Algorithm
	XOR, XNOR	N/A	Logic Algorithm
	$\sim A \& B$ [same as A or $\sim B$]	N/A Alternate :	Lua expression " $\sim a$ and b" (a NOR a) AND b
			*

Advanced Logic			
	S/R flip-flop	N/A	Logic Algorithm
	Rise to Pulse	Pulse Duration	Delayed Function (Pulse)
	Fall to Pulse	Pulse Duration	Logic (a NOR a) \rightarrow Delayed Function (Pulse)
	Edge to Pulse	Pulse Duration	Logic OR of above two
	"Gater" (Gate Merger) ["re-trigger"]	Gap Duration	Build with Logic and Delayed Pulse :
			
	"Vote"	N/A	Build with AND, XOR, and D flip-flop :
			

Other			
	Delay	Duration	Delay (Mono) algorithm
	Comparator	Threshold	Logic – use threshold + hysteresis
	Divider / Multiplier	Ratio	Clock Divider / Clock Multiplier algorithms
	Random	Max chance %	Step Sequencer with Randomize

* Request for primitive factory algorithm / settings