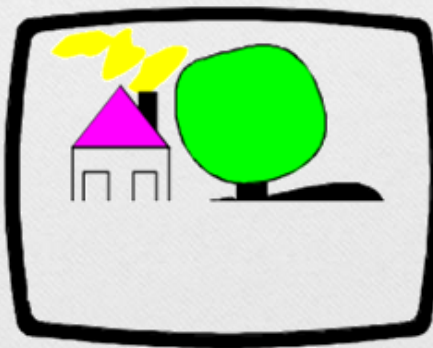
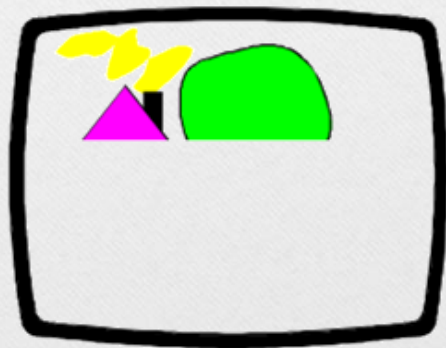
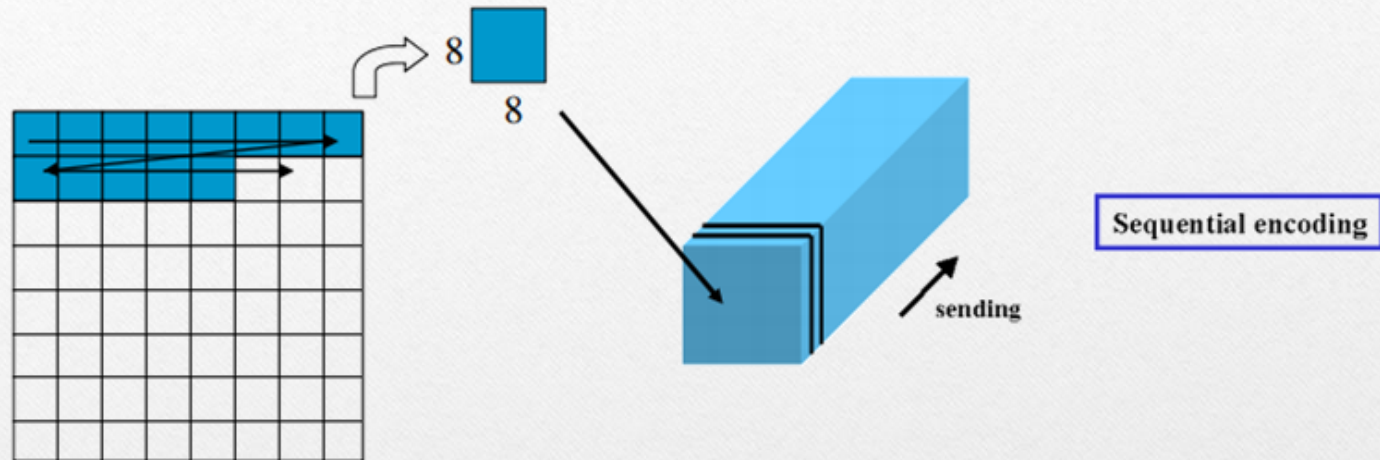


Jpeg Decoder

Baseline Sequential DCT-based

Baseline Sequential DCT-based



Baseline Sequential DCT-based Encoding Process

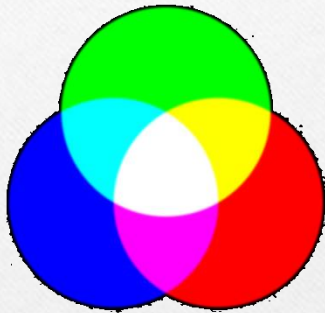
- Color Space Conversion
- Subsampling
- Partition
- Encoding Flow Control
 - Discrete Cosine Transform (DCT)
 - Quantization
 - Entropy Encoding (Huffman)



Decoding

Color Space Conversion

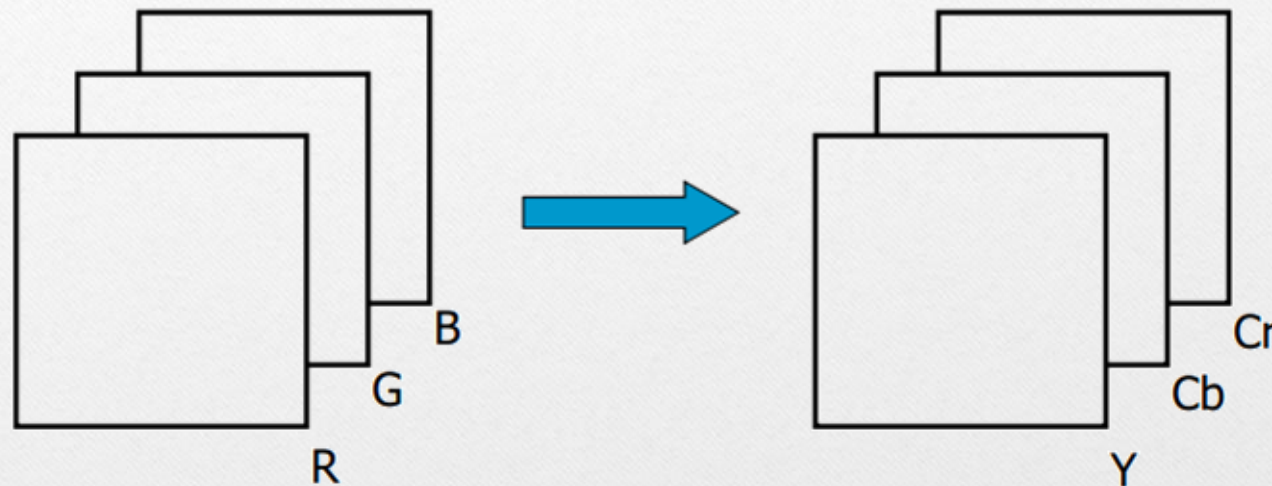
RGB



YCbCr



Color Space Conversion



Y – Luminance(亮度) Domain Cb,Cr – Chrominance(色差) Domain

$$Y = 0.299R + 0.587G + 0.144B \quad R = Y + 1.402 * (Cr - 128)$$

$$Cb = -0.168R - 0.331G - 0.449B \quad G = Y - 0.34414 * (Cb - 128) - 0.71414 * (Cr - 128)$$

$$Cr = 0.500R - 0.419G - 0.081B \quad B = Y + 1.772 * (Cb - 128)$$

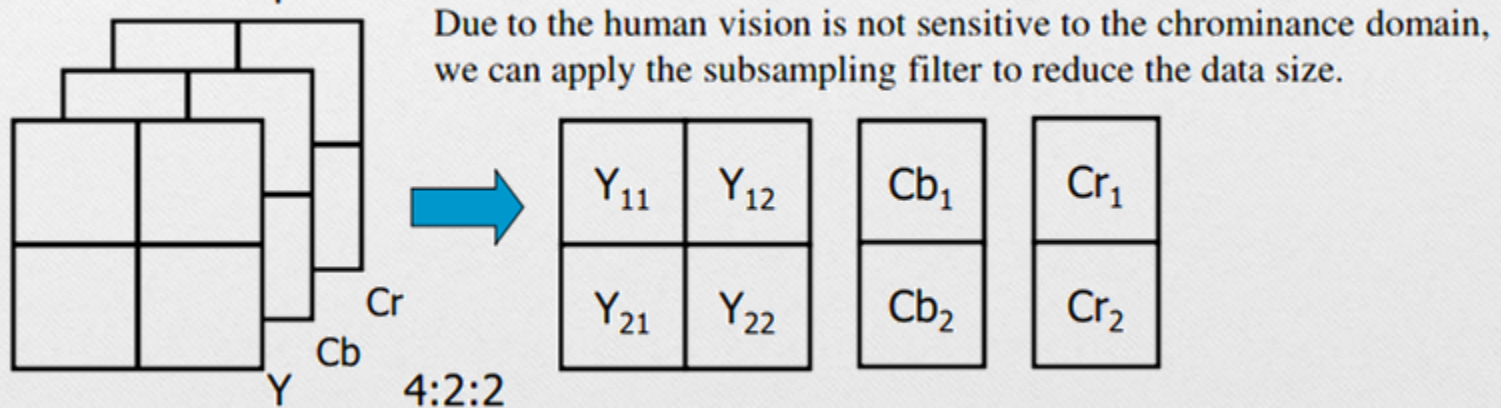
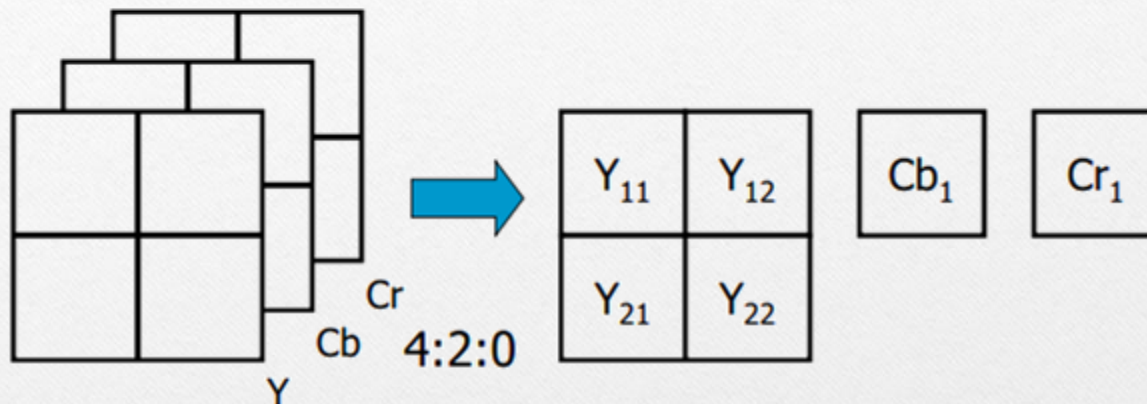
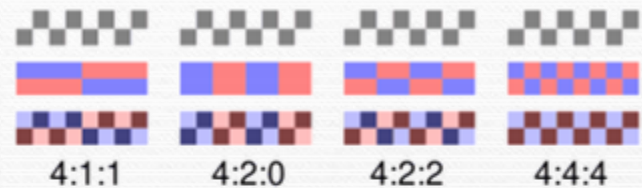
Baseline Sequential DCT-based Encoding Process

- Color Space Conversion
- Subsampling
- Partition
- Encoding Flow Control
 - Discrete Cosine Transform (DCT)
 - Quantization
 - Entropy Encoding (Huffman)



Decoding

Subsampling



Note: If there is no subsampling, we called this 4:4:4 mode

Baseline Sequential DCT-based Encoding Process

- Color Space Conversion
- Subsampling
- Partition
- Encoding Flow Control
 - Discrete Cosine Transform (DCT)
 - Quantization
 - Entropy Encoding (Huffman)



Decoding

Partition

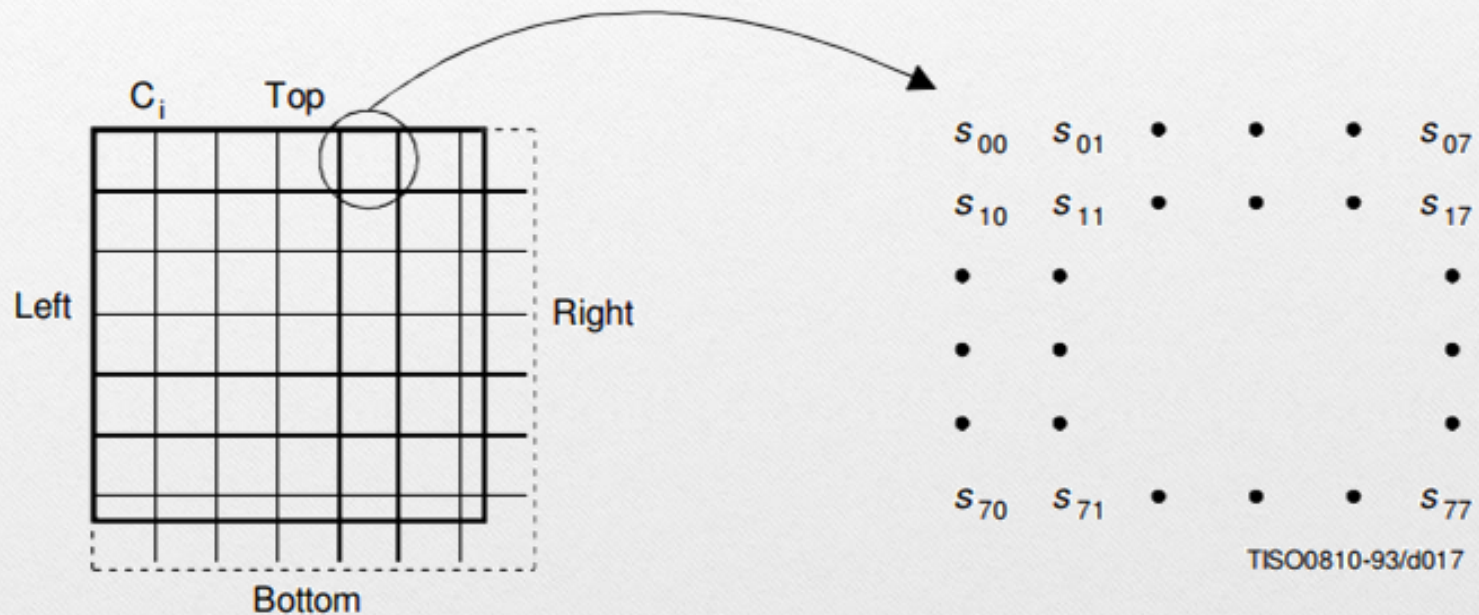


Figure A.4 – Partition and orientation of 8 x 8 sample blocks

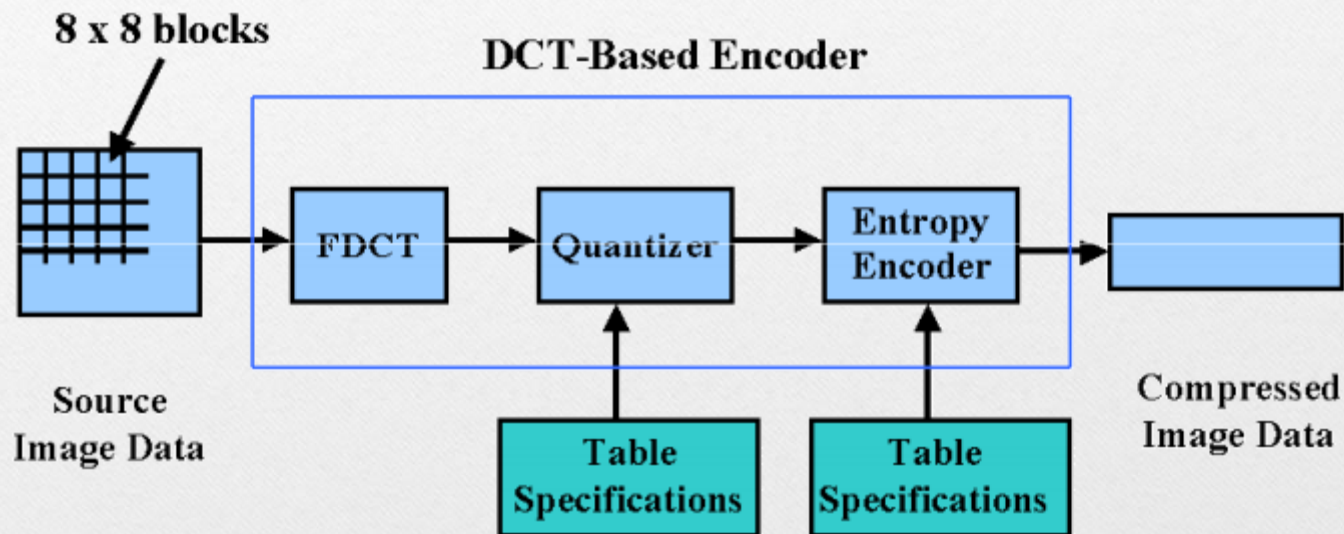
Baseline Sequential DCT-based Encoding Process

- Color Space Conversion
- Subsampling
- Partition
- Encoding Flow Control
 - Discrete Cosine Transform (DCT)
 - Quantization
 - Entropy Encoding (Huffman)



Decoding

Encoding Flow control



DCT-Based Encoder Processing Steps

Quantization

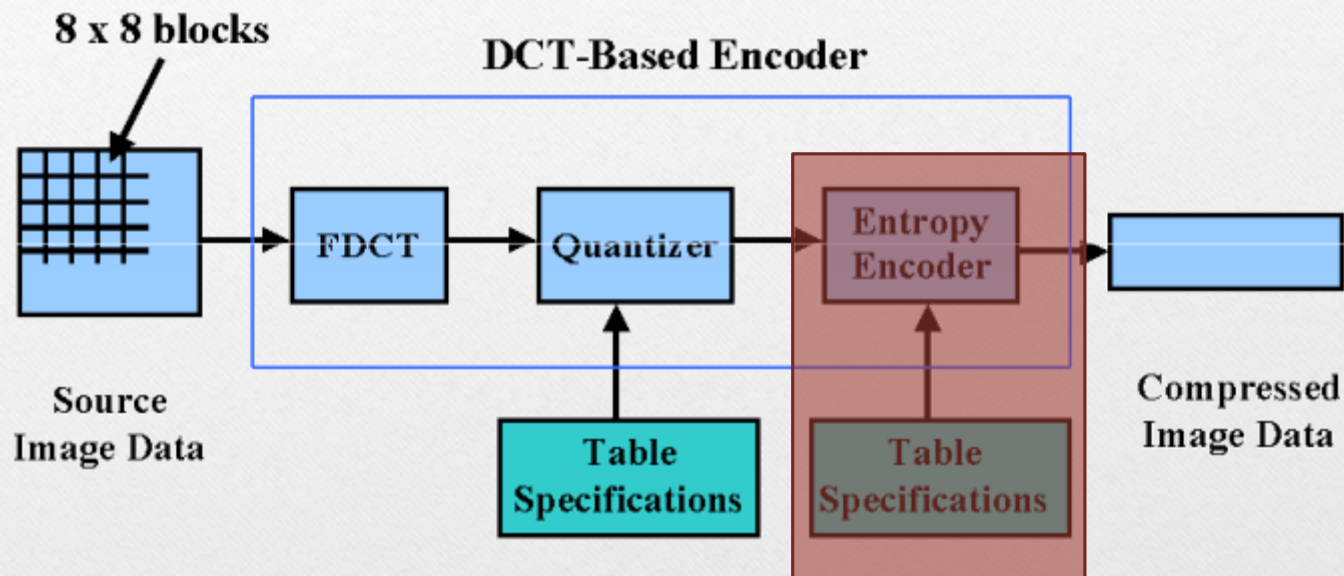
Table K.1 – Luminance quantization table

16	11	10	16	24	40	51	61
12	12	14	19	26	58	60	55
14	13	16	24	40	57	69	56
14	17	22	29	51	87	80	62
18	22	37	56	68	109	103	77
24	35	55	64	81	104	113	92
49	64	78	87	103	121	120	101
72	92	95	98	112	100	103	99

Table K.2 – Chrominance quantization table

17	18	24	47	99	99	99	99
18	21	26	66	99	99	99	99
24	26	56	99	99	99	99	99
47	66	99	99	99	99	99	99
99	99	99	99	99	99	99	99
99	99	99	99	99	99	99	99
99	99	99	99	99	99	99	99
99	99	99	99	99	99	99	99

Encoding Flow control



DCT-Based Encoder Processing Steps

Huffman Encoding

- DC Diff
- AC Run-Length pair

DC Encoding, AC Scanning

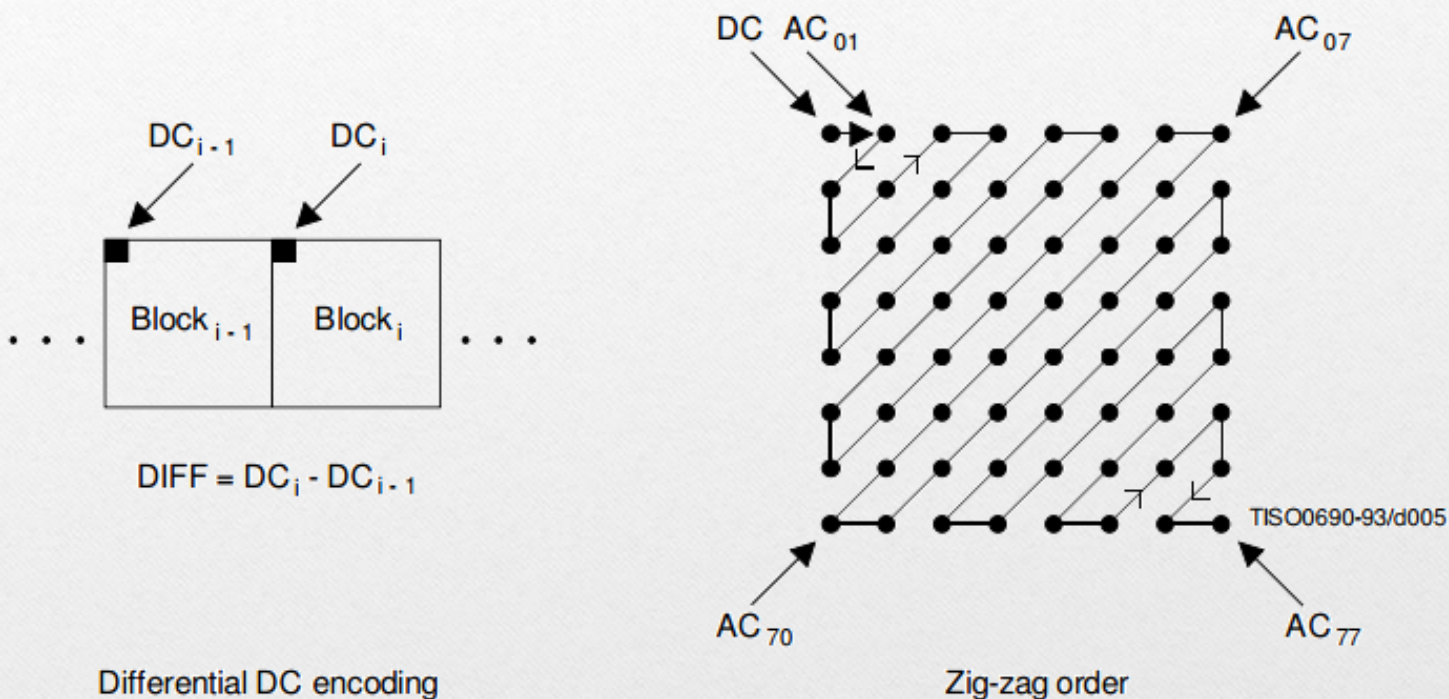


Figure 5 – Preparation of quantized coefficients for entropy encoding

DC Diff

(ITU-T81 Annex F)

SSSS	DIFF values
0	0
1	-1,1
2	-3,-2,2,3
3	-7..-4,4..7
4	-15..-8,8..15
5	-31..-16,16..31
6	-63..-32,32..63
7	-127..-64,64..127
8	-255..-128,128..255
9	-511..-256,256..511
10	-1 023..-512,512..1 023
11	-2 047..-1 024,1 024..2 047

1. Decoding codeword with *Huffman decoding* for getting SSSS
2. Using following SSSS bits as index for looking up DIFF value
3. Plus the DC value of last block

DC Huffman Decoding

$T = \text{DECODE}$

$\text{DIFF} = \text{RECEIVE}(T)$

$\text{DIFF} = \text{EXTEND}(\text{DIFF}, T)$

Example: 1011111.....

1. Decode and get $101_2 \rightarrow T = 4$

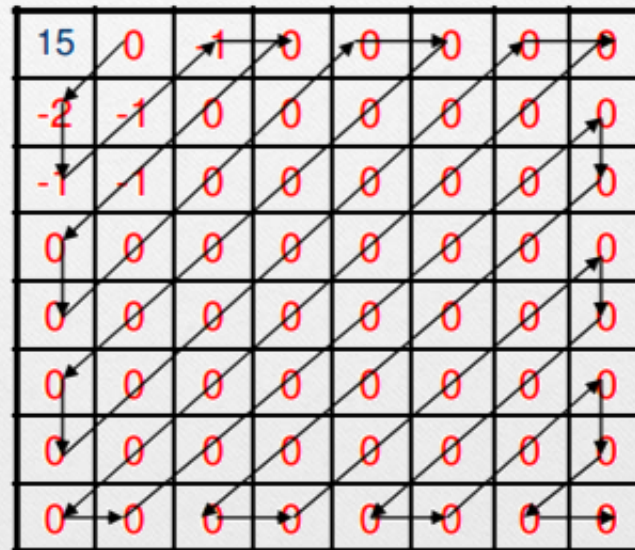
SSSS	DIFF values
0	0
1	-1,1
2	-3,-2,2,3
3	-7,-4,4,7
4	-15,-8,8,15
5	-31,-16,16,31
....

Huffman decoding

2. Get T bits \rightarrow Get $1111_2 \rightarrow \text{DIFF} = 1111_2$
3. Extent DIFF \rightarrow Get **15**

AC Run-length encoding

■ Run-length encoding



An 8x8 grid of quantized coefficients. The top-left element is 15 (blue), and the rest are 0 or -1 or -2 (red). Black arrows show the zigzag traversal path starting from the top-left and ending at the bottom-right.

15	0	-1	0	0	0	0	0
-2	-1	0	0	0	0	0	0
-1	-1	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0

15 0 -2 -1 -1 -1 0 0 -1 0 0 0

<1,-2> <0,-1> <0,-1> <0,-1> <2,-1> <EOB>

AC Huffman Encoding

RRRRSSSS

SSSS	AC coefficients
1	-1,1
2	-3,-2,2,3
3	-7..-4,4..7
4	-15..-8,8,15
5	-31..-16,16..31
6	-127..-64,64..127
....

AC coefficient magnitude category

Run/Size	Code length	Code word
0 / 0 (EOB)	4	1010
0 / 1	2	00
..
1 / 2	5	11011
..
2 / 1	5	11100
....

AC Huffman Table

AC code word = Run/Size code word + AC coefficient code word

<1,-2>	<0,-1>	<0,-1>	<0,-1>	<2,-1>	<EOB>
11011 01	00 0	00 0	00 0	11100 0	1010

AC Huffman Decoding

Example: 1101101.....

	0	1	2	SSSS	13	14
0	EOB					
.	N/A					
.	N/A					
.	N/A					
15	ZRL					

RRRR COMPOSITE VALUES

1. Decode and get $11011_2 \rightarrow RS = 1/2 \rightarrow S=2$
2. Get S bits $\rightarrow SSSS = 01_2$
3. Extent SSSS $\rightarrow SSSS = -2_{10}$

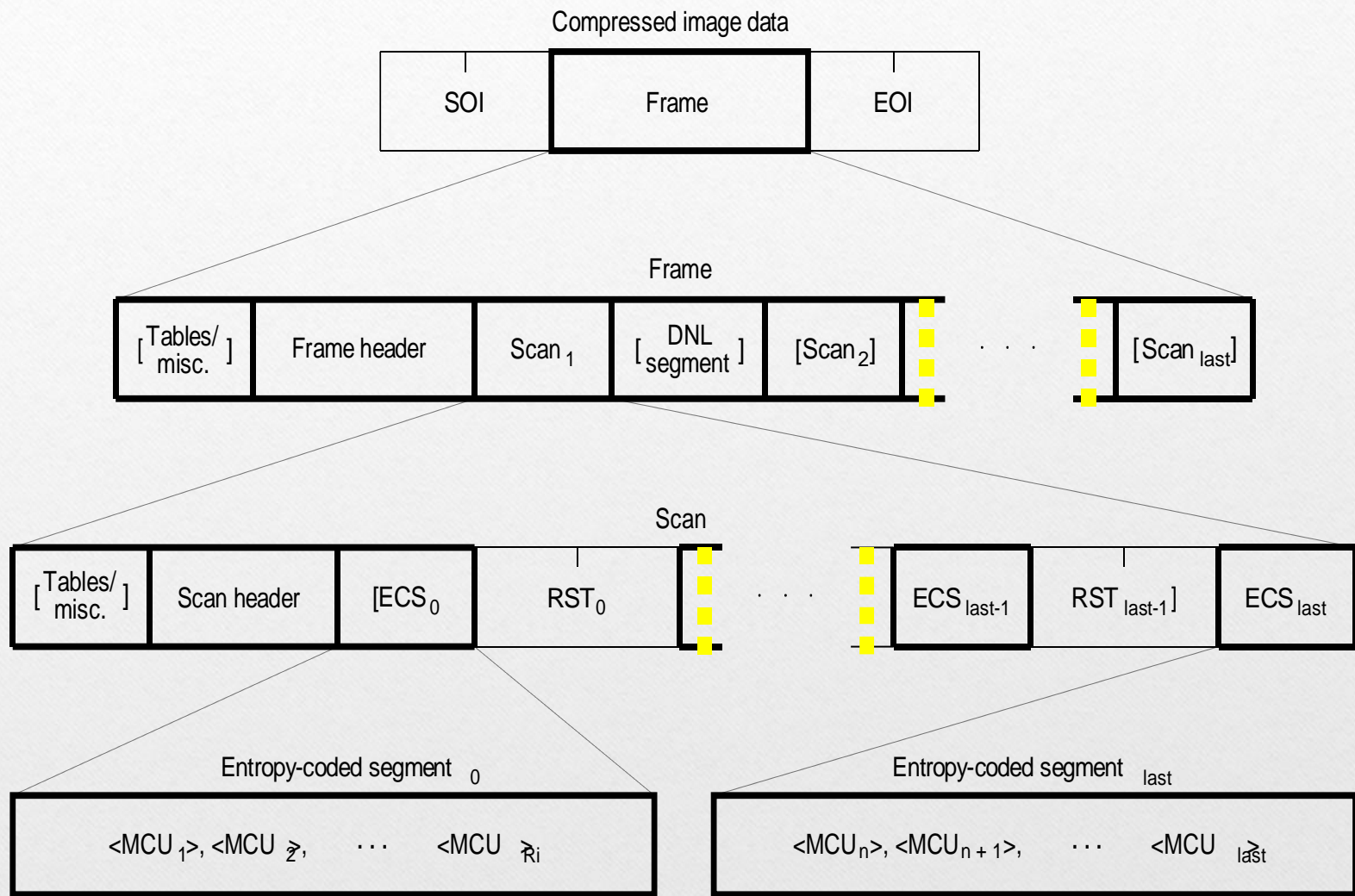
Huffman decoding

SSSS	AC coefficients
1	-1,1
2	-3,-2,2,3
3	-7,-4,4,7
4	-15,-8,8,15
5	-31,-16,16,31
6	-127,-64,64,127
....

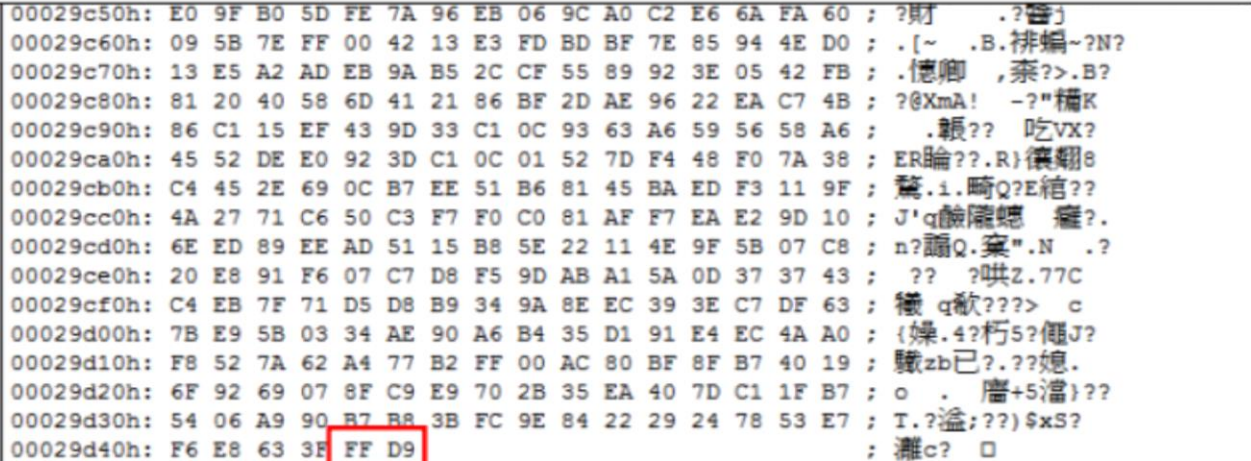
4. Finally, (Run, Length) = (R,SSSS) = (1,-2)

Header Processing

Important header



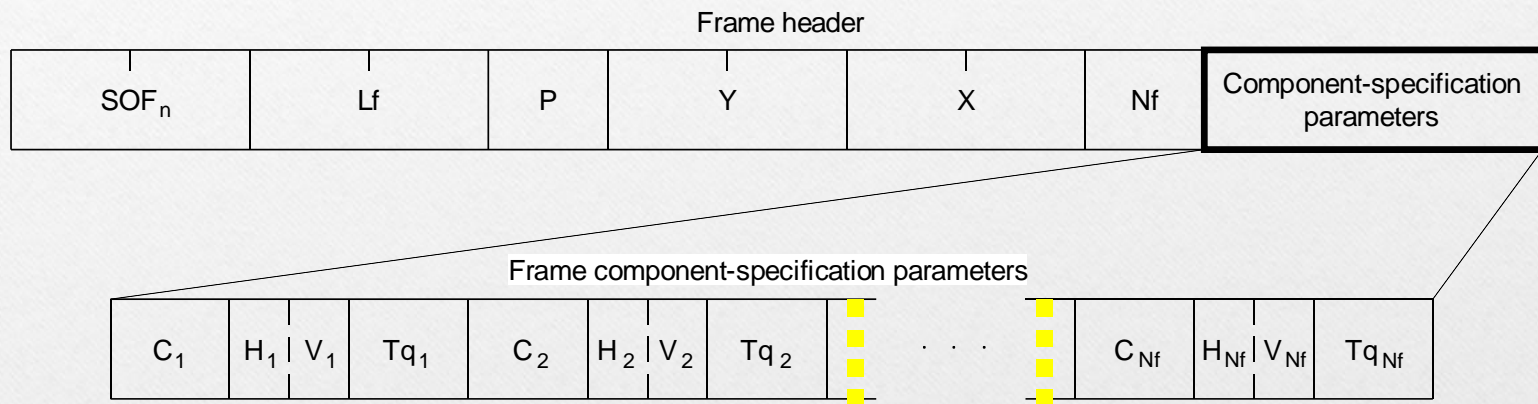
Code Assignment	Symbol	Description
Start Of Frame markers, non-differential, Huffman coding		
X'FFC0'	SOF0	Baseline DCT
Huffman table specification		
X'FFC4'	DHT	Define Huffman table(s)
Restart interval termination		
X'FFD0' through X'FFD7'	RSTm*	Restart with modulo 8 count "m"
Other markers		
X'FFD8'	SOI*	Start of image
X'FFD9'	EOI*	End of image
X'FFDA'	SOS	Start of scan
X'FFDB'	DQT	Define quantization table(s)
X'FFDD'	DRI	Define restart interval
X'FFE0' through X'FFEF'	APPn	Reserved for application segments



Header Structure (ITU-T81 Annex B)



SOF



TISO0850-93/d021

Y: Number of lines

X: Number of samples per line

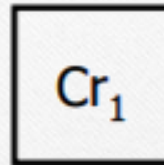
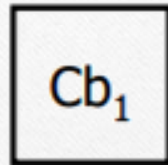
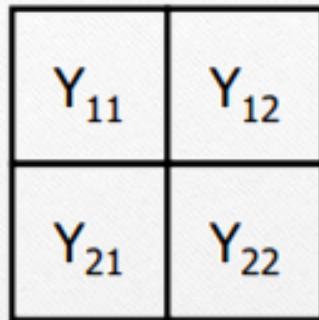
C_i: Component identifier

H_i: Horizontal sampling factor

V_i: Vertical sampling factor

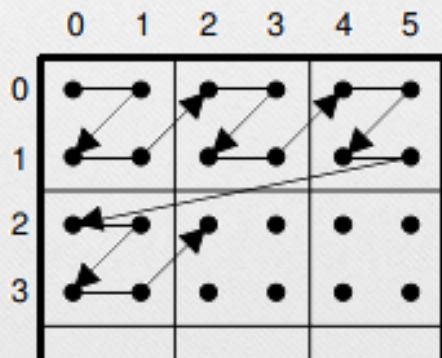
Parameter	Size (bits)	Sequential DCT		Progressive DCT	Lossless
		Baseline	Extended		
Lf	16	$8 + 3 * Nf$			
P	8	8	8, 12	8, 12	2-16
Y	16	0-65535			
X	16	1-65535			
Nf	8	1-255	1-255	1-4	1-255
Ci	8	0-255			
Hi	4	1-4			
Vi	4	1-4			
Tqi	8	0-3	0-3	0-3	0-1

MCU – Minimum Coded Unit, which is comprised by blocks from each component



In 4:2:0 mode, there are 6 blocks ($2*2+1*1+1*1$) in an MCU.

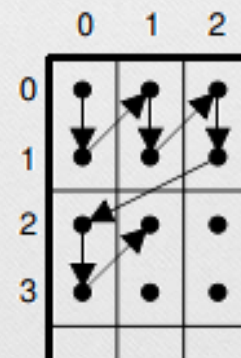
$Cs_1: H_1 = 2, V_1 = 2$



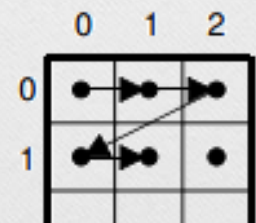
$Cs_2: H_2 = 2, V_2 = 1$



$Cs_3: H_3 = 1, V_3 = 2$



$Cs_4: H_4 = 1, V_4 = 1$



TISO0800-93/d016

SOF

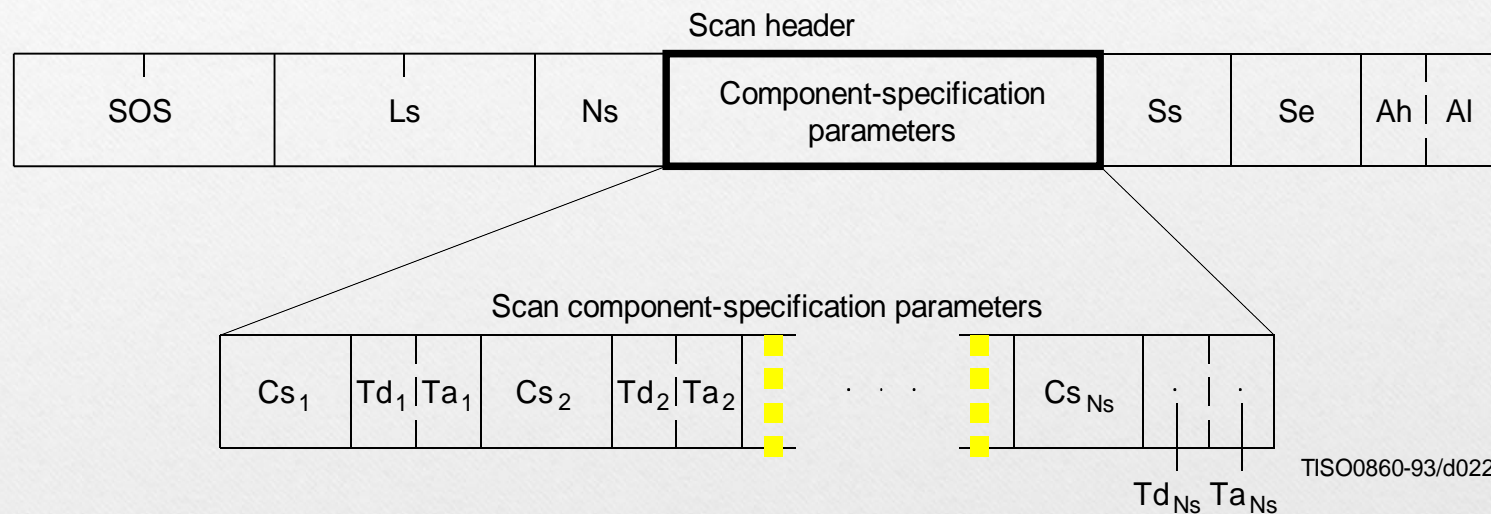
- Each component's size will be calculated by the following equation:

$$x_i = X \times \frac{H_i}{H_{\max}} \quad \text{and} \quad y_i = Y \times \frac{V_i}{V_{\max}}$$

- Ex: $X=512, Y=512, H_{\max}=2, V_{\max}=2$

Component 0	$H_0=2, V_0=2$	→	$x_0=512, y_0=512$
Component 1	$H_1=1, V_1=1$	→	$x_1=256, y_1=256$
Component 2	$H_2=1, V_2=1$	→	$x_2=256, y_2=256$

SOS



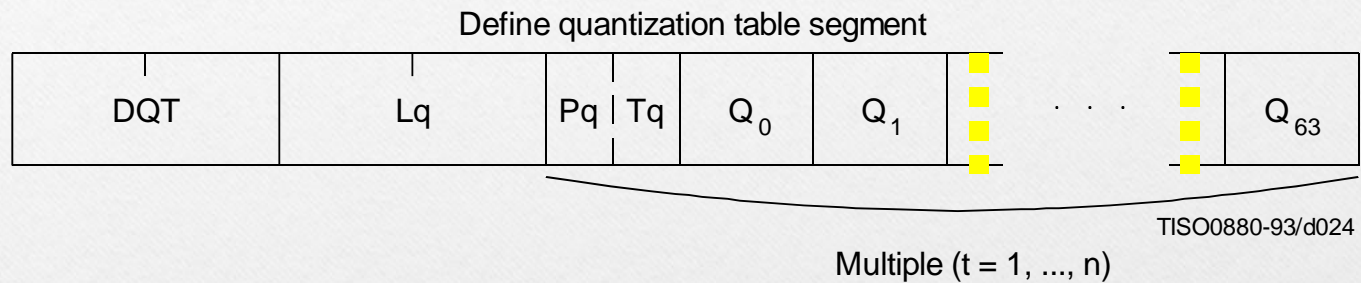
Ns: Number of image components in scan

Tdj: DC entropy coding table destination selector

Taj: AC entropy coding table destination selector

		Values			
Parameter	Size (bits)	Sequential DCT		Progressive DCT	Lossless
		Baseline	Extended		
Ls	16	$6 + 2 * N_s$			
Ns	8	1-4			
Csj	8	0-255			
Tdj	4	0-1	0-3	0-3	0-3
Taj	4	0-1	0-3	0-3	0
Ss	8	0	0	0-63	1-7
Se	8	63	63	Ss-63	0
Ah	4	0	0	0-13	0
Al	4	0	0	0-13	0-15

DQT

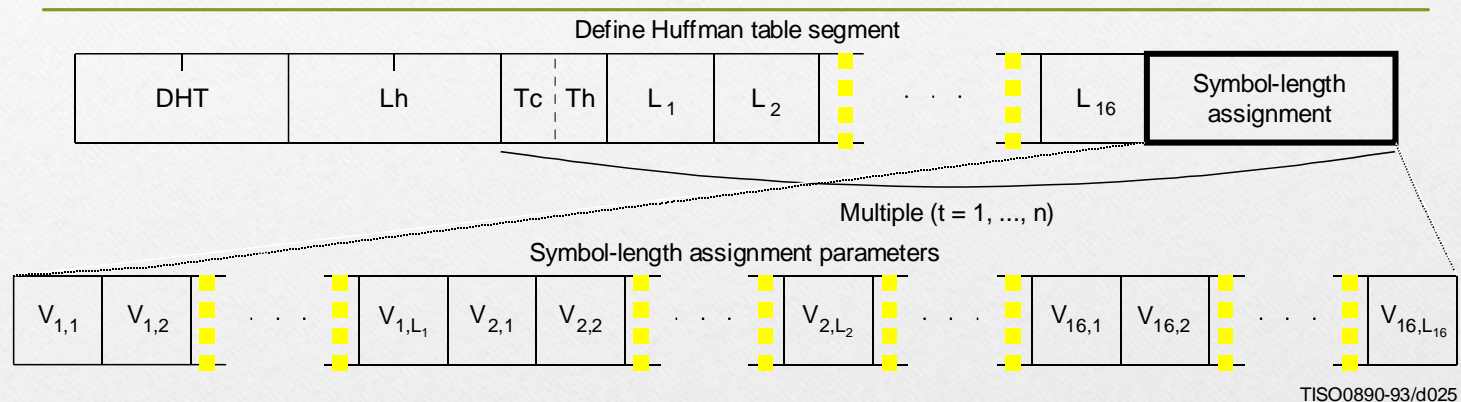


T_q: Quantization table destination identifier
- 0 for DC 1 for AC

Q_k: Quantization table element

		Values			
Parameter	Size (bits)	Sequential DCT		Progressive DCT	Lossless
		Baseline	Extended		
Lq	16				Undefined
Pq	4	0	0, 1	0, 1	Undefined
Tq	4	0-3			Undefined
Q _k	8, 16	1-255, 1-65535			Undefined

DHT



(codeword)

Tc: Table class, DC or AC?

Th: Huffman table destination identifier

(length)

Li: Number of Huffman codes of length i

Vi,j: Value associated with each Huffman code

(symbol)

$$C_1 = 00 \dots 0$$

$$C_{i+1} = (C_i + 1) * 2^{p-q}$$

and

$$C_n = 11 \dots 1$$

where p and q are the codeword lengths for s_i and s_{i+1}

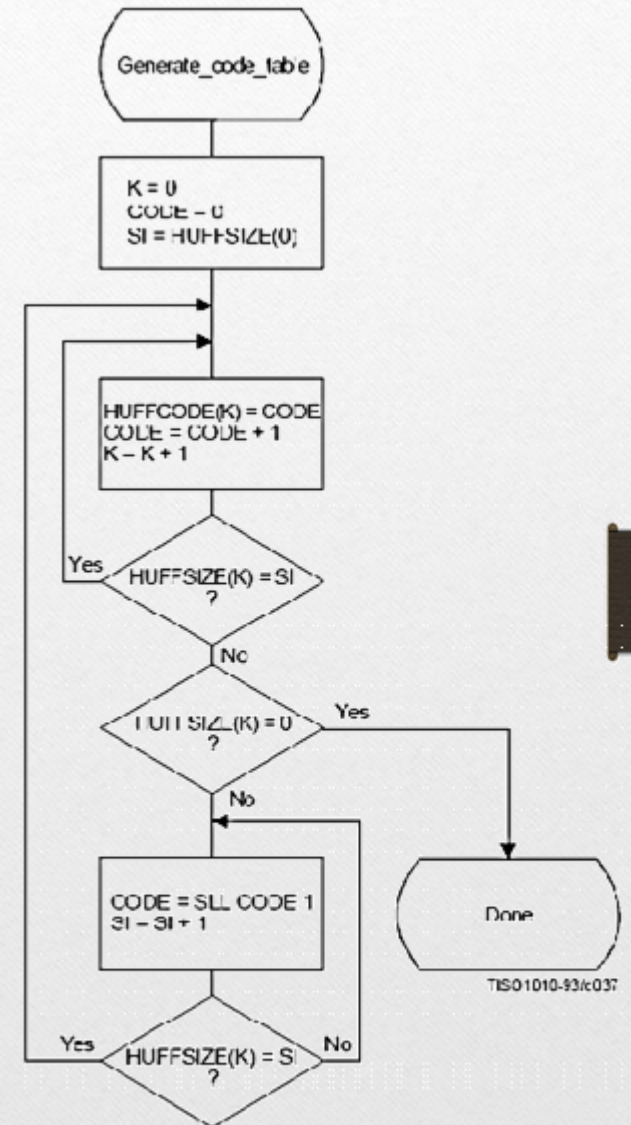
■ Example

Tc: 0 Th: 0

Li: 0 1 5 1 1 1 1 1 0 0 0 0 0 0 0

Vi,j: 0 1 2 3 4 5 6 7 8 9 10 11

Size	Codeword	Value
2	00	0
3	010	1
3	011	2
3	100	3
3	101	4
3	110	5
4	1110	6
5	11110	7
6	111110	8
7	1111110	9
8	11111110	10
9	111111110	11



(ITU-T81 Annex C)

Figure C.2 – Generation of table of Huffman codes

In common case, there are **four** huffman tables:

- Luminance' s DC huffman table
- Luminance' s AC huffman table
- Chrominance' s DC huffman table
- Chrominance' s AC huffman table

		Values			
Parameter	Size (bits)	Sequential DCT		Progressive DCT	Lossless
		Baseline	Extended		
Lh	16				
Tc	4	0, 1			0
Th	4	0, 1	0-3		
Li	8	0-255			
Vi, j	8	0-255			

DRI

- R_i – Specifies the number of MCU in the restart interval.
- After the number of MCU reaches R_i , a RST marker is inserted and the MCU counter is reset.
- You can resynchronize to the next RST marker when bit error or packet loss occurred during image transmission and the number of lost MCU can be known from the difference of RST id.

Other Marker

- APP, COM, DNL, DHP, EXP ...
- **Skip it** by using the length field after the marker