

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE, JAIPUR

NBA - Criteria 3.1.1

S.No.	Sem.	Subject Code	Subject	Course Outcomes
1	3	3CS1A	Electronic Devices Lab	
2	3	3CS8A	Data Structures Lab	
3	3	3CS9A	Digital Electronics Lab	
4	3	3CS10A	C++ Programming	
5	3	3CS11A	Unix Shell Programming	Implement basic set of commands and utilities of Linux administration Apply Linux tools such as vi editor for text processing Execute shell programs to automate various operations in Linux
6	4	4CS7A	Microprocessor Lab	
7	4	4CS8A	Communication Lab	CO1: Analyze analog modulation and demodulation using AM and FM calculate modulation index. CO2: Analyze pulse analog modulation and demodulation via PAM, PWM and PPM. CO3: Analyze digital modulation PCM encoding and ASK, FSK transmitter and receiver. CO4: Study AGC mechanism and Heterodyne receiver
8	4	4CS9A	Computer Aided Software Engineering Lab	
9	4	4CS10A	Business Entrepreneurship Development	
10	5	5CS7A	DataBase Lab	
11	5	5CS8A	System Design in UML Lab	
12	5	5CS9A	Operating Systems Simulation Lab	CO1. Analyze and simulate CPU Scheduling Algorithms. CO2. Implement memory management schemes and page replacement schemes CO3. Implement deadlock prevention & avoidance algorithm
13	5	5CS10	Digital Hardware Design Lab	
14	6	6CS7A	Java Programming Lab	
15	6	6CS8A	Computer Graphics & Multimedia Lab	
16	6	6CS9A	Design and Analysis of Algorithms Lab.	
17	6	6CS10A	Embedded System Design Lab	
18	6	6CS11A	Humanities and Social Sciences	

19	7	7CS7A	Web Development Lab	
20	7	7CS8A	VLSI Physical Design Lab	CO1: Implementing the canonical form inter conversion through programming
				CO2:Implementing the graphical sequencing and colouring algorithm through programming.
				CO3:Implementing decision tree concept using programming logic.
				CO4:Implementing Hardware modelling of logic gates and combinational digital circuits using VHDL.
21	7	7CS9A	Compiler Design Lab	
22	7	7CSPR	Project-I	
23	7	7CSTR	Practical Training*	
24	8	8CS5A	Unix Network Programming & Simulation Lab	
25	8	8CS6A	FPGA Lab.	CO1: Understanding the basic signal operations through DSP fundamentals
				CO2: Implementing Unit impulse/step and ramp functions using MATLAB
				CO3: Implementing triangular and exponential growth signal functions using MATLAB
26	8	8CS7A	Digital Image Processing lab	
27	8	8CSPR	Project-II	
28	8	8CSSM	Seminar	