

Operating System: GATE PYQ - Virtual Memory

By: Vishvadeep Gothi





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14+ Years of Teaching Experience, 10+ Years in GATE/IES





Subjects Taken:

Computer Organization & Architecture, data structure, operating system, C-Programming, Data science, Machine Learning, Python, coding interview problems,







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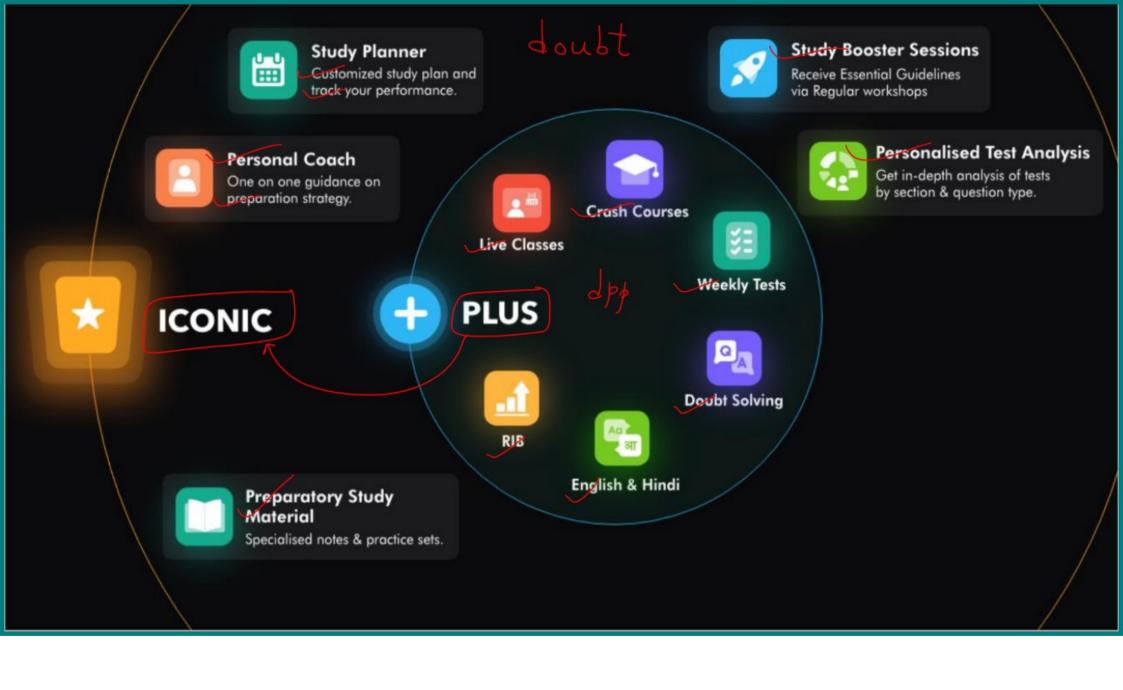
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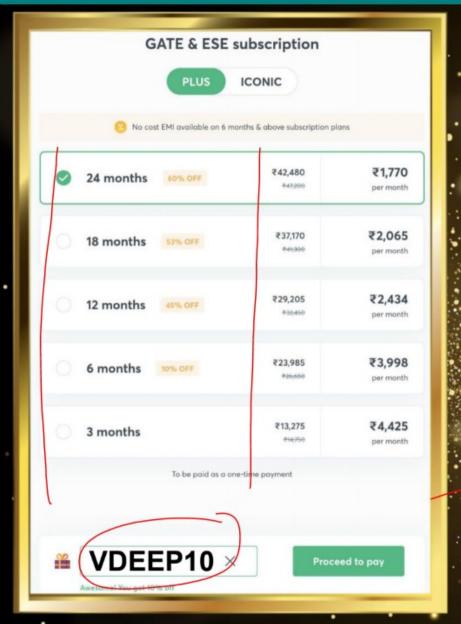
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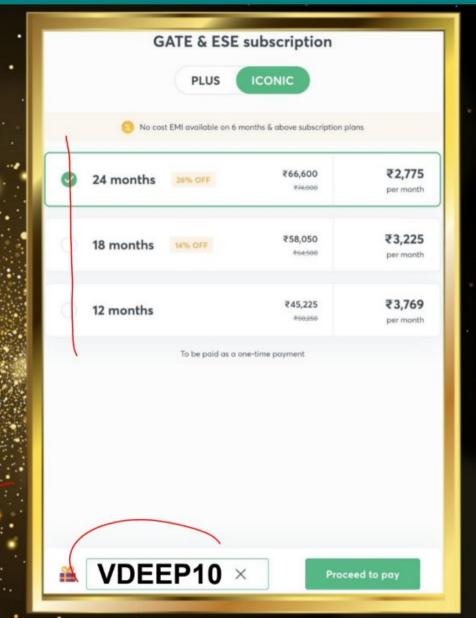


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7:00 AM

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Are You Ready??









Match the pairs in the following question.

List - I

- (A) Virtual Memory —
- (B) Shared memory 5
- (C) Look-ahead buffer 2
- (D) Look-aside buffer $\longrightarrow \mathcal{N}$

List - II

- (p) Temporal locality
- (q) Spatial Locality
- (r) Address Translation
- (s) Mutual exclusion



>2-level paging

 $t_{D} = 10^{-8} \text{ Sec}$

In a two-level virtual memory, the memory access time for main memory, $t_M=10^{-8}\,$ sec, and the memory access time for the secondary memory, $t_D=10^{-3}\,$ sec. What must be the hit ratio, H such that the access efficiency is within $80\,$ percent of its maximum value?

NO TLB given

H = mm hit

(no page fault)

Best Value = $3 * 10^{-8} \text{ Sec}$ $0.8 * 3 * 10^{-8} = H * 3 * 10^{-8} + 10^{-3}$ $(1-H) * [2 * 10^{-8} + 10^{-3}]$ 99.99%

page fault rate



Indicate all the false statements from the statements given below:

- A. The amount of virtual memory available is limited by the availability of the secondary memory

 B. Any implementation of a critical section requires the use of an indivisible machine- instruction, such as test-and-set.
- C. The use of monitors ensure that no dead-locks will be caused .
- Dr. The LRU page-replacement policy may cause thrashing for some type of programs.
- EX The best fit techniques for memory allocation ensures that memory will never be fragmented.

E. False

A True
B. False
C. False
D. True



In a paged segmented scheme of memory management, the segment table itself must have a page table because

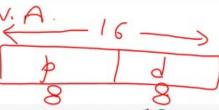
- A. The segment table is often too large to fit in one page
- B. Each segment is spread over a number of pages
- C. Segment tables point to page tables and not to the physical locations of the segment
- D. The processor's description base register points to a page table

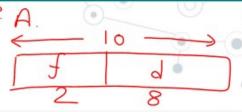


In a virtual memory system the address space specified by the address lines of the CPU must be target than the physical memory size and target than the secondary storage size.

- A. smaller, smaller
- B. smaller, larger
- C. larger, smaller
- D. larger, larger







A demand paged virtual memory system uses 16 bit virtual address, page size of 256 bytes, and has 1 Kbyte of main memory.LRU page replacement is implemented using the list, whose current status (page number is decimal) is



For each hexadecimal address in the address sequence given below,

00FF, 010D, 10FF, 11B0

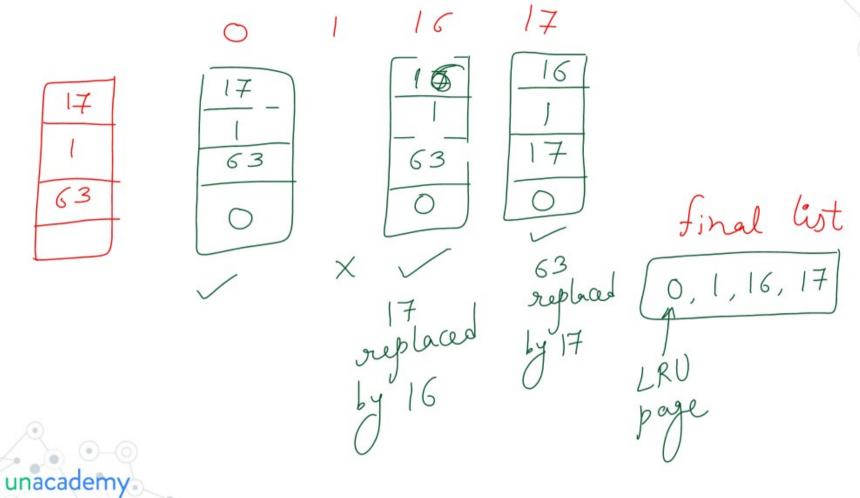
Page no. ref. => 0, 1, 16, 17

indicate

- i. the new status of the list
- ii. page faults, if any, and 3
- ii. page replacements, if any.

no. of trames = 4





ang inst executing time = 1 + j*1

If an instruction takes i microseconds and a page fault takes an additional j microseconds, the

effective instruction time if on the average a page fault occurs every \boldsymbol{k} instruction is:

$$A. \ i + \frac{j}{k}$$

B.
$$i + (j \times k)$$

c.
$$\frac{i+j}{k}$$

D.
$$(i+j) imes k$$

for
$$-11-1$$
 inst, $-11-=\frac{1}{k}$







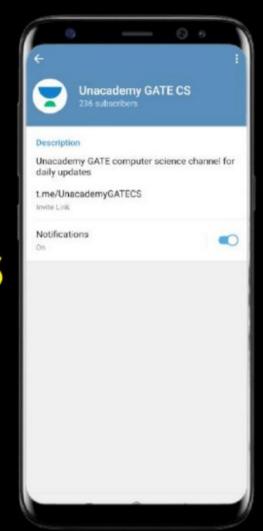
Which of the following is/are advantage(s) of virtual memory?

- A. Faster access to memory on an average.
- B. Processes can be given protected address spaces.
- C. Linker can assign addresses independent of where the program will be loaded in physical memory.
- D. Program larger than the physical memory size can be run.





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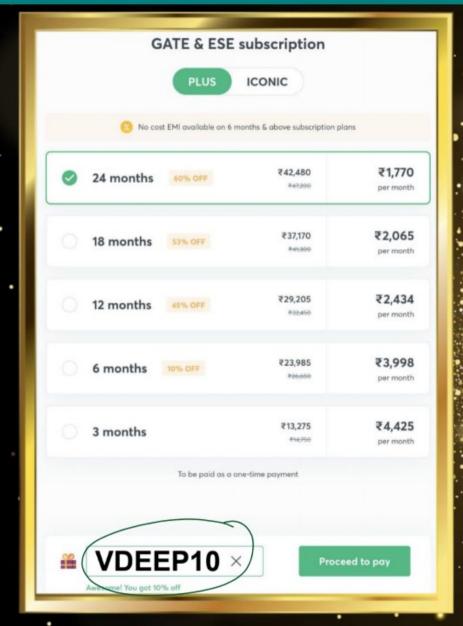
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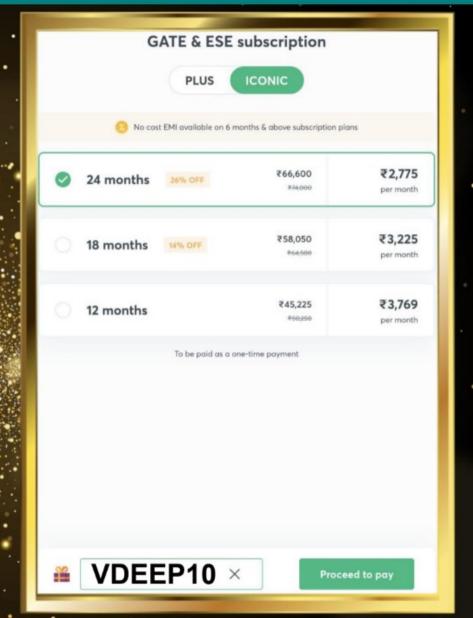


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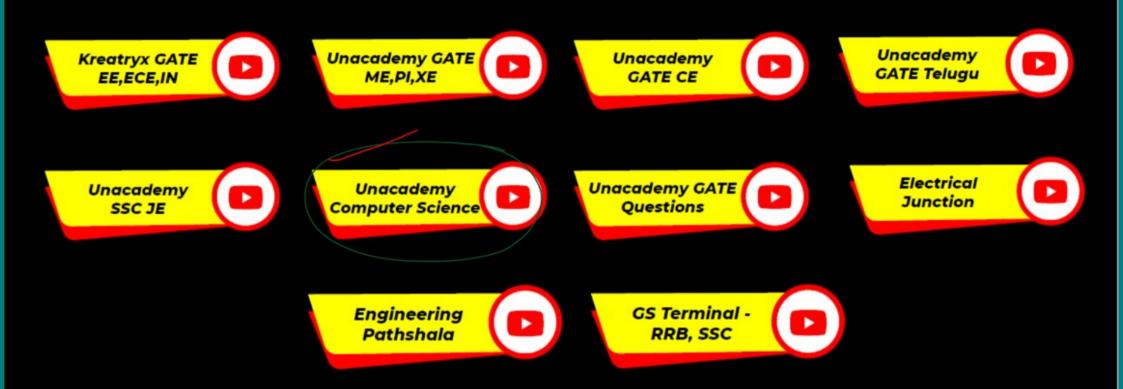
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