General Description

The MAX4465–MAX4469 are micropower op amps optimized for use as microphone preamplifiers. They provide the ideal combination of an optimized gain bandwidth product vs. supply current, and low voltage operation in ultra-small packages. The MAX4465/MAX4467/MAX4469 are unity-gain stable and deliver a 200kHz gain bandwidth from only 24µA of supply current. The MAX4466/MAX4468 are decompensated for a minimum stable gain of +5V/V and provide a 600kHz gain bandwidth product. In addition, these amplifiers feature Rail-to-Rail® outputs, high AVOL, plus excellent power-supply rejection and common-mode rejection ratios for operation in noisy environments.

The MAX4467/MAX4468 include a complete shutdown mode. In shutdown, the amplifiers' supply current is reduced to 5nA and the bias current to the external microphone is cut off for ultimate power savings. The single MAX4465/MAX4466 are offered in the ultra-small 5-pin SC70 package, while the single with shutdown MAX4467/MAX4468 and dual MAX4469 are available in the space-saving 8-pin SOT23 package.

Applications

Microphone Preamplifiers

Hearing Aids

Cellular Phones

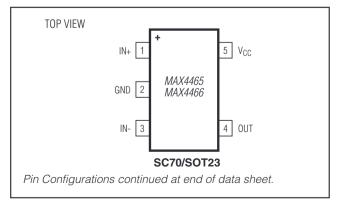
Voice-Recognition Systems

Digital Dictation Devices

Headsets

Portable Computing

Pin Configurations



Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Features

- ♦ +2.4V to +5.5V Supply Voltage Operation
- ♦ Versions with 5nA Complete Shutdown Available (MAX4467/MAX4468)
- ♦ Excellent Power-Supply Rejection Ratio: 112dB
- ♦ Excellent Common-Mode Rejection Ratio: 126dB
- ♦ High A_{VOL}: 125dB (R_L = 100kΩ)
- ♦ Rail-to-Rail Outputs
- ♦ Low 24µA Quiescent Supply Current
- ◆ Gain Bandwidth Product: 200kHz (MAX4465/MAX4467/MAX4469) 600kHz Ay ≥ 5 (MAX4466/MAX4468)
- ♦ Available in Space-Saving Packages 5-Pin SC70 (MAX4465/MAX4466) 8-Pin SOT23 (MAX4467/MAX4468/MAX4469)

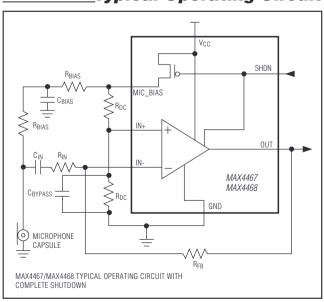
Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX4465EXK+T	-40°C to +85°C	5 SC70
MAX4465EUK+T	-40°C to +85°C	5 SOT23
MAX4466EXK+T	-40°C to +85°C	5 SC70
MAX4466EUK+T	-40°C to +85°C	5 SOT23

⁺Denotes a lead(Pb)-free/RoHS-compliant package.

Ordering Information continued at end of data sheet.

Typical Operating Circuit



ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{CC} to GND)+6V
All Other Pins to GND0.3V to (V _{CC} + 0.3V)
Output Short-Circuit Duration
OUT Shorted to GND or VCCContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
5-Pin SC70 (derate 2.5mW/°C above +70°C)200mW
5-Pin SOT23 (derate 7.1mW/°C above +70°C)571mW

8-Pin SOT23 (derate 5.3mW/°C above +7	70°C)421mW
Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = \infty \ to \ V_{CC}/2, SHDN = GND \ (MAX4467/MAX4468 \ only). \ T_A = T_{MIN} \ to \ T_{MAX}, \ unless \ otherwise \ noted. Typical values specified at T_A = +25°C.) \ (Note 1)$

PARAMETER	SYMBOL	CONDIT	IONS	MIN	TYP	MAX	UNITS	
Supply Voltage Range	Vcc	Inferred from PSRR test	2.4		5.5	V		
Supply Current		T _A = +25°C			24	48		
(Per Amplifier)	Icc	TA = TMIN to TMAX				60	μΑ	
Supply Current in Shutdown	Ishdn	SHDN = V _{CC} (Note 2)			5	50	nA	
Input Offset Voltage	Vos				±1	±5	mV	
Input Bias Current	ΙΒ	$V_{CM} = -0.1V$			±2.5	±100	nA	
Input Offset Current Range	los	$V_{CM} = -0.1V$			±1	±15	nA	
Input Common-Mode Range	V _{CM}	Inferred from CMRR test		-0.1		V _{CC} - 0.1	V	
Common-Mode Rejection Ratio	CMRR	-0.1V ≤ V _{CM} ≤ V _{CC} - 1V		80	126		dB	
		$2.4V \le V_{CC} \le 5.5V$		80	112			
Power-Supply Rejection Ratio	PSRR	MAX4465/MAX4467/MAX4469, f = 3.4kHz			75		dB	
		MAX4466/MAX4468, f = 3.4kHz			80			
		R _L = 100k Ω to V _{CC} /2, 0.05V ≤ V _{OUT} ≤ V _{CC} - 0.05V			125		15	
Open-Loop Gain	Avol	$R_L = 10k\Omega$ to $V_{CC}/2$, $0.1V \le V_{OUT} \le V_{CC} - 0.1V$		80	95		dB	
0 1 17 11 0 1 11 1	.,	lv. v. l	$R_L = 100k\Omega$		10			
Output Voltage Swing High	VoH	IV _{CC} - V _{OH} I	$R_L = 10k\Omega$		16	50	mV	
0 1 17/11 0 1 1	.,		$R_L = 100k\Omega$		10			
Output Voltage Swing Low	VoL		$R_L = 10k\Omega$		14	50	mV	
Output Short-Circuit Current		To either supply rail			15		mA	
Output Leakage Current in Shutdown		SHDN = V_{CC} , $0 \le V_{OUT} \le V_{CC}$; (Notes 2, 3)			±0.5	±100	nA	
SHDN Logic Low	VIL	(Note 2)			\	√ _{CC} × 0.3	V	
SHDN Logic High	VIH	(Note 2)		$V_{CC} \times 0.7$	7		V	
SHDN Input Current		(Note 2)			2	25	nA	
0 . D 1	ODWE	MAX4465/MAX4467/MAX4469			200			
Gain Bandwidth Product	GBWP	MAX4466/MAX4468			600		kHz	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = \infty \text{ to } V_{CC}/2, SHDN = GND (MAX4467/MAX4468 only), T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values specified at } T_A = +25^{\circ}C.) (Note 1)$

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS
Channel-to-Channel Isolation		MAX4469 only, f = 1kHz			85		dB
Phase Margin	ØM	$R_L = 100k\Omega$			70		degrees
Gain Margin		$R_L = 100k\Omega$			20		dB
Slew Rate	SR	Output step = 4V	MAX4465/MAX4467/ MAX4469, A _V = +1	67/			mV/μs
			MAX4466/MAX4468, A _V = +5		300		
Input Noise Voltage Density	en	f = 1kHz	f = 1kHz		80		nV/√Hz
Total Harmonic Distortion	THD	$f = 1kHz$, $R_L = 10k\Omega$,	MAX4465/MAX4467/ MAX4469		0.02		%
		V _{OUT} = 2Vp-p	MAX4466/MAX4468		0.03		1
Compositive Lond Otability	CLOAD	MAX4465/MAX4467/MA	AX4469, A _V = +1		100		nE
Capacitive Load Stability	CLOAD	MAX4466/MAX4468, A _V = +5			100		- pF
SHDN Delay Time	tshdn	(Note 2)			1		μs
Enable Delay Time	t _{EN}	(Note 2)			50		μs
Power-On Time	ton	(Note 2)			40		μs
Bias Switch On-Resistance	R _S	I _S = 5mA (Note 2)			20	500	Ω

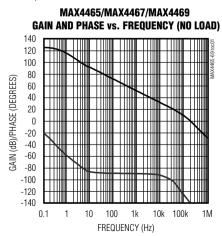
Note 1: All specifications are 100% production tested at $T_A = +25$ °C. All temperature limits are guaranteed by design.

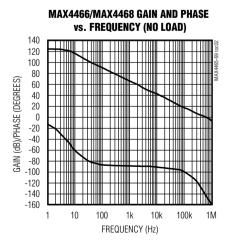
Note 2: Shutdown mode is available only on the MAX4467/MAX4468.

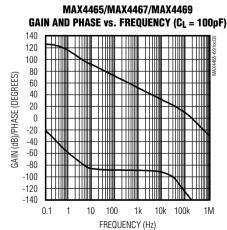
Note 3: External feedback networks not considered.

_Typical Operating Characteristics

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2$, SHDN = GND (MAX4467/MAX4468 only), $T_A = +25^{\circ}C$, unless otherwise noted.)

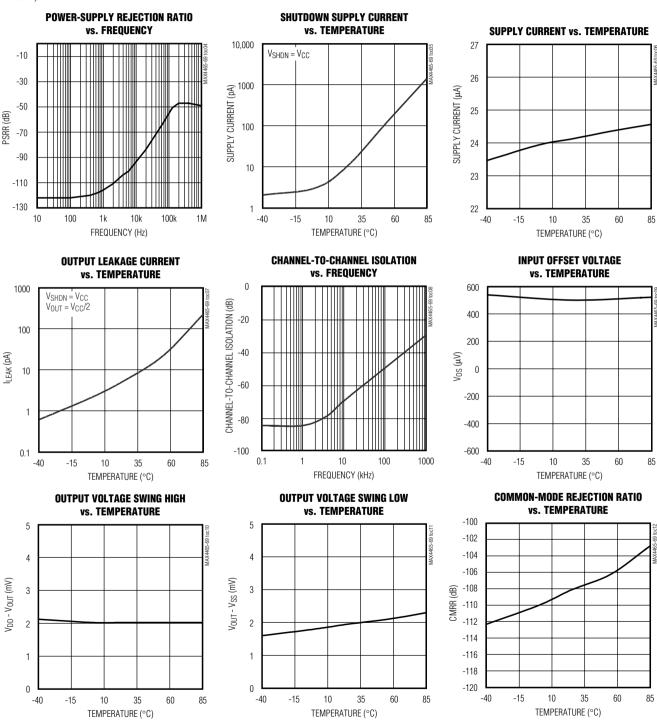






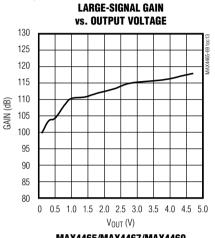
Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2$, SHDN = GND (MAX4467/MAX4468 only), $T_A = +25^{\circ}C$, unless otherwise noted.)



Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2$, SHDN = GND (MAX4467/MAX4468 only), $T_A = +25^{\circ}C$, unless otherwise noted.)



LARGE-SIGNAL GAIN
VS. TEMPERATURE

130

125

125

110

-40

-15

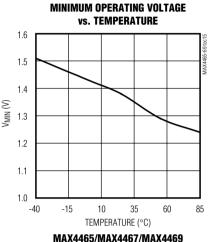
10

35

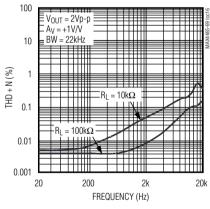
60

85

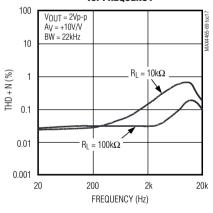
TEMPERATURE (°C)



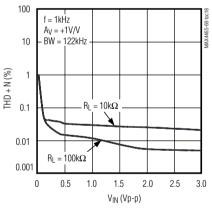
MAX4465/MAX4467/MAX4469
TOTAL HARMONIC DISTORTION PLUS NOISE
vs. FREQUENCY



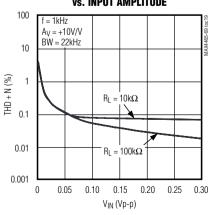
MAX4466/MAX4468
TOTAL HARMONIC DISTORTION
vs. Frequency



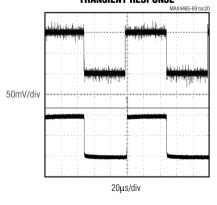
MAX4465/MAX4467/MAX4469
TOTAL HARMONIC DISTORTION PLUS NOISE
vs. INPUT AMPLITUDE



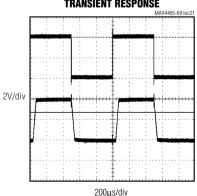
MAX4466/MAX4468
TOTAL HARMONIC DISTORTION PLUS NOISE
vs. Input amplitude



NONINVERTING SMALL-SIGNAL TRANSIENT RESPONSE

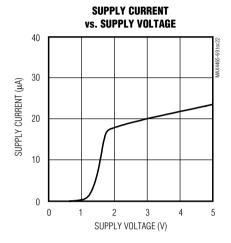


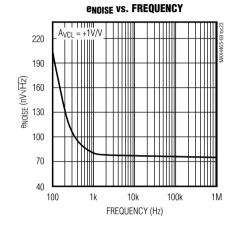
NONINVERTING LARGE-SIGNAL TRANSIENT RESPONSE

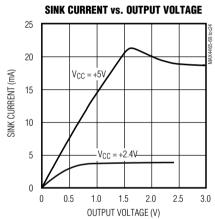


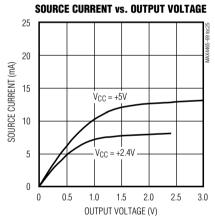
Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{CM} = 0V, V_{OUT} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2$, SHDN = GND (MAX4467/MAX4468 only), $T_A = +25^{\circ}C$, unless otherwise noted.)









Pin Description

	PIN			
MAX4465 MAX4466	MAX4467 MAX4468	MAX4469	NAME	FUNCTION
4	6 (8)	1	OUT	Amplifier Output
_	_	1	OUTA	Amplifier Output A
_	1 (4)	_	MIC_BIAS	External Microphone Bias Network Switch Output
3	2 (3)	_	IN-	Inverting Amplifier Input
1	3 (2)	_	IN+	Noninverting Amplifier Input
2	4 (1)	4	GND	Ground

⁽⁾ denotes S0T23 package of the MAX4467/MAX4468

Pin Description (continued)

	PIN			
MAX4465 MAX4466	MAX4467 MAX4468	MAX4469	NAME	FUNCTION
5	7 (7)	8	V _{CC}	Positive Supply. Bypass with a 0.1µF capacitor to GND.
_	_	2	INA-	Inverting Amplifier Input A
_	_	3	INA+	Noninverting Amplifier Input A
_	_	6	INB-	Inverting Amplifier Input B
_	_	5	INB+	Noninverting Amplifier Input B
_	_	7	OUTB	Amplifier Output B
	8 (6)		SHDN	Active-High Shutdown Input. Connect to GND for normal operation. Connect to V _{CC} for shutdown. Do not leave unconnected.
_	5 (5)	_	N.C.	No Connection. Not internally connected.

⁽⁾ denotes SOT23 package of the MAX4467/MAX4468.

Detailed Description

The MAX4465–MAX4469 are low-power, micropower op amps designed to be used as microphone preamplifiers. These preamplifiers are an excellent choice for noisy environments because of their high common-mode rejection and excellent power-supply rejection ratios. They operate from a single +2.4V to +5.5V supply.

The MAX4465/MAX4467/MAX4469 are unity-gain stable and deliver a 200kHz gain bandwidth from only 24 μ A of supply current. The MAX4466/MAX4468 have a minimum stable gain of +5V/V while providing a 600kHz gain bandwidth product.

The MAX4467/MAX4468 feature a complete shutdown, which is active-high, and a shutdown-controlled output providing bias to the microphone. The MAX4465/MAX4467/MAX4469 feature a slew rate suited to voice channel applications. The MAX4466/MAX4468 can be used for full-range audio, e.g., PC99 inputs.

Rail-to-Rail Output Stage

The MAX4465–MAX4469 can drive a $10k\Omega$ load and still typically swing within 16mV of the supply rails. Figure 1 shows the output voltage swing of the MAX4465 configured with Av = ± 10 .

Switched Bias Supply

When used as a microphone amplifier for an electret microphone, some form of DC bias for the microphone is necessary. The MAX4467/MAX4468 have the ability to

turn off the bias to the microphone when the device is in shutdown. This can save several hundred microamps of supply current, which can be significant in low power applications. The MIC_BIAS pin provides a switched version of VCC to the bias components. Figure 3 shows some typical values.

Driving Capacitive Loads

Driving a capacitive load can cause instability in many op amps, especially those with low quiescent current. The MAX4465/MAX4467/MAX4469 are unity-gain stable for a range of capacitive loads up to 100pF. Figure 4 shows the response of the MAX4465 with an excessive capacitive load.

Applications Information

Shutdown Mode

The MAX4467 and MAX4468 feature a low-power, complete shutdown mode. When SHDN goes high, the supply current drops to 5nA, the output enters a high impedance state and the bias current to the microphone is switched off. Pull SHDN low to enable the amplifier. Do not leave SHDN unconnected. Figure 5 shows the shutdown waveform.

Common-Mode Rejection Ratio

A microphone preamplifier ideally only amplifies the signal present on its input and converts it to a voltage appearing at the output. When used in noninverting mode, there is a small output voltage fluctuation when both inputs experience the same voltage change in the

common mode. The ratio of these voltages is called the common-mode gain. The common-mode rejection ratio is the ratio of differential-mode gain to common-mode gain. The high CMRR properties of the MAX4465–MAX4469 provide outstanding performances when configured as a noninverting microphone preamplifier.

Power-Up

The MAX4465–MAX4469 outputs typically settle within 1µs after power-up. Figure 6 shows the output voltage on power-up.

Power Supplies and Layout

The MAX4465–MAX4469 operate from a single +2.4V to +5.5V power supply. Bypass the power supply with a 0.1µF capacitor to ground. Good layout techniques are necessary for the MAX4465–MAX4469 family. To decrease stray capacitance, minimize trace lengths by placing external components close to the op amp's pins. Surface-mount components are recommended. In systems where analog and digital grounds are available, the MAX4465–MAX4469 should be connected to the analog ground.

Test Circuits/Timing Diagrams

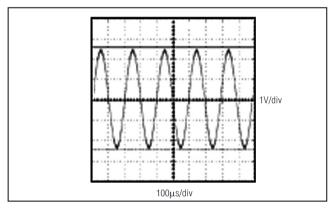


Figure 1. Rail-to-Rail Output Operation

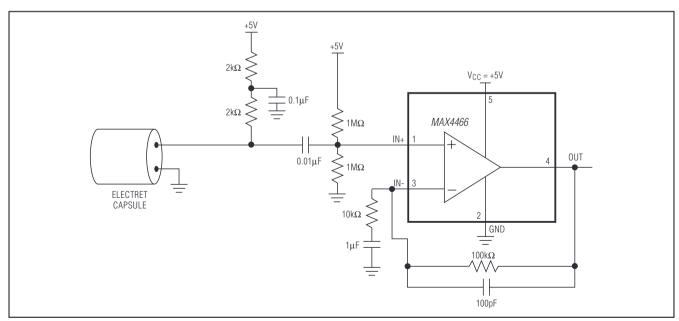


Figure 2. MAX4466 Typical Application Circuit

Test Circuits/Timing Diagrams (continued)

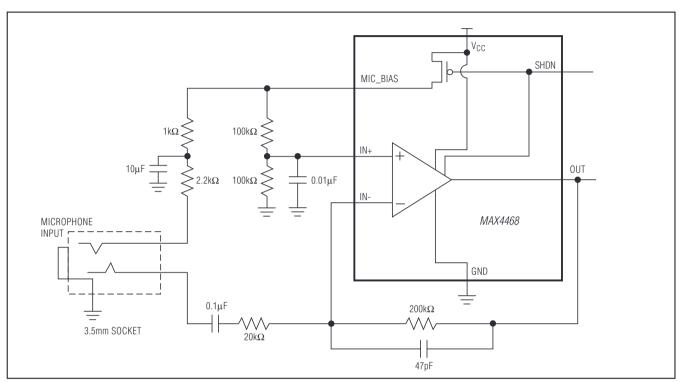


Figure 3. Bias Network Circuit

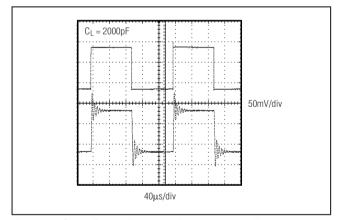


Figure 4. Small-Signal Transient Response with Excessive Capacitive Load

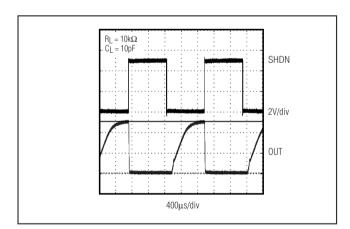


Figure 5. MAX4467/MAX4468 Shutdown Waveform

Test Circuits/Timing Diagrams (continued)

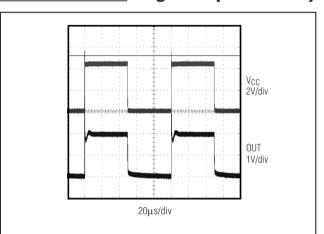


Figure 6. Power-Up/Power-Down Waveform

Chip Information

PROCESS: BICMOS

Ordering Information (continued)

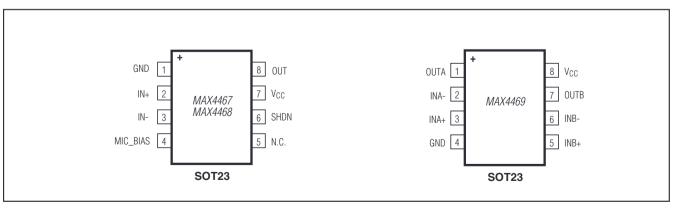
PART	TEMP RANGE	PIN-PACKAGE
MAX4467EKA+T	-40°C to +85°C	8 SOT23
MAX4468EKA+T	-40°C to +85°C	8 SOT23
MAX4469EKA+T	-40°C to +85°C	8 SOT23

⁺Denotes a lead(Pb)-free/RoHS-compliant package.

Selector Guide

PART	MINIMUM STABLE GAIN	EXTERNAL MICROPHONE SHDN	GBWP (kHz)	PIN-PACKAGE
MAX4465	+1	No	200	5 SC70/5 SOT23
MAX4466	+5	No	600	5 SC70/5 SOT23
MAX4467	+1	Yes	200	8 SOT23
MAX4468	+5	Yes	600	8 SOT23
MAX4469	+1	No	200	8 SOT23

Pin Configurations (continued)



Package Information

For the latest package outline information and land patterns (footprints), go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
SOT23-5	U5+1	<u>21-0057</u>	90-0174
SC70	X5+1	<u>21-0076</u>	<u>90-0188</u>
SOT23-8	K8+5	21-0078	90-0176

Revision History

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
2	6/12	Added lead-free packaging information, removed SO packaging information	1, 2, 10