Lab 6: Peripheral Components:

VGA, Mouse, and Dual FPGA

# Design Explanation

## Chip 2 Chip

根據助教於課堂上的解釋，主人的行為可以用以下自動機表示。

Wait request

Send data

Wait ack

Wait to send data

Send button pushed

Receive ack from slave

Counted for 1s

Receive ack from slave

而奴隸的行為可以用以下自動機表示。

Wait request

Wait data

Wait to send ack

Received valid

Received request

奴隸的線路設計如下。











## 拉霸機

The details are down below:

1. Design Specification:
2. Truth Table:
3. Gate Level Circuits:

# Design Verification

## Chip 2 Chip

Enumerate all possible inputs, and verify the outputs manually. The pictures below are the representation with verification.

## 拉霸機

????????

# Contribution

## Lawrence Wu

Chip 2 Chip之設計。

## Ariel Chang

# What have we learned?

* + - 1. 記得要填註解上去，不然接手的人會討厭你
      2. 模組切的好，今晚睡得好；模組沒切好，糾錯糾到老