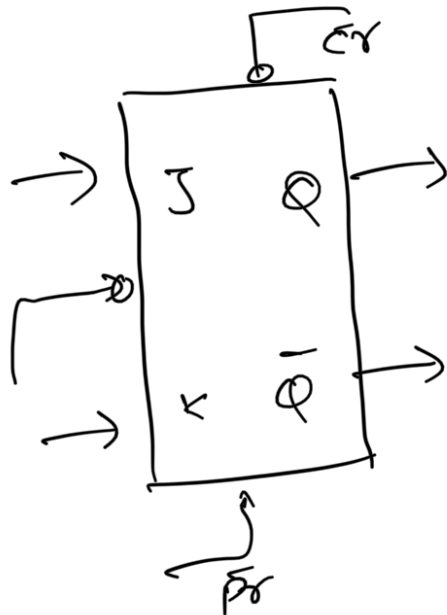


Flip flops

master - slave flip flop:-

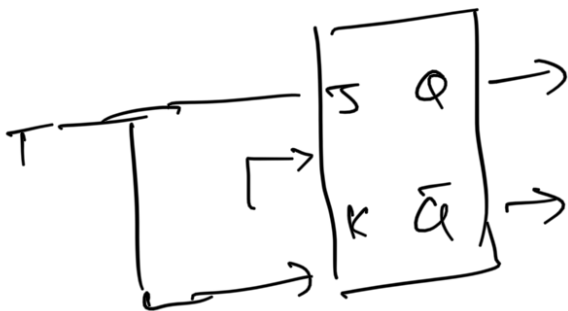


J	K	Q_{n+1}
0	0	Q_n
0	1	0
1	0	1
1	1	\bar{Q}_n

T-Flip Flop:

Toggle
(any number of times)

We get T-f/f by making $J=K$



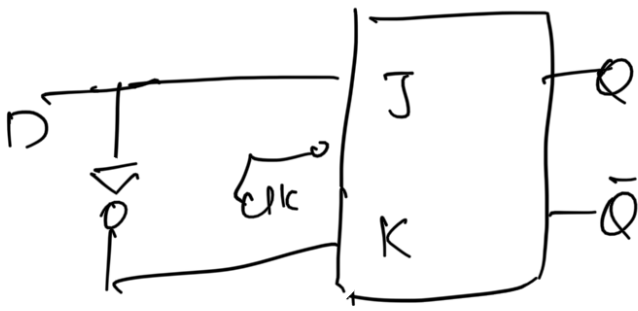
T	Q_{n+1}
0	Q_n
1	\bar{Q}_n

→ Can be used as frequency divider

D-Flip-flop

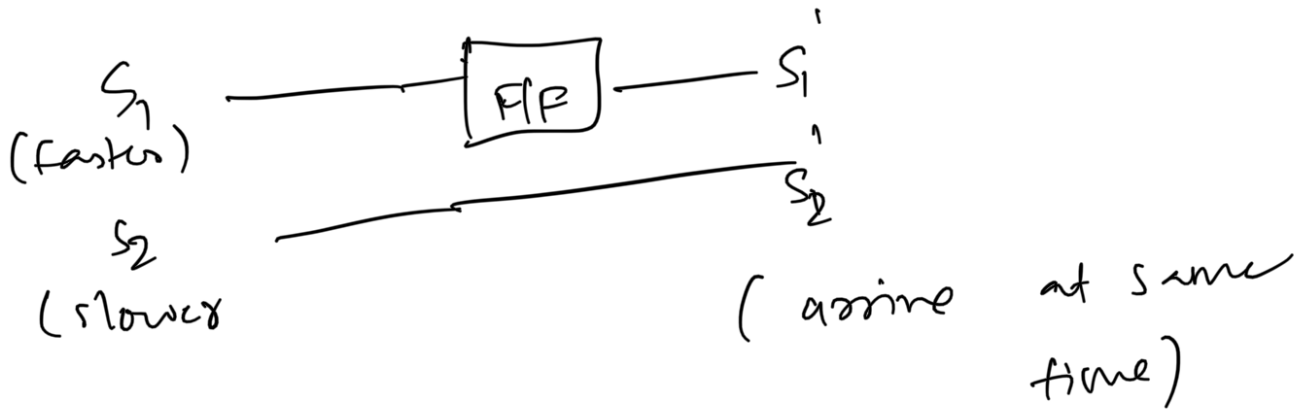
Delay F/F

We get T-f/f by



D	Q _{next}
0	0
1	1

This is used to synchronise two signals



→ used in shift registers

F/F are edge triggered
latches are level triggered