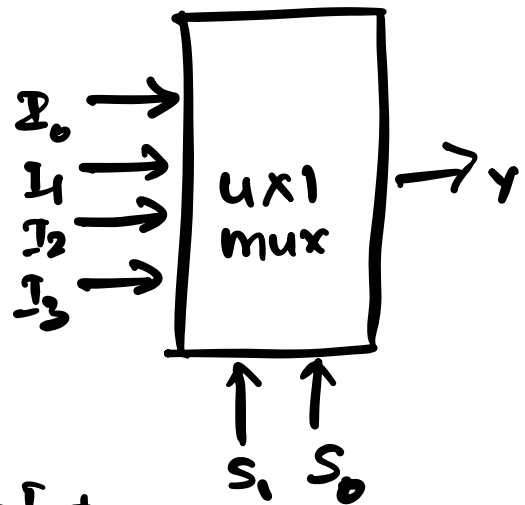


Multiplexers:

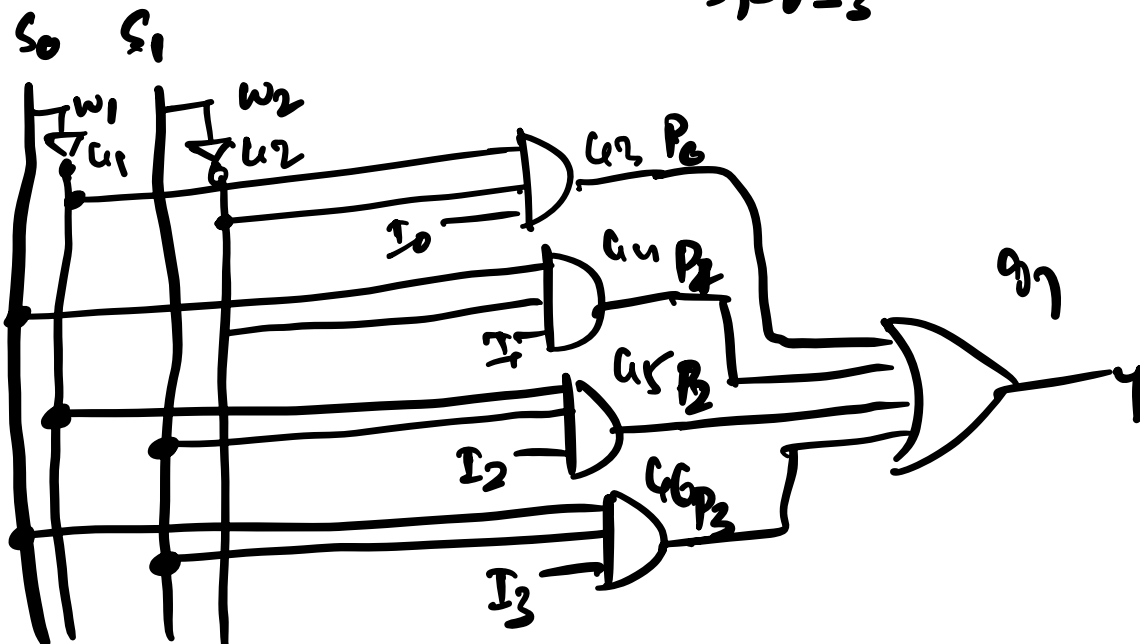
2^n inputs, 1 output, n selection signals

Ex:- 4x1 mux $2^2 \rightarrow$ selection lines
 \nearrow inputs \searrow outputs

S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3



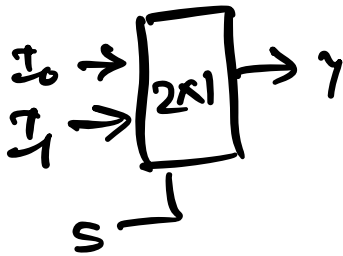
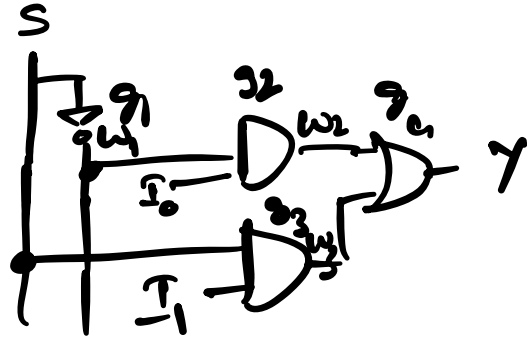
$$Y = \bar{S}_0 \bar{S}_1 I_0 + \bar{S}_0 S_1 I_1 + S_0 \bar{S}_1 I_2 + S_0 S_1 I_3$$

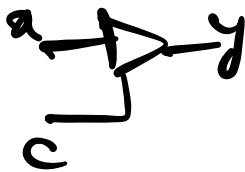


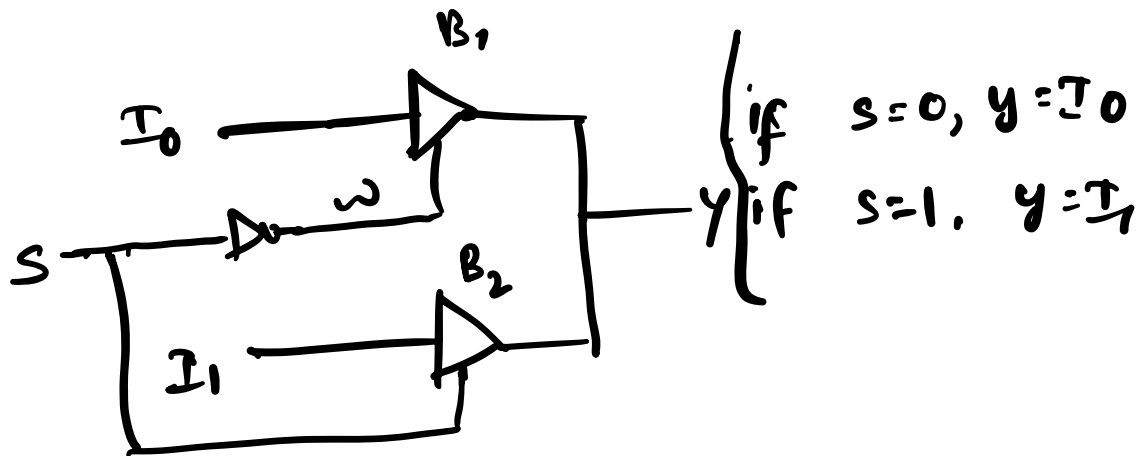
2x1 mux:

S	y
0	I_0
1	I_1

$$y = \bar{S} I_0 + S I_1$$

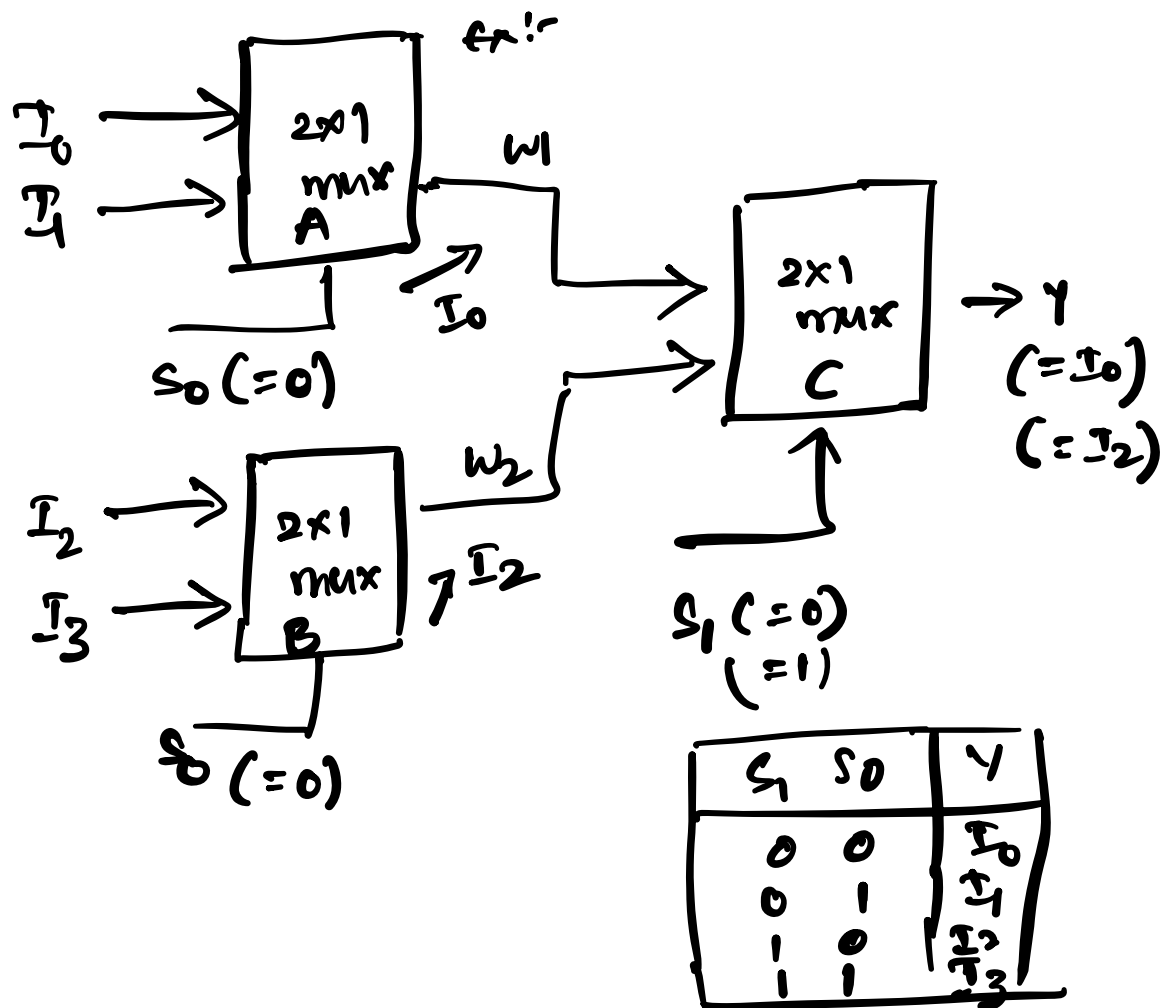


using buffer:  $c=1, y=y$
 $c=0, y=2$ (high impedance)



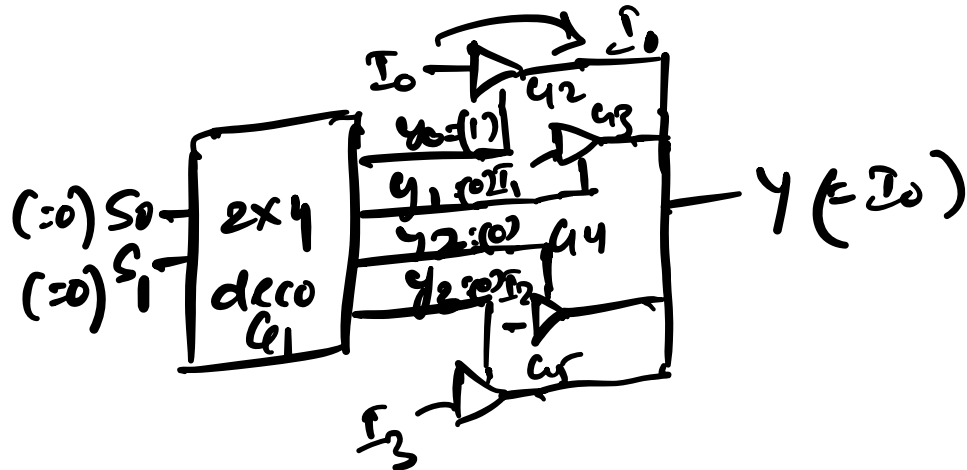
4x1 mux using 2x1 mux:-

we need 3 2x1 mux's:-



4x1 mux using buffer

We use 2x4 decoder



```

module mux_4x1(y, S, I)
output y;
input [3:0] I;
input [1:0] S;
wire [3:0] y;
decoder_2x4 u1([3:0] y, [1:0] S);
bufif u2(y, I0, y[0]);
bufif u3(y, I1, y[1]);
bufif u4(y, I2, y[2]);
bufif u5(y, I3, y[3]);
endmodule.
    
```

8x1 mux using 4x1 mux's

