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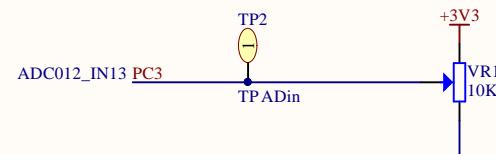
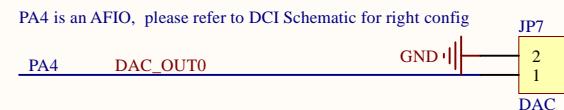
B

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ADC**DAC**

PA4 is an AFIO, please refer to DCI Schematic for right config

Company Name: GigaDevice

File Name: AD_DA

Revision: 1.0

Data: 2016-8

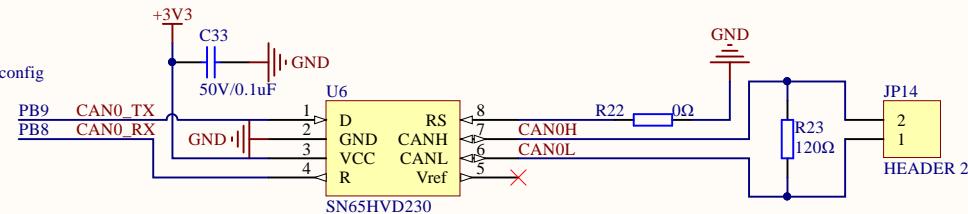
Author: wangzhan

A

A

CAN

PB8, PB9 are AFIOs, please refer to DCI schematic for right config



B

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Company Name: GigaDevice

File Name: CAN

Revision: 1.0

Data: 2016-8

Author: wangzhan

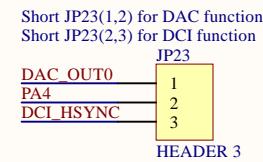
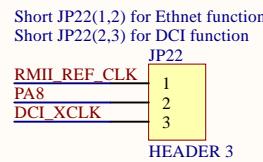
1

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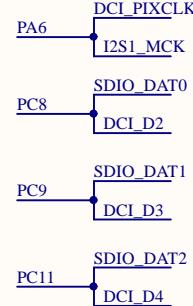
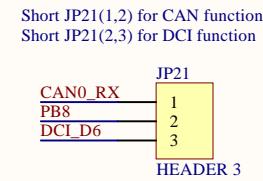
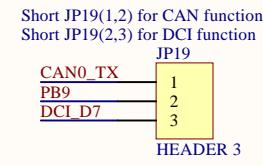
3

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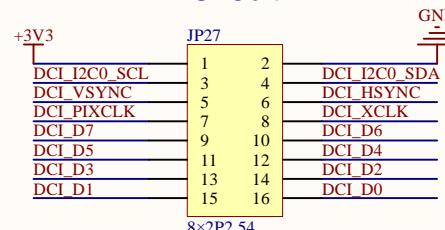


B



PB7	DCI_I2C0_SDA
PB6	DCI_I2C0_SCL
PG9	DCI_VSYNC
PA6	DCI_PIXCLK
PD3	DCI_D5
PC11 R12	0Ω DCI_D4
PC9 R16	0Ω DCI_D3
PC8 R20	0Ω DCI_D2
PC7	DCI_D1
PC6	DCI_D0

DCI 8bit



OV2640

DCI_8bit, TLI and SDRAM can be used at the same time

Company Name: GigaDevice

File Name: DCI

Revision: 1.0

Data: 2016-8

Author: wangzhan

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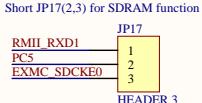
A

A

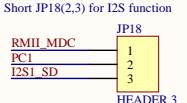
Ethernet

PG11、PG13、PG14 are AFIOs, please refer to SPI schematic for right config

Short JP17(1,2) for Ethernet function
Short JP17(2,3) for SDRAM function



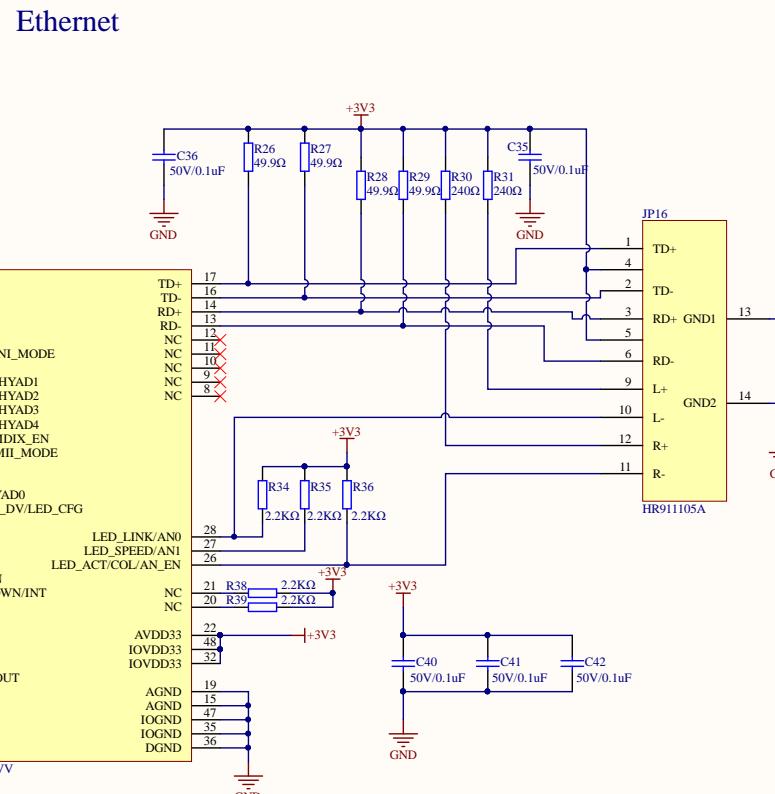
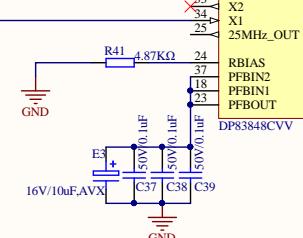
Short JP18(1,2) for Ethernet function
Short JP18(2,3) for I2S function



PG11 RMII_TX_EN
PG13 RMII_RXD0
PG14 RMII_TXD1
PC4 RMII_RXD0
PA7 RMII_CRS_DV
PA2 RMII_MDIO
PA1 RMII_REF_CLK

RP2
RMII_TX_EN
RMII_RXD0
RMII_TXD1
RMII_CRS_DV
RMII_RXD0
RMII_RXD1
RMII_MODE
R32 2.2KΩ
+3V3
U8 TX_CLK
TX_EN
TXD_0
TXD_1
TXD_2
TXD_3/SNI_MODE
RXD_0/PHYAD1
RXD_1/PHYAD2
RXD_2/PHYAD3
RXD_3/PHYAD4
RX_ER/MDIIX_EN
RX_DV/MII_MODE
RX_CLK
COL/PHYAD0
CRS/CRS_DV/LED_CFG
RMII_MDC
RMII_MDIO
NRST
+3V3 R40 2.2KΩ

PA8_MCO RMII_REF_CLK
PA8 is an AFIO, refer to DCI schematic for right config

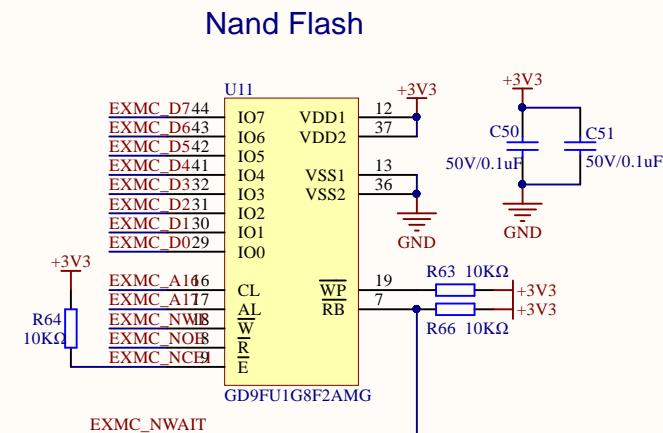


Company Name: GigaDevice

File Name: Ethernet

Revision: 1.0 Data: 2016-8 Author: wangzhan

PD14	EXMC_D0
PD15	EXMC_D1
PD0	EXMC_D2
PD1	EXMC_D3
PE7	EXMC_D4
PE8	EXMC_D5
PE9	EXMC_D6
PE10	EXMC_D7
PD11	EXMC_A16
PD12	EXMC_A17
PD7	EXMC_NCE1
PD4	EXMC_NOE
PD5	EXMC_NWE
PD6	EXMC_NWAIT



Company Name: GigaDevice

File Name: EXMC

Revision: 1.0

Data: 2016-8

Author: wangzhan

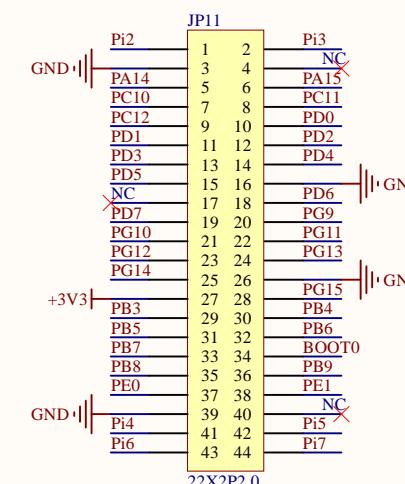
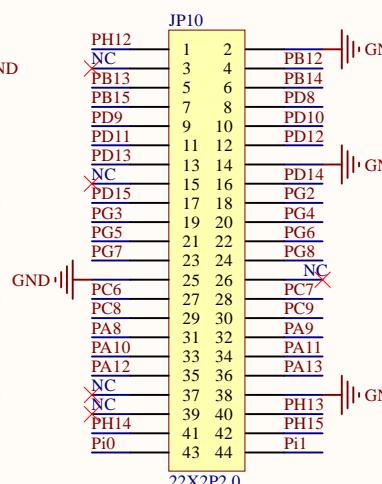
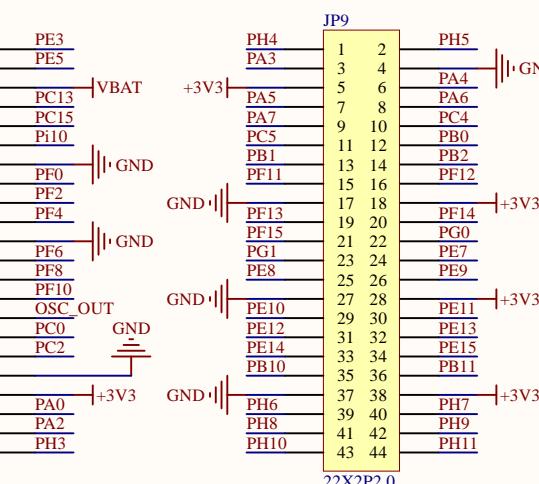
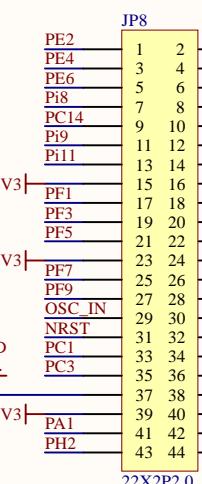
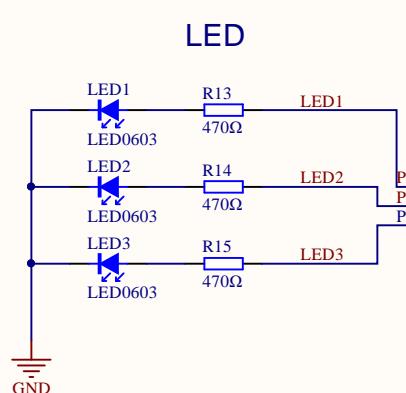
1

2

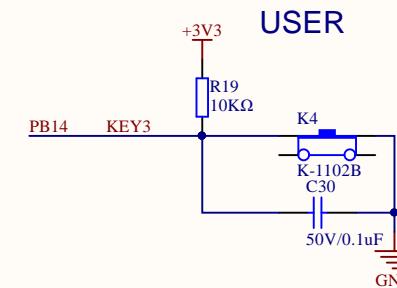
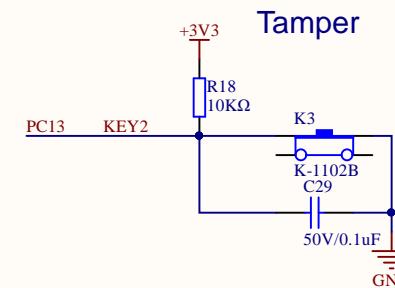
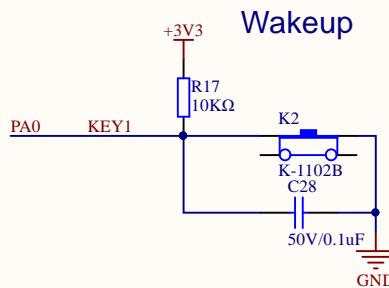
3

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Extension Pin



KEY



Company Name: GigaDevice

File Name: Extension

Revision: 1.0

Data: 2016-8

Author: wangzhan

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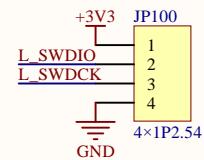
1

2

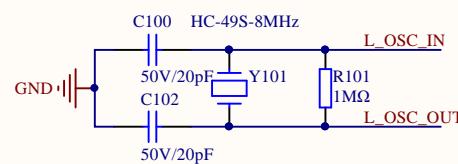
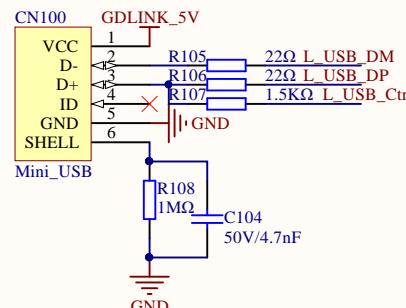
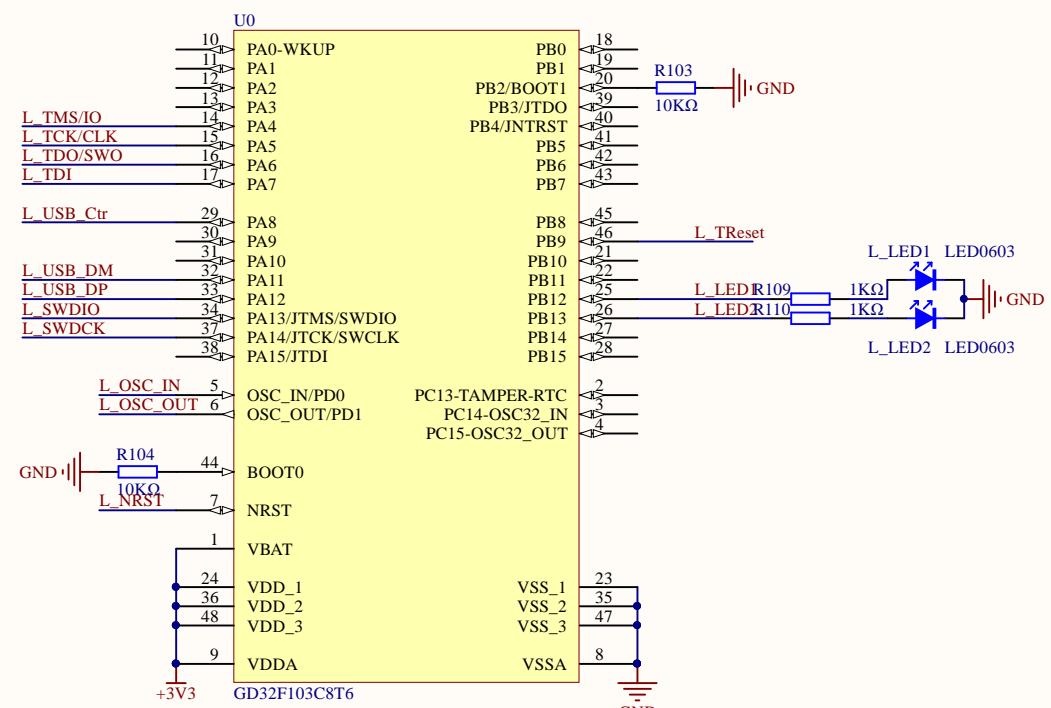
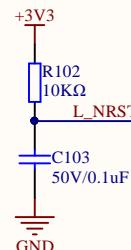
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A

MCU SWD

L_TDI	PA15
L_TMS/IO	PA13
L_TCK/CLK	PA14
L_TDO/SWO	PB3
L_TReset	NRST

HSE**Reset**

Company Name: GigaDevice

File Name: GDLink

Revision: 1.0

Data: 2016-8

Author: XuFei

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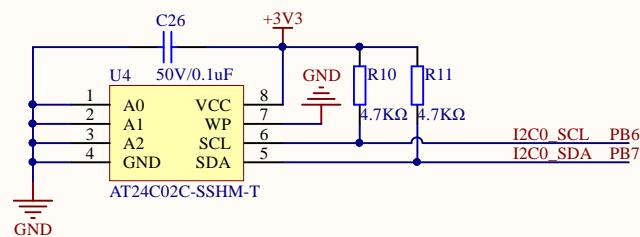
A

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I2C



Company Name: GigaDevice

File Name: I2C

Revision: 1.0

Data: 2016-8

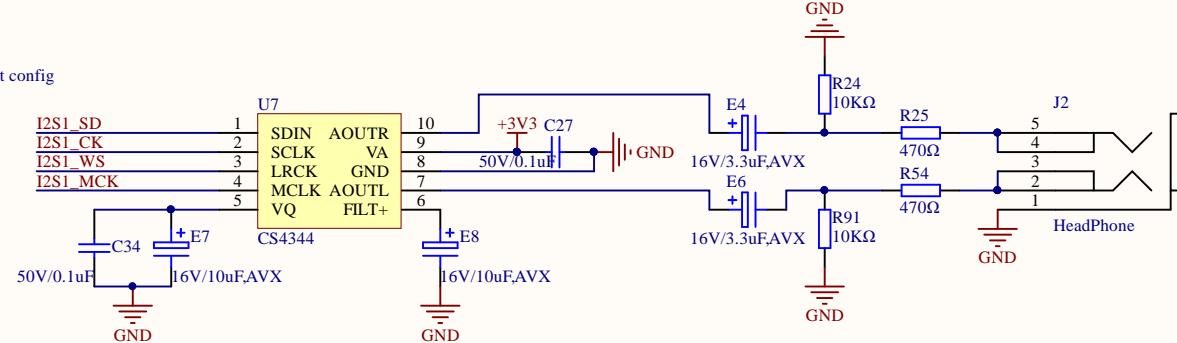
Author: wangzhan

A

I2S

PCI is an AFIO, please refer to ETHNET Schematic for right config

PC1	I2S1_SD
Pi1	R43 0Ω I2S1_CK
Pi0	R44 0Ω I2S1_WS
PA6	R55 0Ω I2S1_MCK



Pi1 → LCD_G6
Pi1 → I2S1_CK

Pi0 → LCD_G5
Pi0 → I2S1_WS

PA6 → DCI_PIXCLK
PA6 → I2S1_MCK

Company Name: GigaDevice

File Name: I2S

Revision: 1.0

Data: 2016-8

Author: wangzhan

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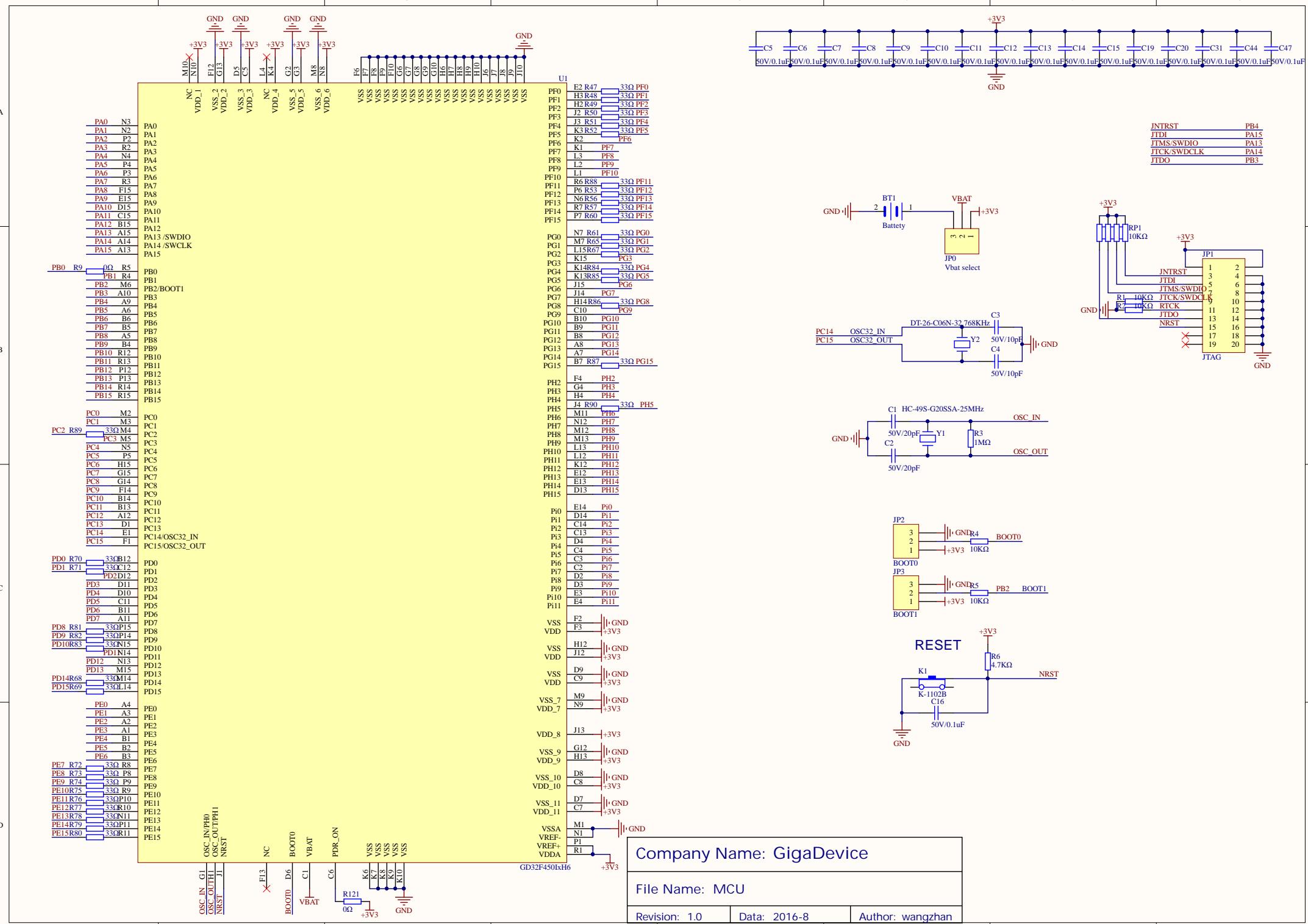
4

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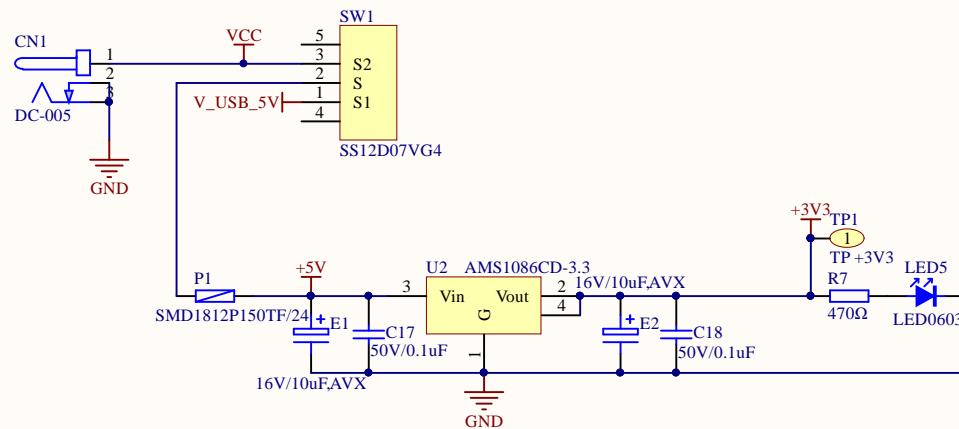
8



A

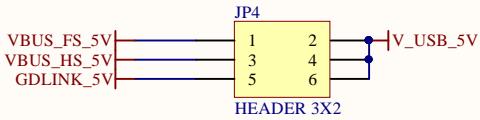
A

POWER

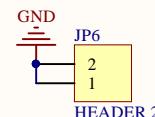


B

B



USB Power Supply selector



Company Name: GigaDevice

File Name: Power

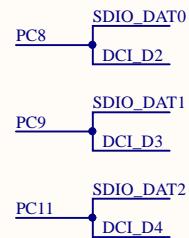
Revision: 1.0

Data: 2016-8

Author: wangzhan

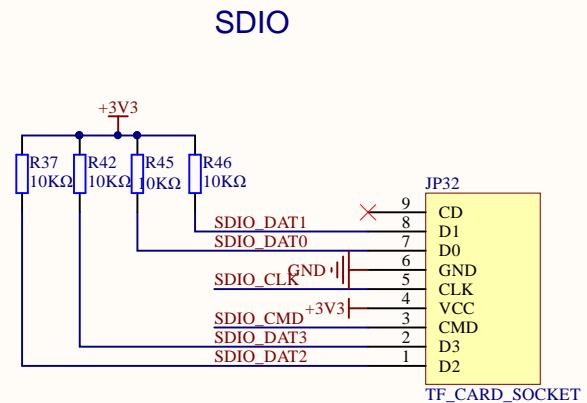
D

D



B

PD2	SDIO_CMD
PC12	SDIO_CLK
PC8	SDIO_DAT0
PC9	SDIO_DAT1
PC10	SDIO_DAT2
PC11	SDIO_DAT3



C

Company Name: GigaDevice

D

File Name: SDIO

Revision: 1.0

Data: 2016-8

Author: wangzhan

SDRAM

A

PF0	EXMC_A0
PF1	EXMC_A1
PF2	EXMC_A2
PF3	EXMC_A3
PF4	EXMC_A4
PF5	EXMC_A5
PF12	EXMC_A6
PF13	EXMC_A7
PF14	EXMC_A8
PF15	EXMC_A9
PG0	EXMC_A10
PG1	EXMC_A11
PG2	EXMC_A12

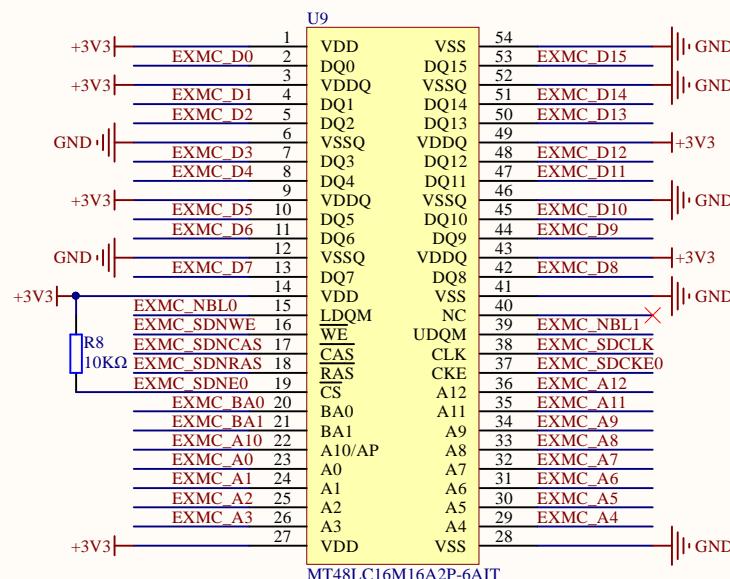
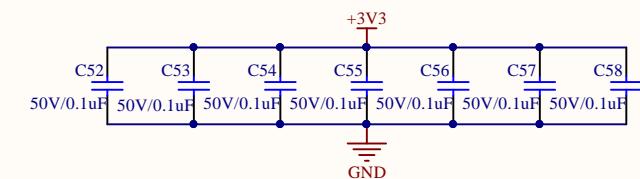
B

PD14	EXMC_D0
PD15	EXMC_D1
PD0	EXMC_D2
PD1	EXMC_D3
PE7	EXMC_D4
PE8	EXMC_D5
PE9	EXMC_D6
PE10	EXMC_D7
PE11	EXMC_D8
PE12	EXMC_D9
PE13	EXMC_D10
PE14	EXMC_D11
PE15	EXMC_D12
PD8	EXMC_D13
PD9	EXMC_D14
PD10	EXMC_D15

PC5 is AFIO, please refer to ETHNET schematic for right config

C

PE0	EXMC_NBL0
PE1	EXMC_NBL1
PC5	EXMC_SDCKE0
PG4	EXMC_BA0
PG5	EXMC_BA1
PG8	EXMC_SDCLK
PG15	EXMC_SDNCAS
PF11	EXMC_SDNRAS
PC2	EXMC_SDNE0
PH5	EXMC_SDNWE



DCI_8bit, TLI and SDRAM can be used at the same time

D

Company Name: GigaDevice

File Name: SDRAM

Revision: 1.0

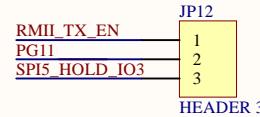
Data: 2016-8

Author: wangzhan

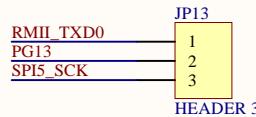
A

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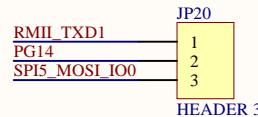
Short JP12(1,2) for Ethernet function
Short JP12(2,3) for SPI & TLI function



Short JP13(1,2) for Ethernet function
Short JP13(2,3) for SPI function

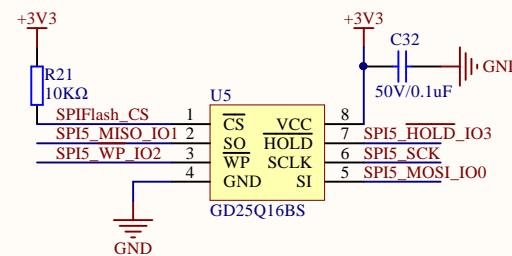
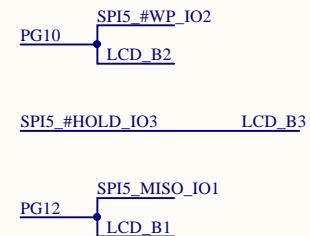


Short JP20(1,2) for Ethernet function
Short JP20(2,3) for SPI function



Standard & Quad SPI Flash

PG12 SPI5_MISO_IO1
PG10 SPI5_WP_IO2
Pi8 SPIFlash_CS



Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2016-8

Author: wangzhan

PG10、PG11、PG12 are AFIO, please refer to SPI Schematic for right config

Pi3	LCD_Touch_PENIRQ
PF9	LCD_SPI4_MOSI
PF8	LCD_SPI4_MISO
PF7	LCD_SPI4_SCK
PF6	LCD_SPI4_NSS
PB15	LCD_PWM_BackLight
PH7	LCD_Touch_Busy

PH2	LCD_R0
PH3	LCD_R1
PH8	LCD_R2
PH9	LCD_R3
PH10	LCD_R4
PH11	LCD_R5
PH12	LCD_R6
PG6	LCD_R7

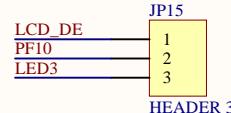
PE5	LCD_G0
PE6	LCD_G1
PH13	LCD_G2
PH14	LCD_G3
PH15	LCD_G4
PI0	LCD_G5
PI1	LCD_G6
PI2	LCD_G7

PE4	LCD_B0
PG12R117	0Ω LCD_B1
PG10R119	0Ω LCD_B2
PG11_R119	0Ω LCD_B3
PI4	LCD_B4
PI5	LCD_B5
PI6	LCD_B6
PI7	LCD_B7

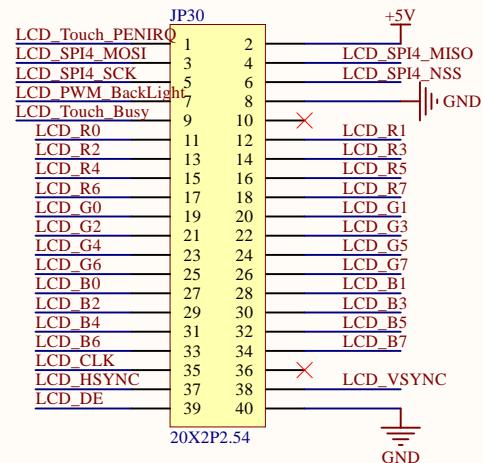
PG7	LCD_CLK
Pi10	LCD_HSYNC
Pi9	LCD_VSYNC

Short JP15(1,2) for TLI function

Short JP15(2,3) for LED function



TLI



DCI_8bit, TLI and SDRAM can be used at the same time

Company Name: GigaDevice

File Name: TLI

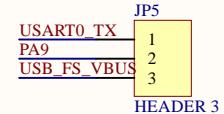
Revision: 1.1

Data: 2021-5

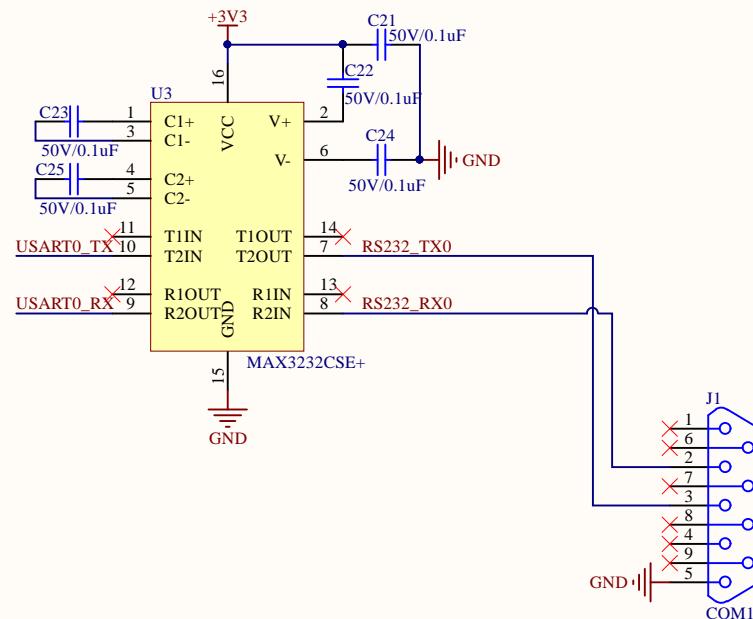
Author: wangzhan

USART0

Short JP5(1,2)for USART0 function
Short JP5(2,3)for USB_FS function



PA10 USART0_RX



Company Name: GigaDevice

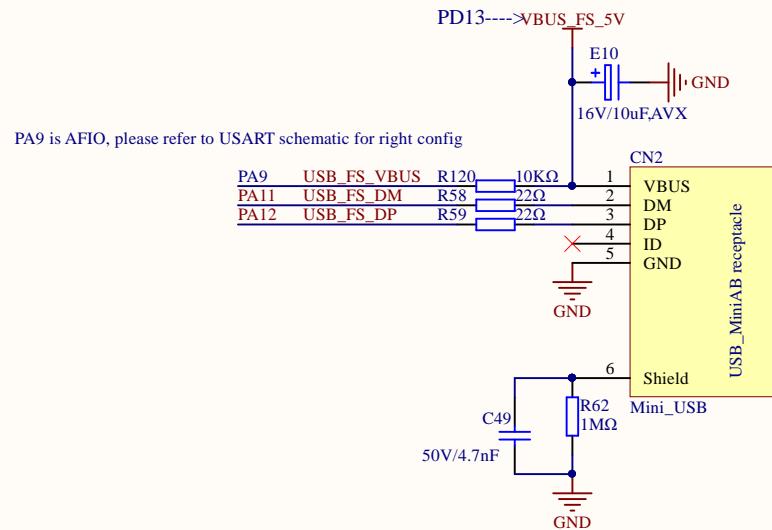
File Name: USART

Revision: 1.0

Data: 2016-8

Author: wangzhan

"VBUS_FS_5V control (active HIGH) " see USB_HS schematic



PA9 is AFIO, please refer to USART schematic for right config

Company Name: GigaDevice

File Name: USB_FS

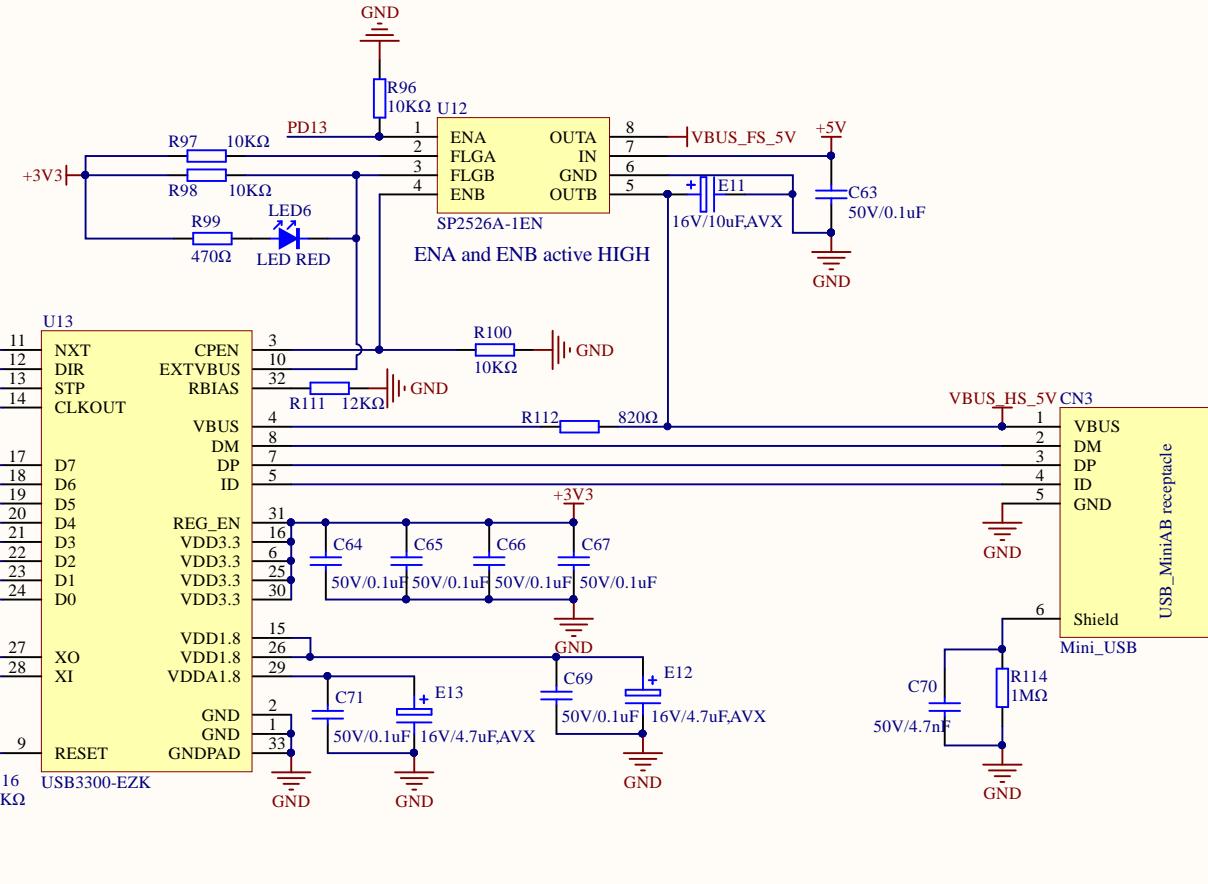
Revision: 1.1

Data: 2016-8

Author: wangzhan

A

USB_HS_ULPI



Company Name: GigaDevice

File Name: USB_HS

Revision: 2.0

Data: 2016-8

Author: wangzhan

