DD?A

Pin	Signal
25	PC[6] ($\overline{\text{RESET}}$/PCINT14)
26	PD[0] (RXD/PCINT16)
27	PD[1] (TXD/PCINT17)
28	PD[2] (INT0/PCINT18)
1	PD[3] (PCINT19/OC2B/INT1)
2	PD[4] (PCINT20/XCK/T0)
7	PD[5] (PCINT21/OC0B/T1)
8	PD[6] (PCINT22/OC0A/AIN0)
9	PD[7] (PCINT23/AIN1)
4	GND
18	GND

Signal	Pin
(PCINT0/CLKO/ICP1) PB[0]	10
(PCINT1/OC1A) PB[1]	11
(PCINT2/$\overline{\text{SS}}$/OC1B) PB[2]	12
(PCINT3/OC2A/MOSI) PB[3]	13
(PCINT4/MISO) PB[4]	14
(SCK/PCINT5) PB[5]	15
(PCINT6/XTAL1/TOSC1) PB[6]	5
(PCINT7/XTAL2/TOSC2) PB[7]	6
VCC	3
AVCC	16
AREF	17

DD?B

Pin	Signal
19	PC[0] (ADC0/PCINT8)
20	PC[1] (ADC1/PCINT9)
21	PC[2] (ADC2/PCINT10)
22	PC[3] (ADC3/PCINT11)
23	PC[4] (ADC4/SDA/PCINT12)
24	PC[5] ADC5/SCL/PCINT13