

RELEASED 28-APRIL-2022

Page	Index	Page	Index	Page	Index	Page	Index
1	cover_page.SchDoc	11	LED.SchDoc	21	31
2	revision_history.SchDoc	12	pic_blk.SchDoc	22	32
3	top_dsn.SchDoc	13	3V3_dcdc_blk.SchDoc	23	33
4	JN5169.SchDoc	14	5V_dcdc_blk.SchDoc	24	34
5	Module.SchDoc	15	25	35
6	crypto_blk.SchDoc	16	26	36
7	bbb_blk.SchDoc	17	27	37
8	headerP3_blk.SchDoc	18	28	38
9	USB_Mini.SchDoc	19	29	39
10	PowerInFilter.SchDoc	20	30	40

Title: cover_page.SchDoc		 wirelessforbusiness www.wi4b.com
Sheet: 1 of 14	Project: Wilamp_DCU.PrjPCB	
Revision: V2.0	Date: 4/28/2022	Engineer: JBayalas
Wi4B S.r.l		

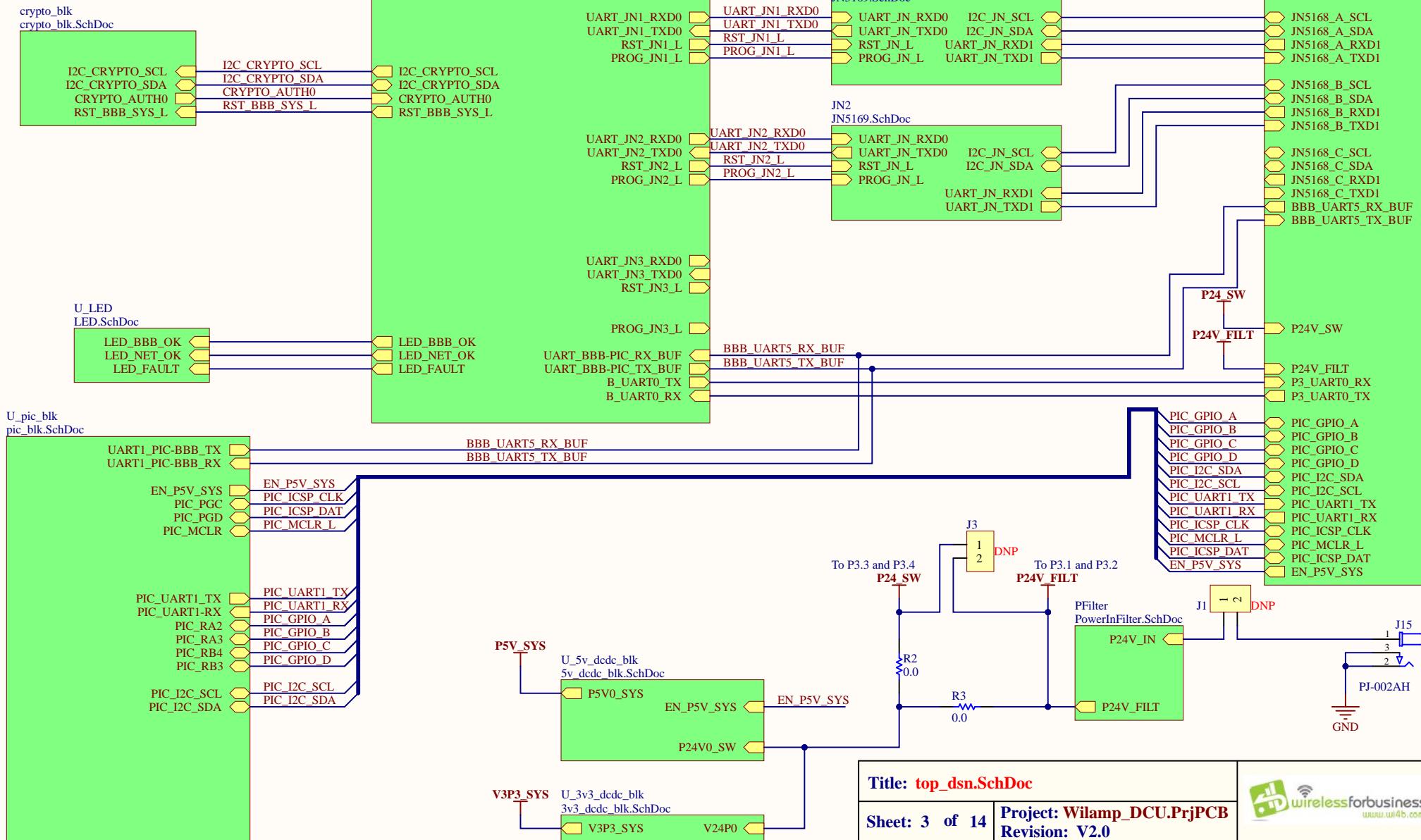
DOC: REVISION HISTORY

Revision	Changes	Engineer
1.0	First Release	JBayalas
1.1	-added P5V_SYS to U16B P9.5 and P9.6 pins -U16C J1.4 and J1.5 connected to P3 pin 41 and 42	JBayalas
2.0	-change JN5168 to JN5169 -JN5169 module 3 removed -some components marked as DNP	JBayalas

Title: revision_history.SchDoc		 wirelessforbusiness www.wi4b.com
Sheet: 2 of 14	Project: Wilamp_DCU.PrjPCB Revision: V2.0	
Date: 4/28/2022	Engineer: JBayalas	Wi4B S.r.l

Wilamp DCU

Version 2.0



Title: top_dsn.SchDoc

Sheet: 3 of 14

Project: Wilamp_DCU.PrjPCB
Revision: V2.0

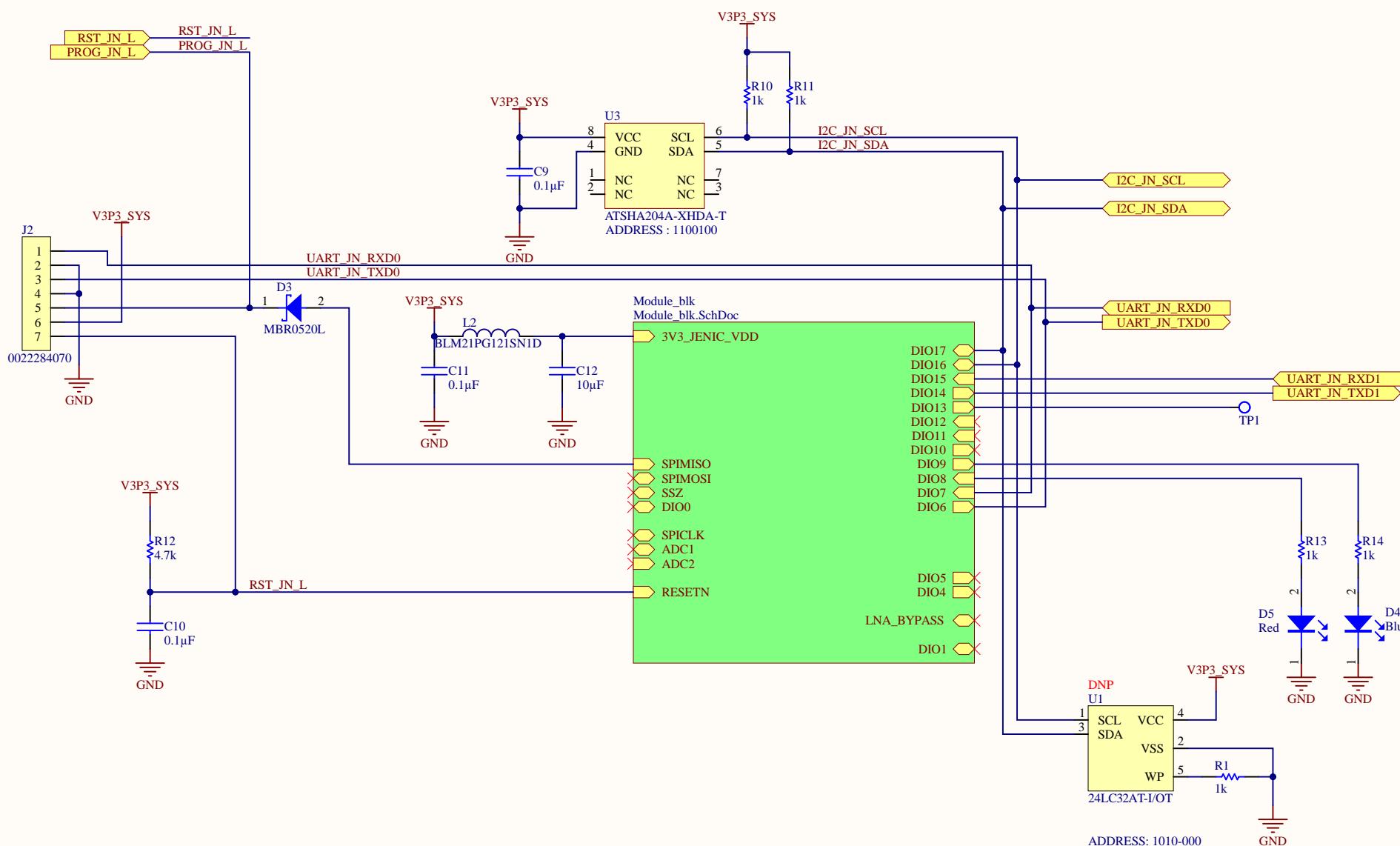
Date: 4/28/2022 Engineer: JBayalas



Wi4B S.r.l

A

A



Title: JN5169.SchDoc

Sheet: 4 of 14

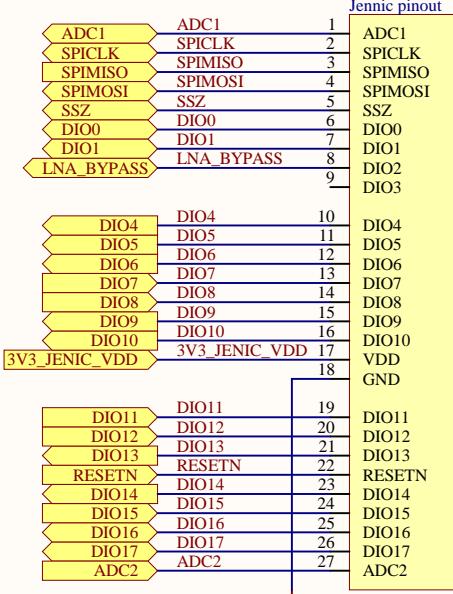
Project: Wilamp_DCU.PrjPCB
Revision: V2.0

Date: 4/28/2022 Engineer: JBayalas

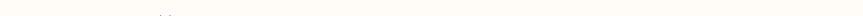
Wi4B S.r.l

1 2 3 4

A



1 2 3



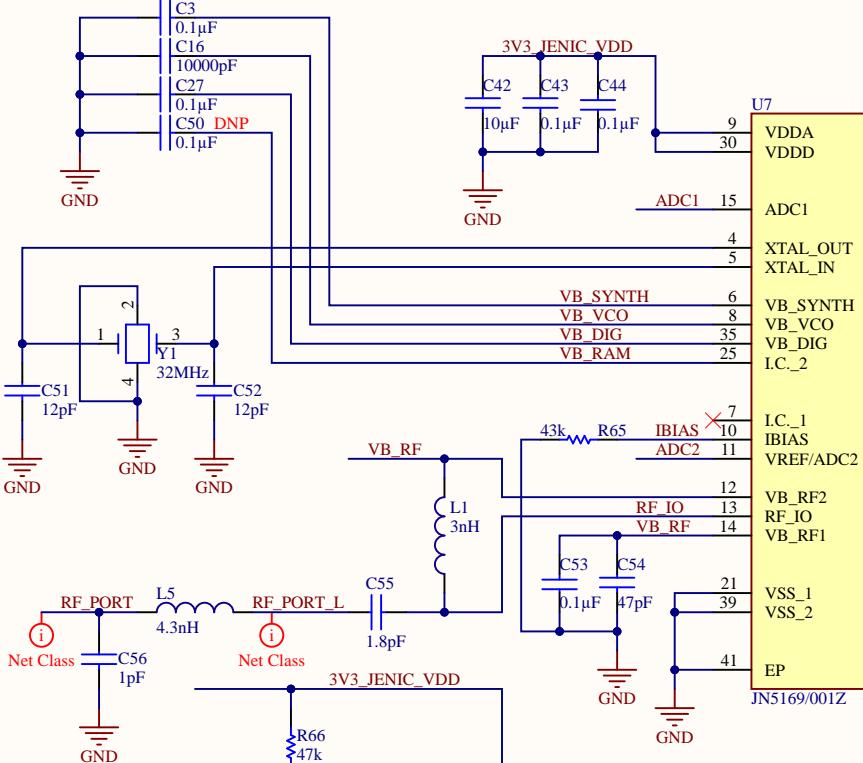
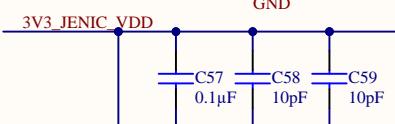
4

20 SPICLK
22 SPIMISO
23 SPIMOSI
24 SSZ

16 DIO0
17 DIO1
18 DIO2
19 DIO3
26 DIO4
27 DIO5
28 DIO6
29 DIO7
31 DIO8
32 DIO9
33 DIO10
34 DIO11
36 DIO12
37 DIO13
38 DIO14
40 DIO15
1 DIO16
2 DIO17

3 RESETN

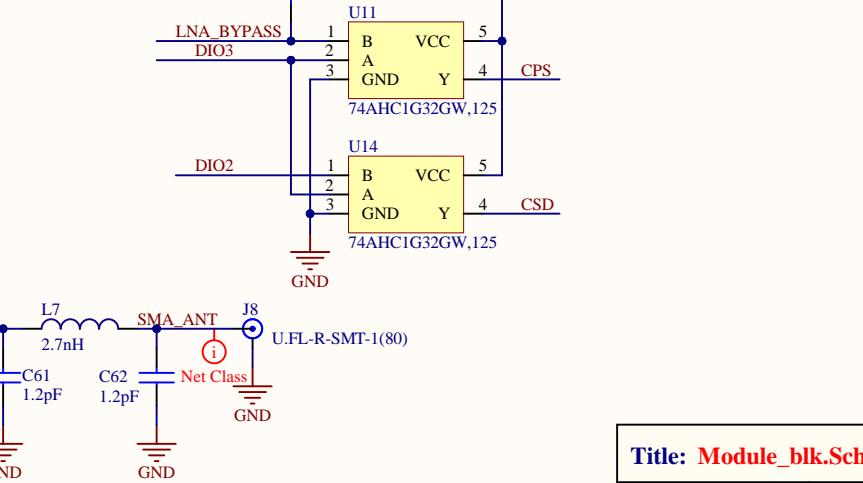
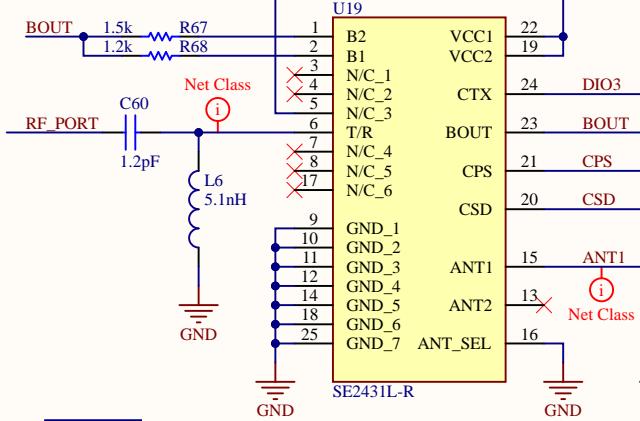
B



DO0/SPICLK/PWM2
DO1/SPIMISO/PWM3
DIO18/SPIMOSI
DIO19/SPISEL0

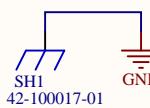
DIO0/ADO/SPISEL1/ADC3
DIO1/ADE/SPISEL2/ADC4/PC0
DIO2/RFTX/TIM0CK_GT/ADC5
DIO3/RFTX/TIM0CAP/ADC6
DIO4/CTS0/JTAG_TCK/TIMOOUT/PC0
DIO5/RTS0/JTAG_TMS/PWM1/PC1
DIO6/TXD0/JTAG_TDO/PWM2
DIO7/RXD0/JTAG_TDI/PWM3
DIO8/TIM0CK_GT/PC1/PWM4
DIO9/TIM0CAP/32KXTALIN/RXD1/32KIN
DIO10/TIM0OUT/32KXTALOUT
DIO11/PWM1/TXD1
DIO12
DIO13
DIO14
DIO15
DIO16/SPISMOSI/SIF_CLK/COMP1P
DIO17/SPISMOSI/SIF_D/COMP1M/PWM4

C



4

D



Title: Module_blk.SchDoc



Sheet: 5 of 14

Project: Wilamp_DCU.PrjPCB
Revision: V2.0

Date: 4/28/2022 Engineer: JBayalas

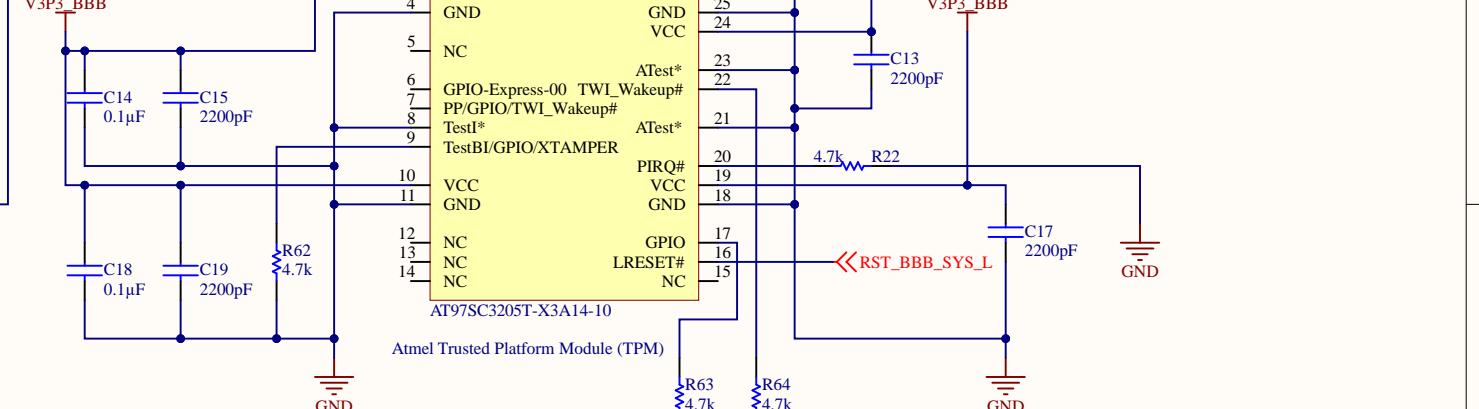
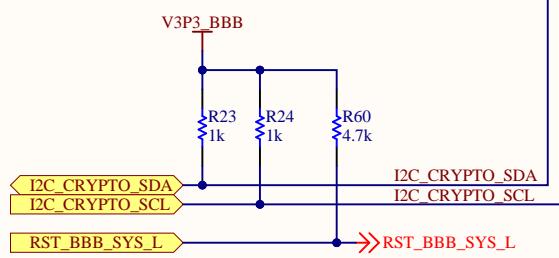
WI4B S.r.l

1 2 3 4

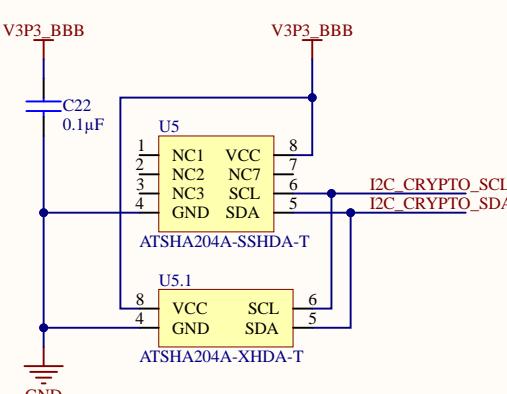
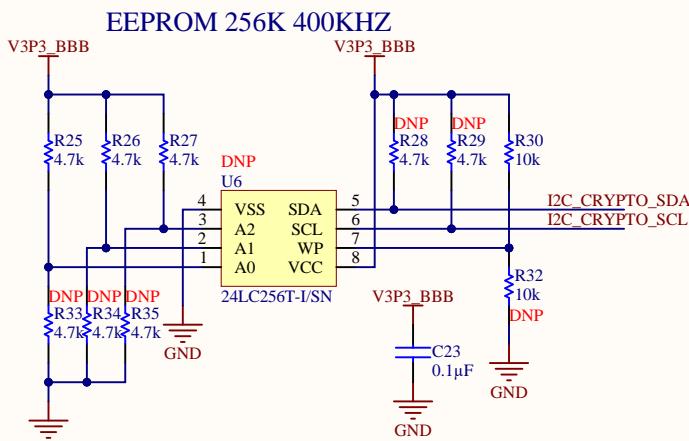
I2C Devices

1 2 3 4

A

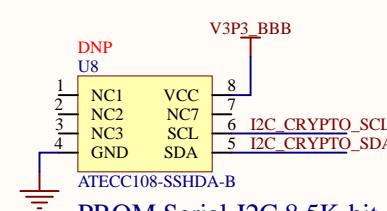
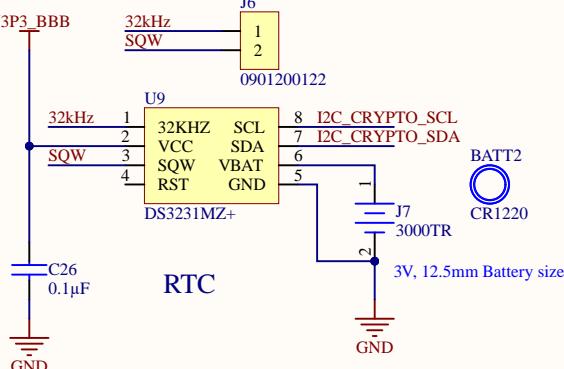


B



AUTHENTICATION CHIP

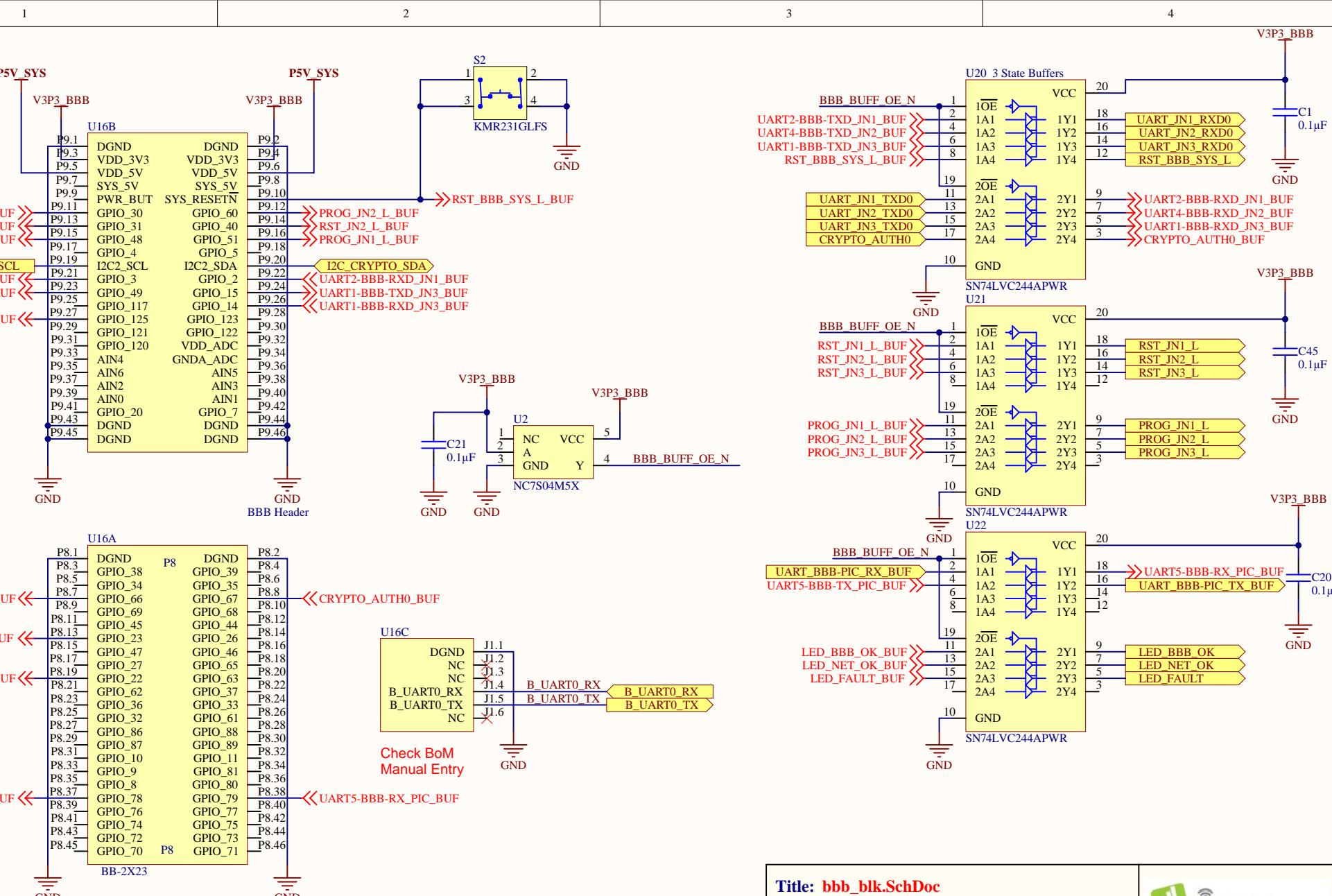
C



D



Title: crypto_blk.SchDoc		
Sheet: 6 of 14	Project: Wilamp_DCU.PrjPCB	Revision: V2.0
Date: 4/28/2022	Engineer: JBayalas	WI4B S.r.l



Title: **bbb_blk.SchDoc**

Sheet: **7 of 14** Project: **Wilamp_DCU.PrjPCB**
Revision: **V2.0**



Date: **4/28/2022** Engineer: **JBayalas**

Wi4B S.r.l

A

A

B

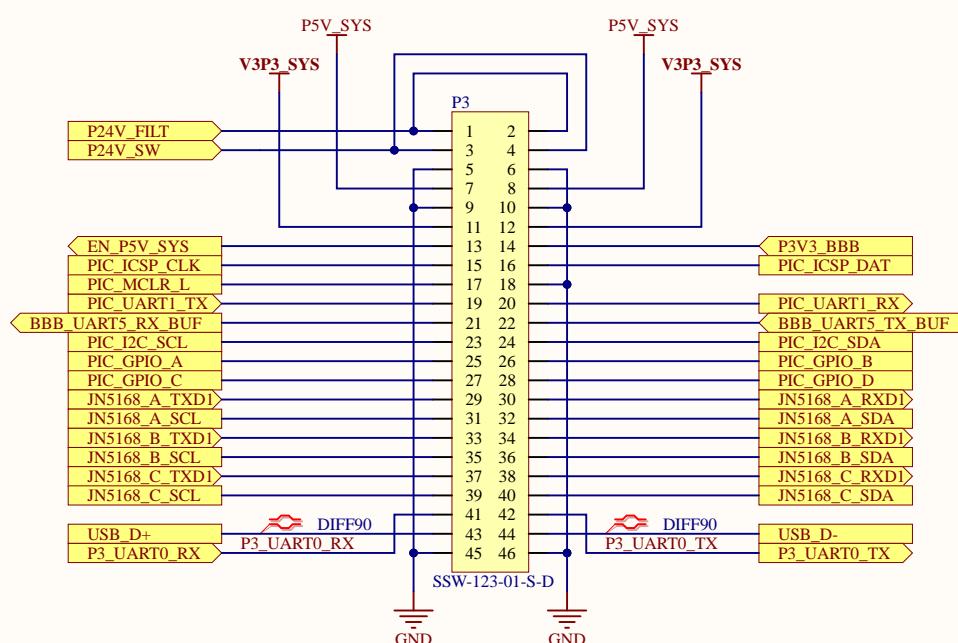
B

C

C

D

D



Title: headerP3_blk.SchDoc



Sheet: 8 of 14

Project: Wilamp_DCU.PrjPCB
Revision: V2.0

Date: 4/28/2022 Engineer: JBayalas

Wi4B S.r.l

A

A

B

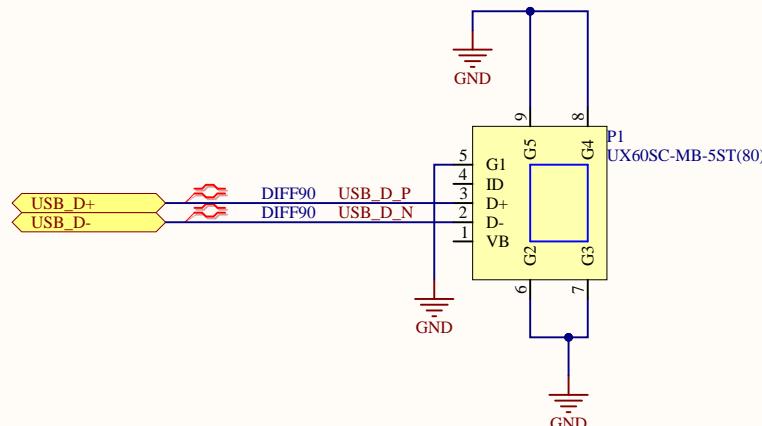
B

C

C

D

D



Title: **USB_Mini.SchDoc**



Sheet: **9 of 14**

Project: **Wilamp_DCU.PrjPCB**
Revision: **V2.0**

Date: **4/28/2022** Engineer: **JBayalas**

Wi4B S.r.l

A

A

B

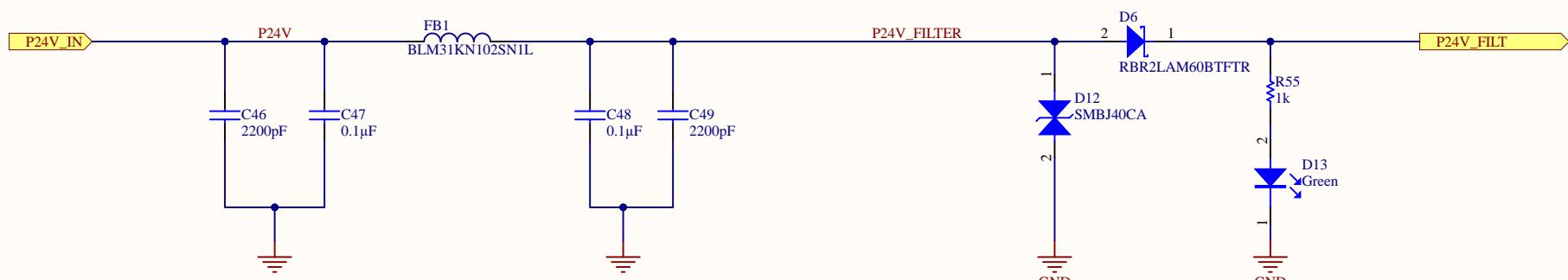
B

C

C

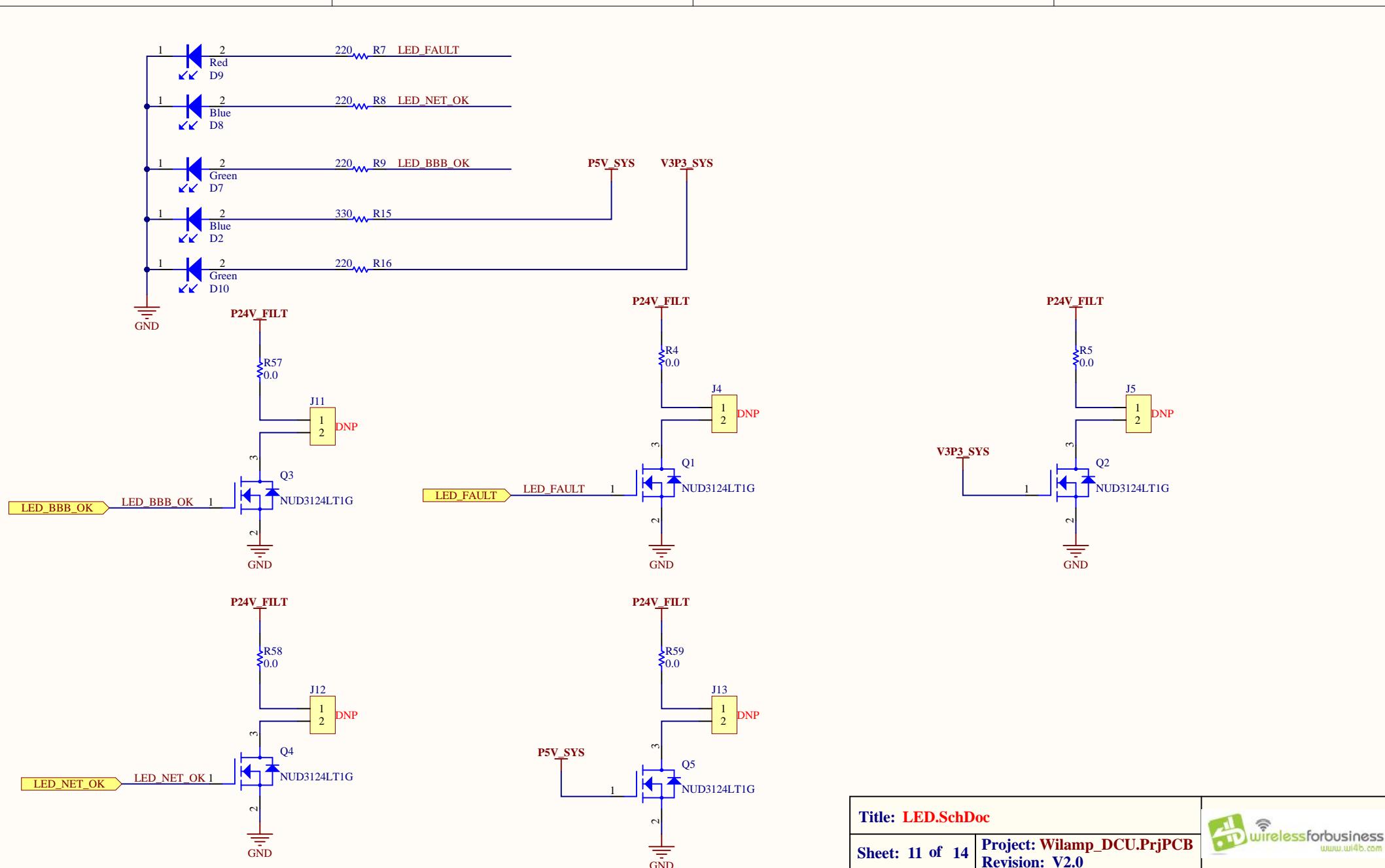
D

D



Title: PowerInFilter.SchDoc	
Sheet: 10 of 14 Project: Wilamp_DCU.PrjPCB	
Revision: V2.0	
Date: 4/28/2022	Engineer: JBayalas





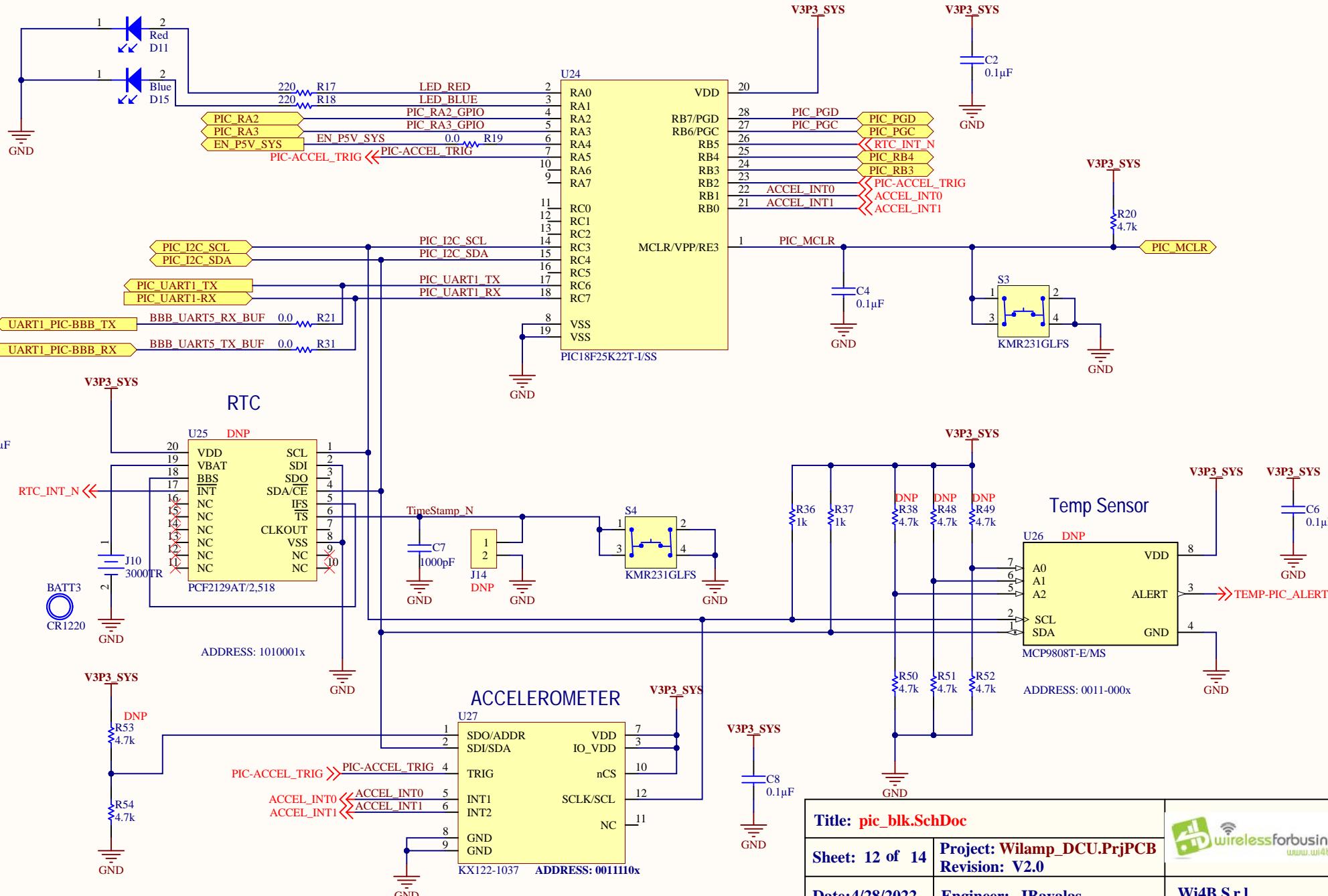
Title: LED.SchDoc

Sheet: 11 of 14

Project: Wilamp_DCU.PrjPCB
Revision: V2.0

Date: 4/28/2022 Engineer: JBayalas

Wi4B S.r.l



Title: [pic_hlk_SchDoc](#)

Sheet: 13 of 14

Sheet: 12 of 14 Revision: V2.0

Date: 4/28/2022



A

B

C

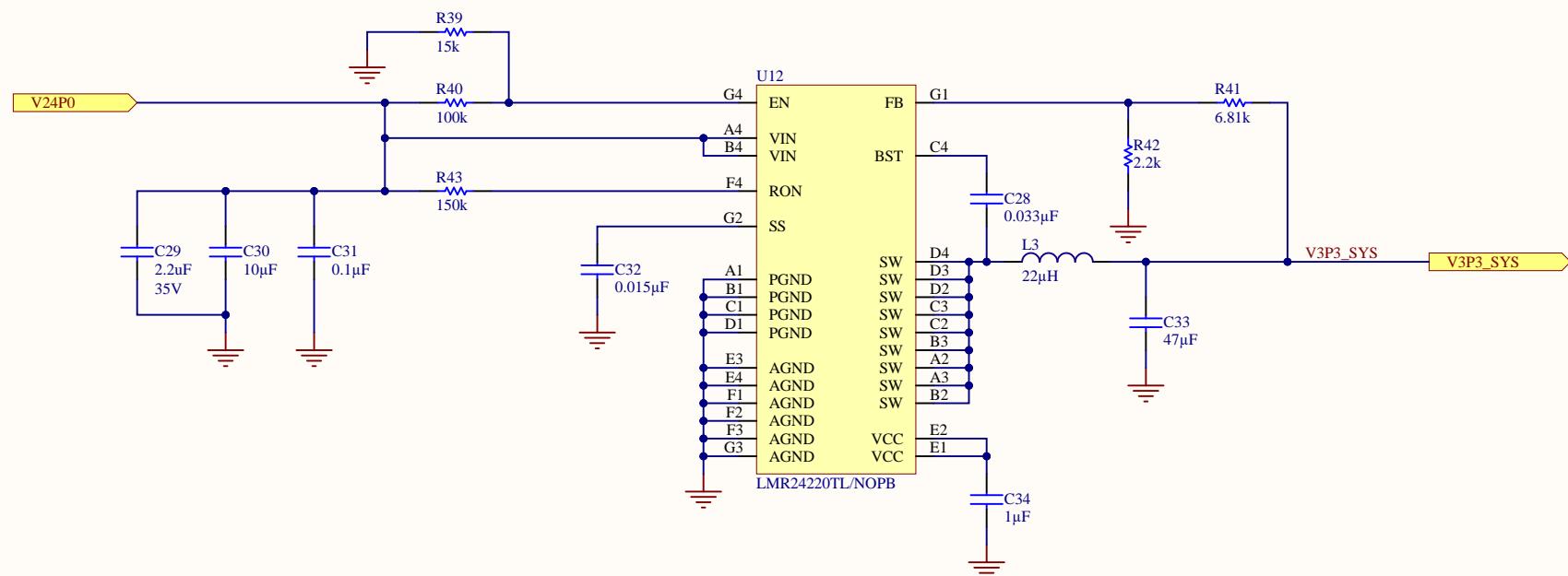
D

A

B

C

D



Title: 3v3_dcde_blk.SchDoc	
Sheet: 13 of 14 Project: Wilamp_DCU.PrjPCB	
Revision: V2.0	
Date: 4/28/2022	Engineer: JBayalas

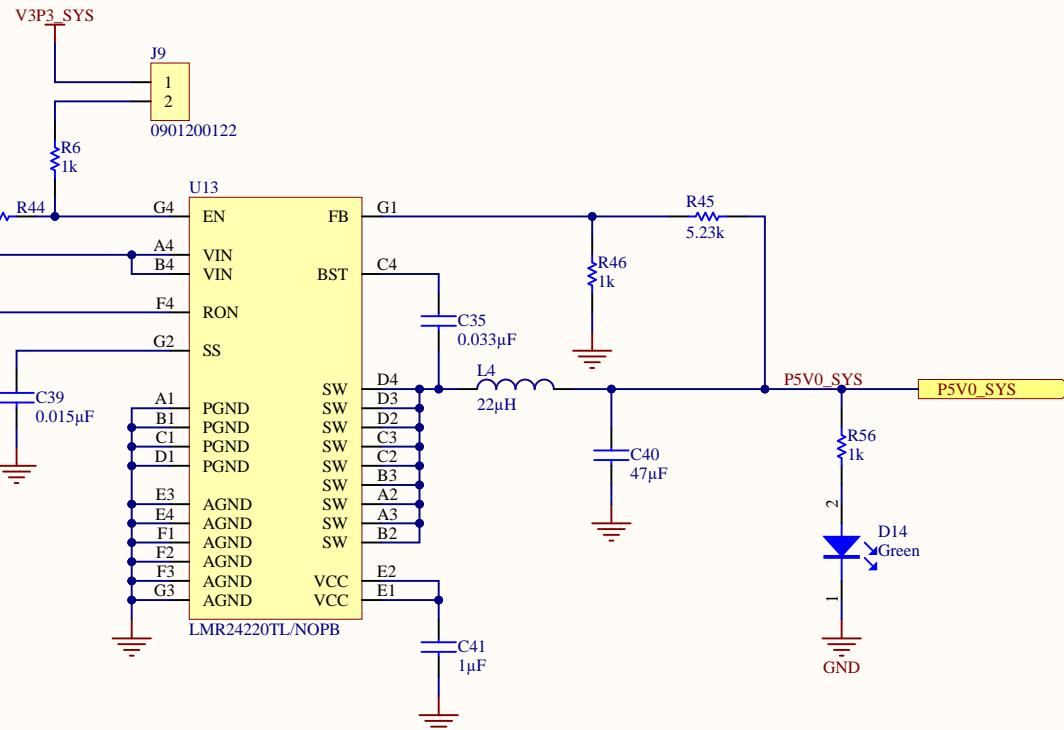


A

B

C

D



Title: 5v_dcde_blk.SchDoc

Sheet: 14 of 14 Project: Wilamp_DCU.PrjPCB
Revision: V2.0

Date: 4/28/2022 Engineer: JBayalas

Wi4B S.r.l.