


RELEASED 28-APRIL-2022

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DOC: REVISION HISTORY

Revision	Changes	Engineer
1.0	First Release	JBayalas
1.1	-added P5V_SYS to U16B P9.5 and P9.6 pins -U16C J1.4 and J1.5 connected to P3 pin 41 and 42	JBayalas
2.0	-change JN5168 to JN5169 -JN5169 module 3 removed -some components marked as DNP	JBayalas

Title: revision_history.SchDoc		
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Date:4/28/2022	Engineer: JBayalas	
		Wi4B S.r.l

Wilamp DCU

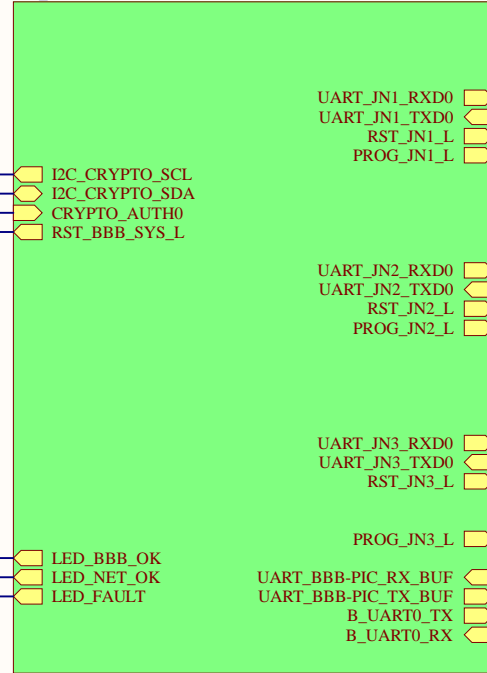
Version 2.0

crypto_blk
crypto_blk.SchDoc



I2C_CRYPTO_SCL
I2C_CRYPTO_SDA
CRYPTO_AUTH0
RST_BBB_SYS_L

bbb_conn_blk
bbb_blk.SchDoc



UART_JN1_RXD0
UART_JN1_TXD0
RST_JN1_L
PROG_JN1_L

UART_JN2_RXD0
UART_JN2_TXD0
RST_JN2_L
PROG_JN2_L

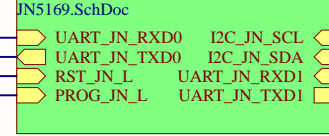
UART_JN3_RXD0
UART_JN3_TXD0
RST_JN3_L

PROG_JN3_L

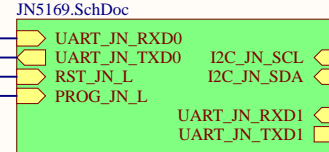
UART_BBB-PIC_RX_BUF
UART_BBB-PIC_TX_BUF
B_UART0_TX
B_UART0_RX

BBB_UART5_RX_BUF
BBB_UART5_TX_BUF

JN1
JN5169.SchDoc



JN2
JN5169.SchDoc

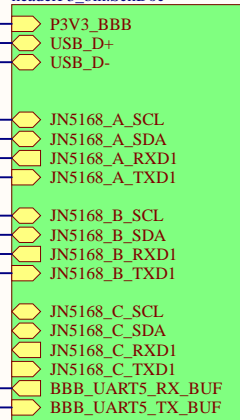


U_USB_Mini
USB_Mini.SchDoc



V3P3_BBB

U_headerP3_blk
headerP3_blk.SchDoc



P24_SW

P24V_FILT

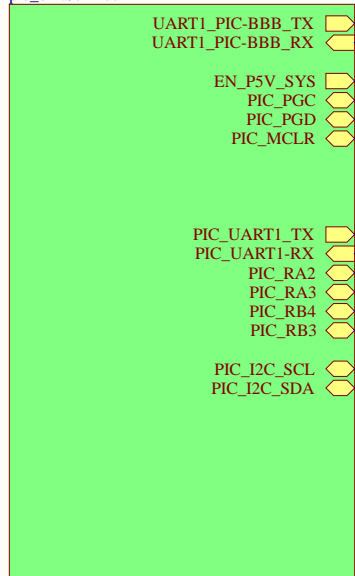
P24V_SW

P24V_FILT

P3_UART0_RX

P3_UART0_TX

U_pic_blk
pic_blk.SchDoc



EN_P5V_SYS
PIC_PGC
PIC_PG0
PIC_MCLR

PIC_UART1_TX
PIC_UART1_RX
PIC_RA2
PIC_RA3
PIC_RB4
PIC_RB3

PIC_I2C_SCL
PIC_I2C_SDA

EN_P5V_SYS
PIC_ICSP_CLK
PIC_ICSP_DAT
PIC_MCLR_L

PIC_UART1_TX
PIC_UART1_RX
PIC_GPIO_A
PIC_GPIO_B
PIC_GPIO_C
PIC_GPIO_D

PIC_I2C_SCL
PIC_I2C_SDA

PIC_GPIO_A
PIC_GPIO_B
PIC_GPIO_C
PIC_GPIO_D
PIC_I2C_SCL
PIC_I2C_SDA
PIC_UART1_TX
PIC_UART1_RX
PIC_ICSP_CLK
PIC_ICSP_DAT
EN_P5V_SYS

J3

1 DNP
2 DNP

To P3.3 and P3.4

P24_SW

To P3.1 and P3.2

P24V_FILT

PFilter
PowerInFilter.SchDoc

P24V_IN

P24V_FILT

J1

1 DNP
2 DNP

J15

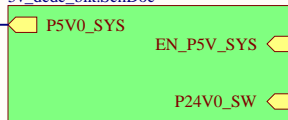
1
2
3

PJ-002AH

GND

P5V_SYS

U_5v_dcde_blk
5v_dcde_blk.SchDoc



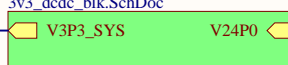
P5V0_SYS

EN_P5V_SYS

P24V0_SW

V3P3_SYS

U_3v3_dcde_blk
3v3_dcde_blk.SchDoc



V3P3_SYS

V24P0

Title: top_dsn.SchDoc

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Project: Wilamp_DCU.PrjPCB

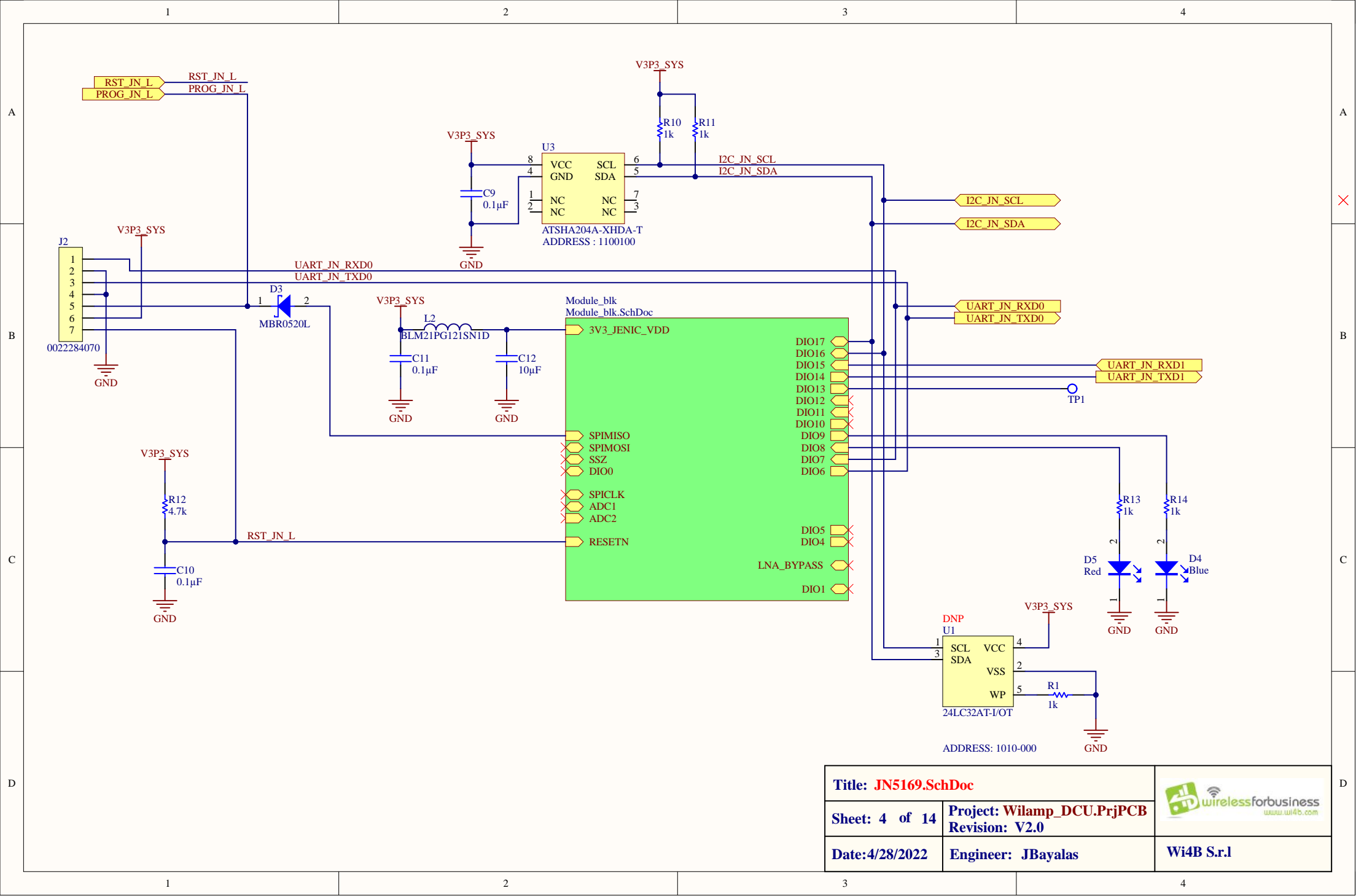
Revision: V2.0

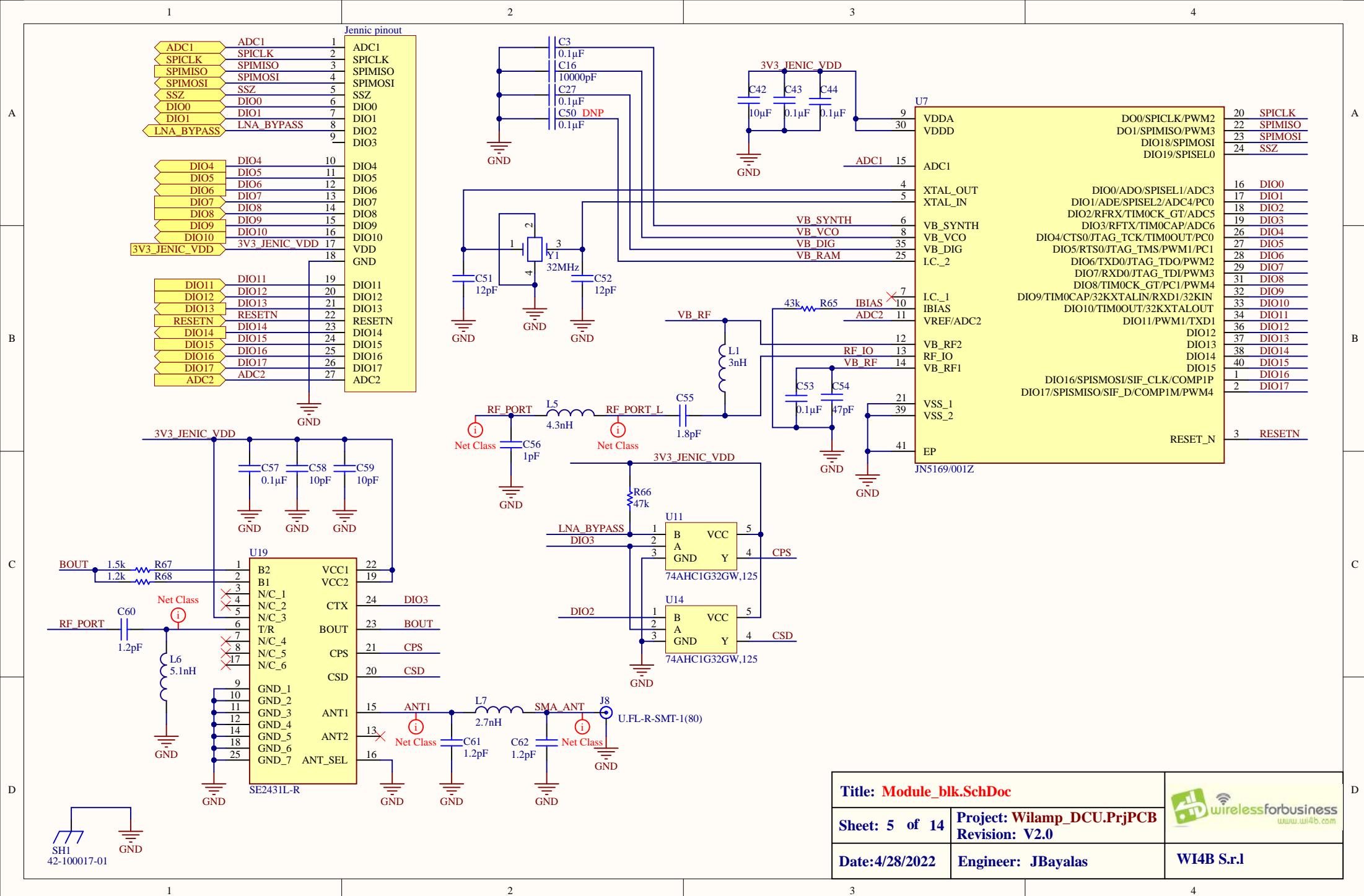
Date:4/28/2022

Engineer: JBayalas

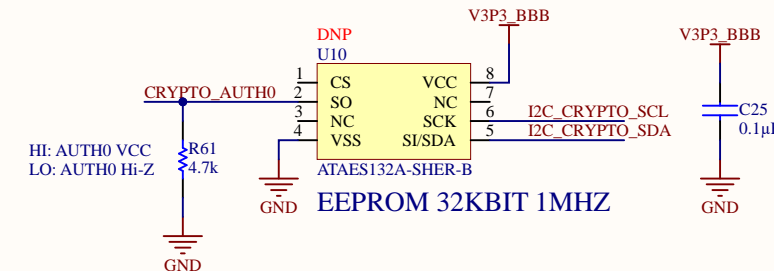
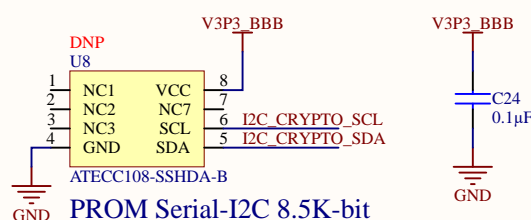
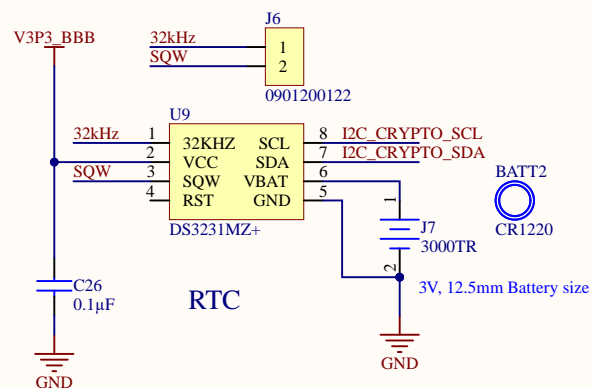
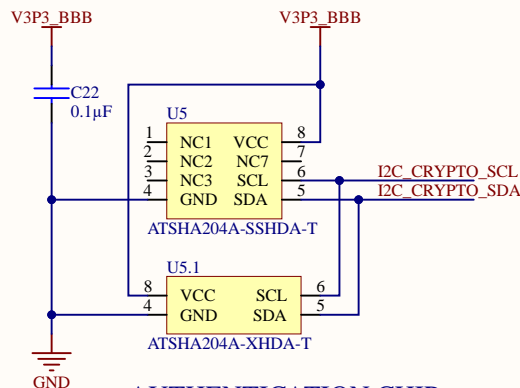
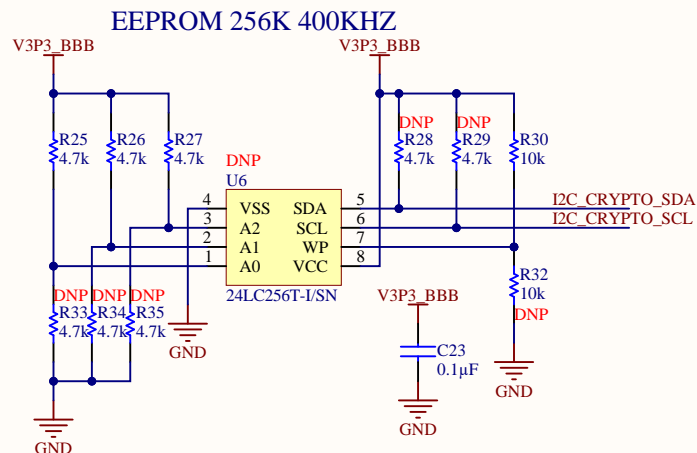
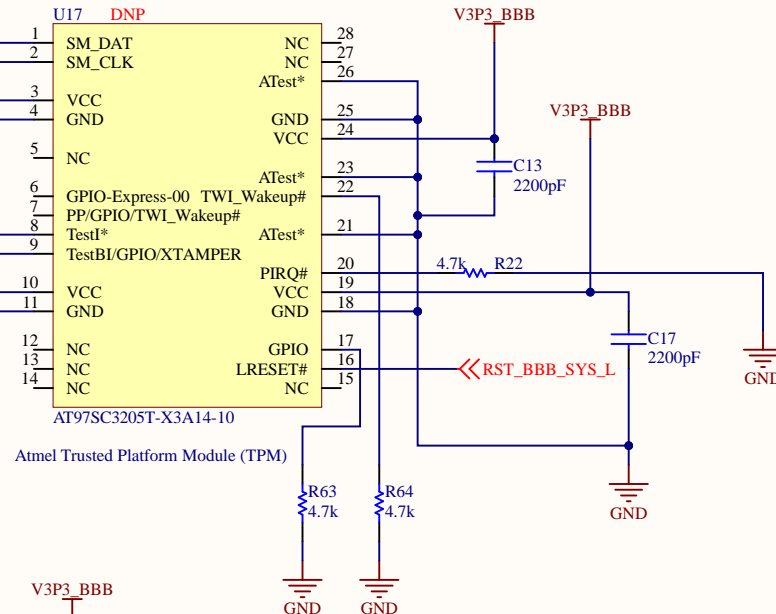
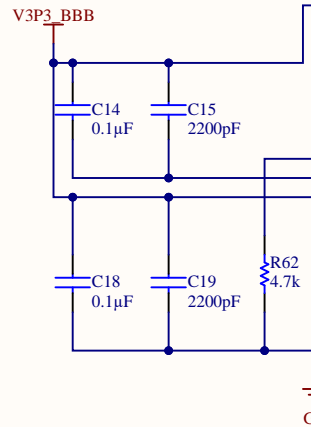
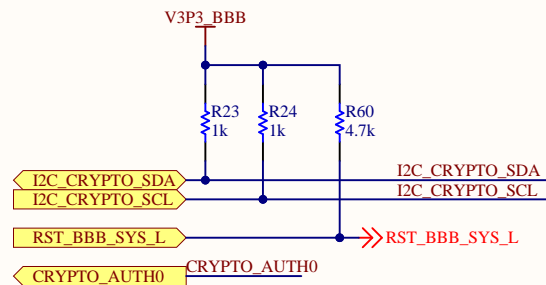



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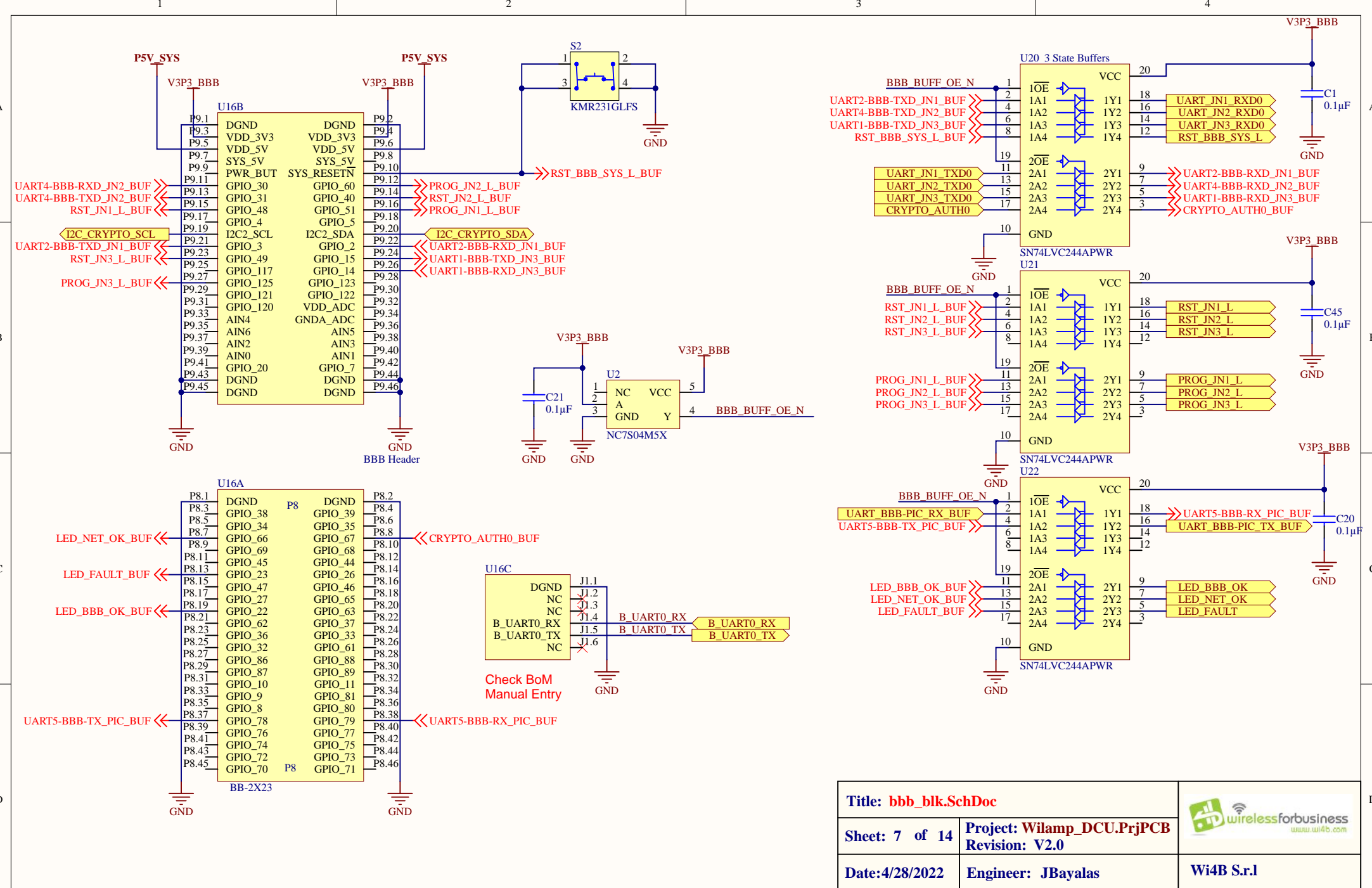


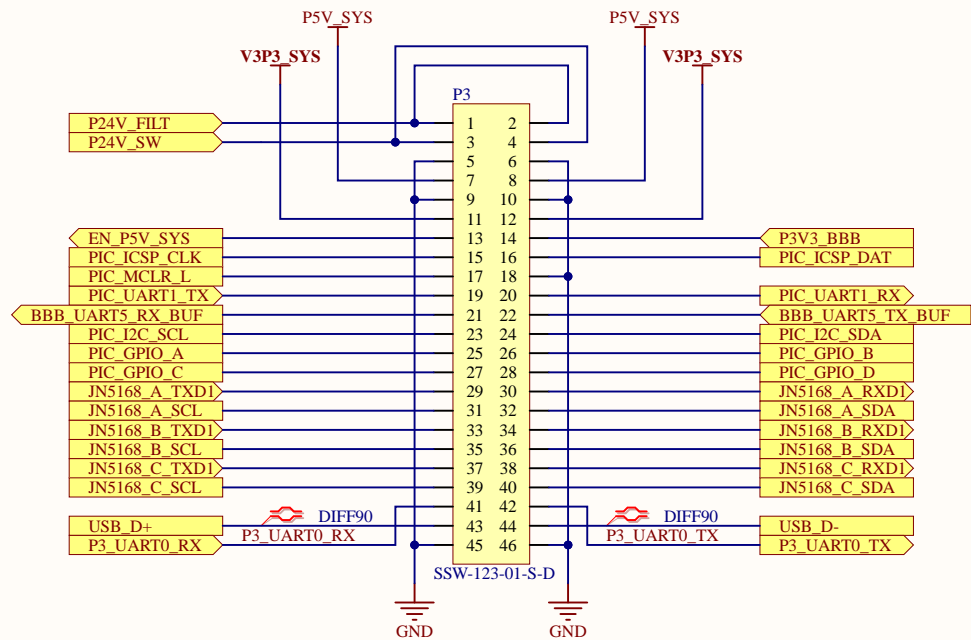


I2C Devices



Title: crypto_blk.SchDoc		
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Title: headerP3_blk.SchDoc

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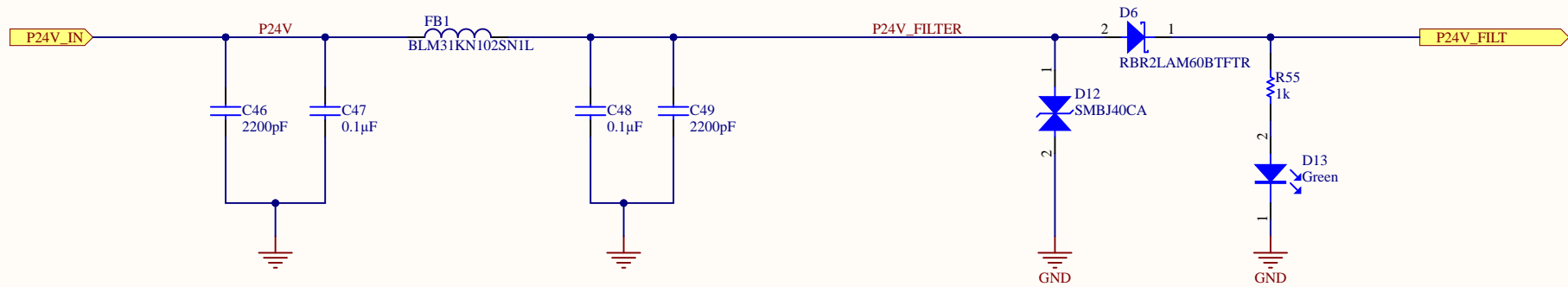
Project: Wilamp_DCU.PrjPCB
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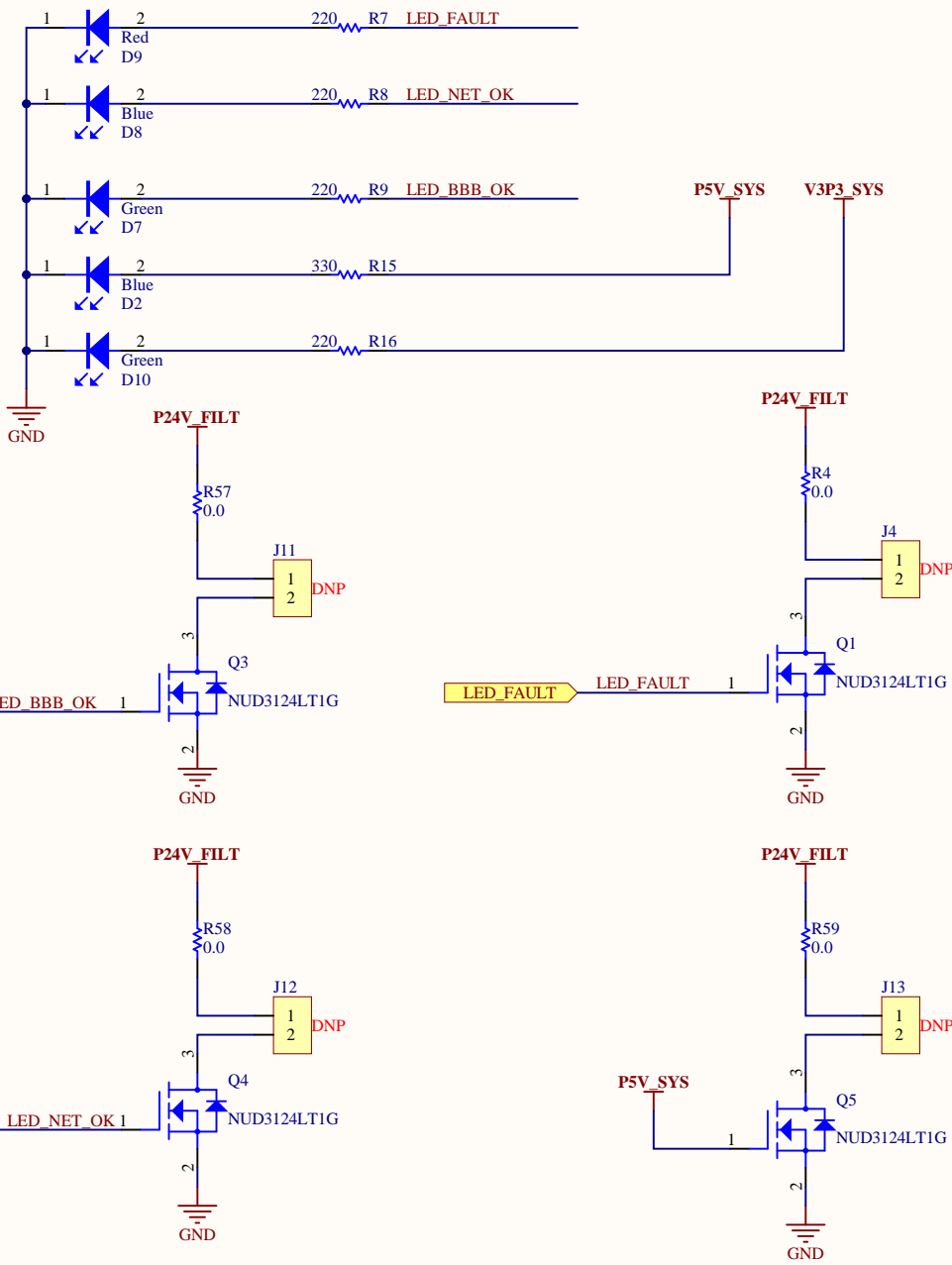
Engineer: JBayalas




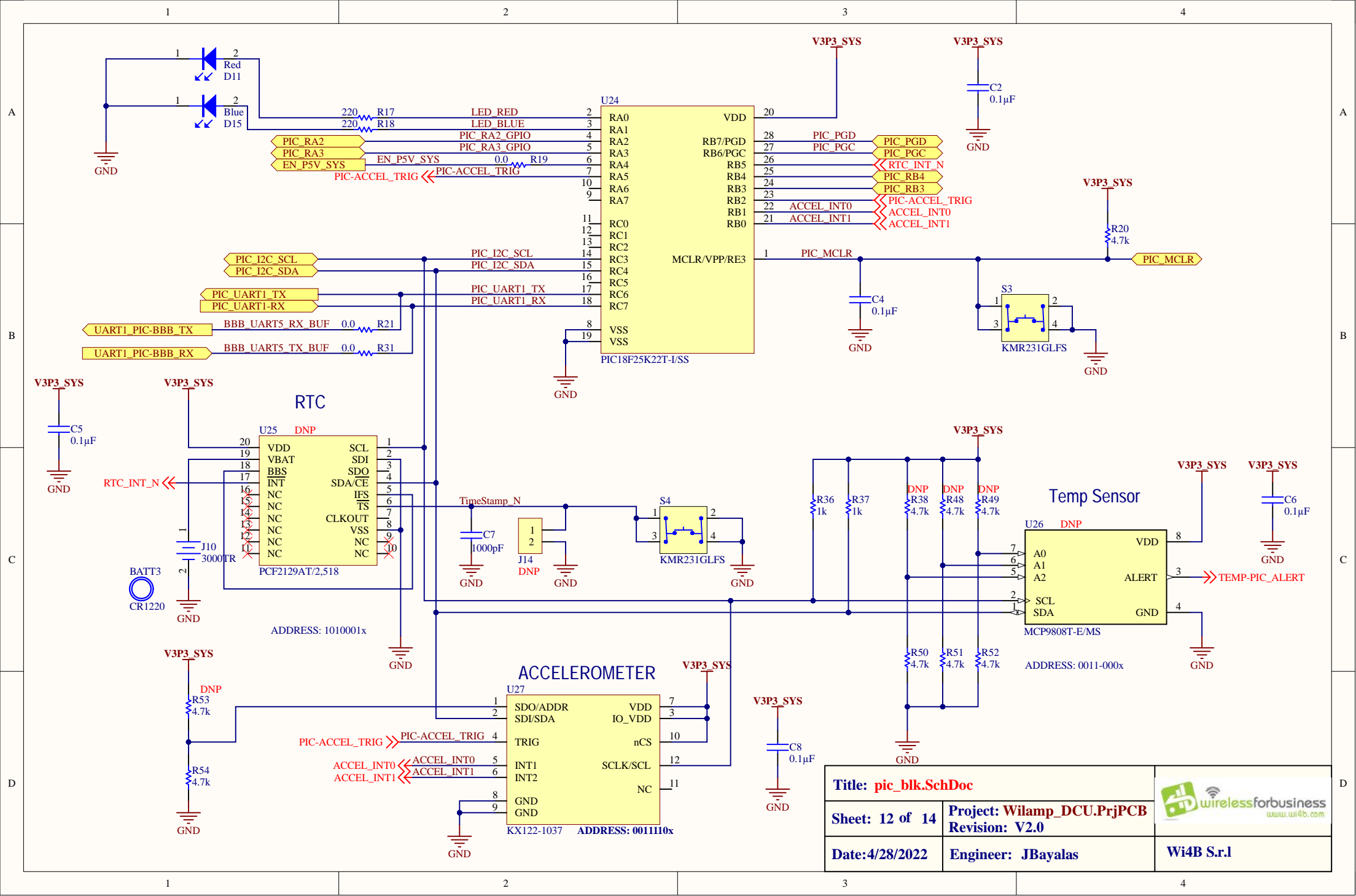
Wi4B S.r.l

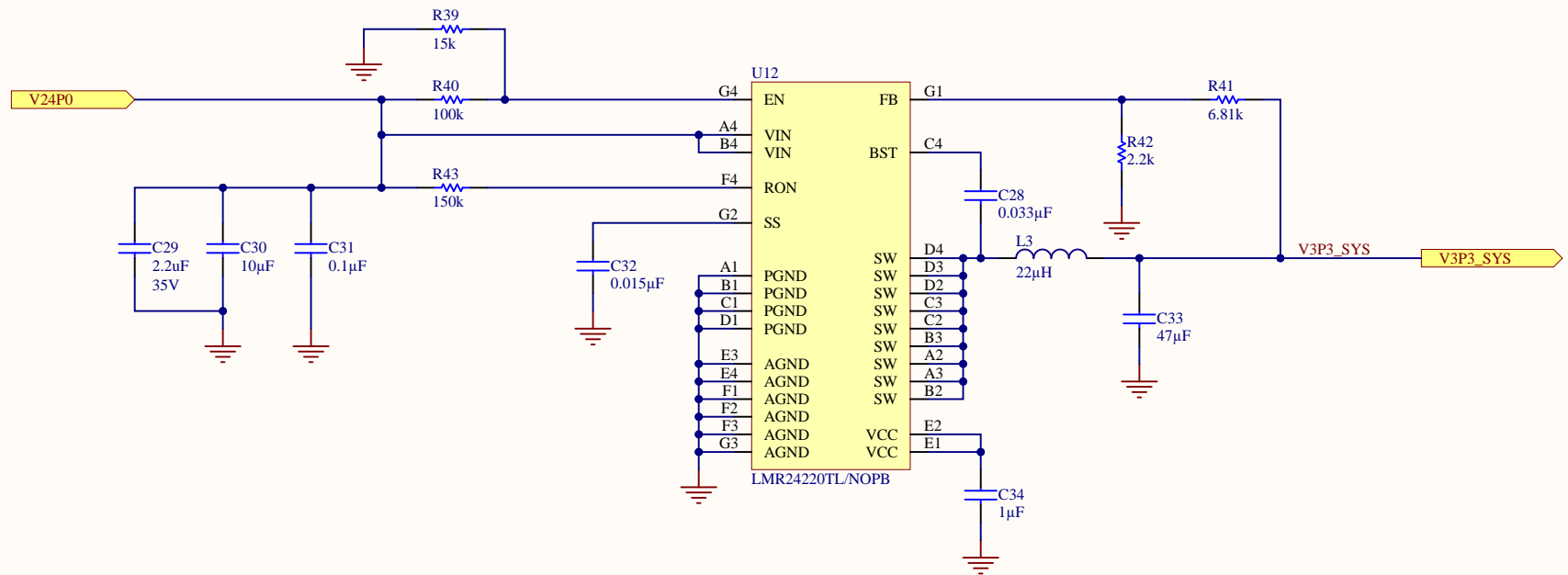


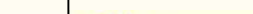
Title: PowerInFilter.SchDoc		 www.wi4b.com
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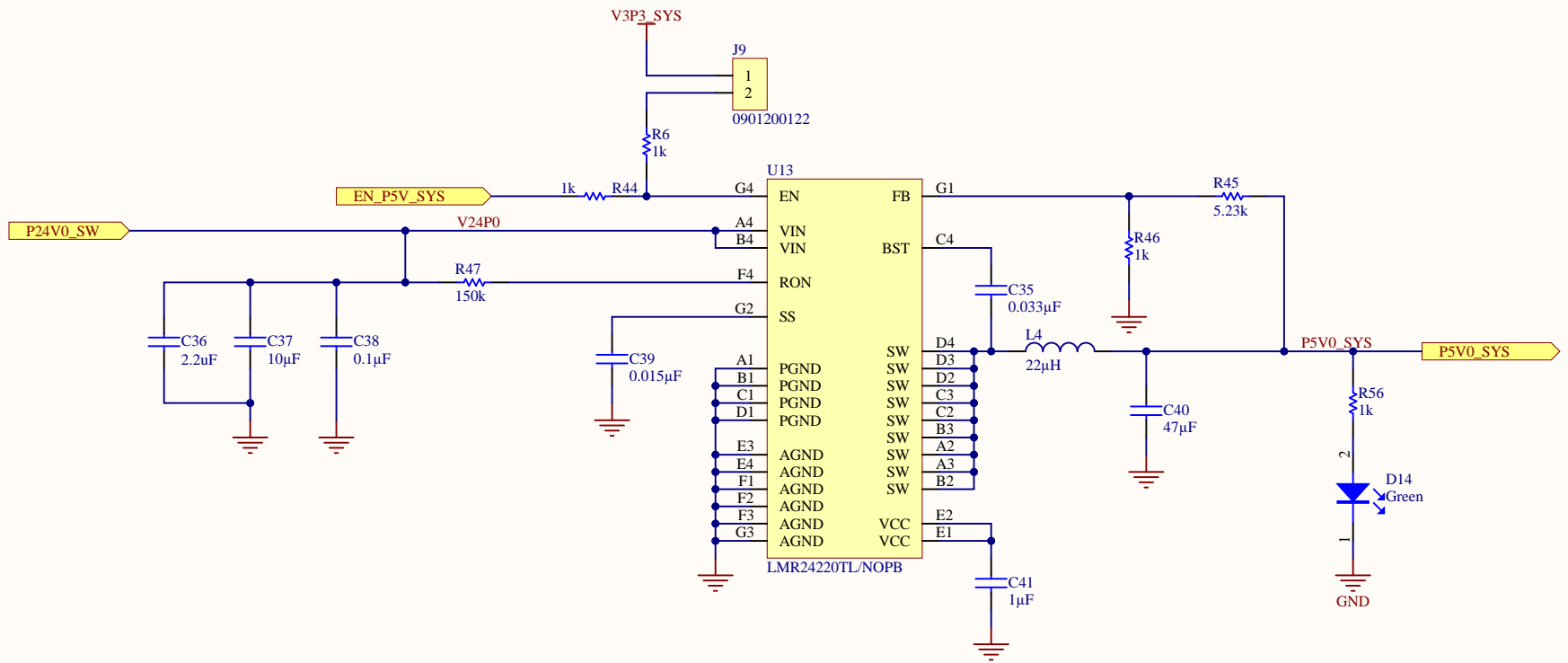



Title: LED.SchDoc		 www.wi4b.com
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Title: 5v_dcde_blk.SchDoc		 www.wi4b.com
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