

Module files:

<std>: stack_interface.v <stu> : stack_interface.v <scntl> : streamingOp_cntl.v

<cntl> : pe_cntl.v
<simdw> : simd_wrapper.v

<sui> : simd_upstream_intf.v <st0p> : streaming0p.v

<dma> : dma_cont.v
<memc> : mem_acc_cont.v

Notes

 $<\!\!st0p\!\!>, <\!\!dma\!\!> : Per lane instances instantiated in streaming0p_datapath.v$