

StreamingOp datapath

Control
from PE Cntl

Control/Status

Status
to PE Cntl

Streaming Op

Input Mux and FIFO

mult/mac

FP
MAC

to SIMD

to DMA

...

32

...

Streaming Op

Input Mux and FIFO

mult/mac

FP
MAC

to SIMD

to DMA

Lane 0

SIMD
Local DMA
Downstream Strm0
Downstream Strm1

Lane 31

SIMD
Local DMA
Downstream Strm0
Downstream Strm1