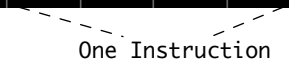


Number of instructions	Instructions per second
1	100
2	50
3	60
4	70
5	68
6	66
7	65



0 : NOP

1 : OP

3 : MW

--	--



0 : NOP

: Target

Number of lanes

6 : STMD Op -

### Memory Access Test

### 1. Memory Access Time