

Pin Numbering - Raspberry Pi Zero W

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Numbering Scheme

Pi4J uses an abstract pin numbering scheme to help insulate software from hardware changes.

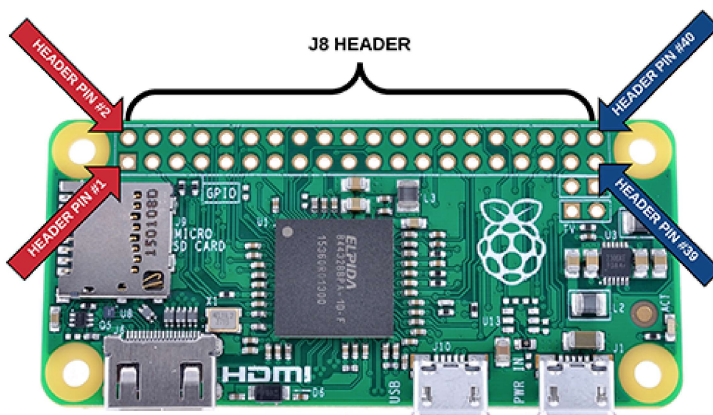
Pi4J implements the same pin number scheme as the Wiring Pi project. More information about the WiringPi pin number scheme can be found here: <http://wiringpi.com/pins/>

Pi4J provides a [RaspiPin](#) enumeration that is used to manage the accessible GPIO pins.

Expansion Header

The Raspberry Pi Zero W board contains a single 40-pin expansion header labeled as 'J8' providing access to 28 GPIO pins.

(Pins 1, 2, 39 & 40 are also labeled below.)



([click here for hi-resolution image](#))

J8 Pinout (40-pin Header)

The diagram below illustrates the GPIO pinout using the Pi4J/WiringPi GPIO numbering scheme.

Raspberry Pi Zero (J8 Header)				
GPIO#	NAME		NAME	GPIO#
	3.3 VDC Power	1		2
8	GPIO 8 SDA1 (I2C)	3		4
9	GPIO 9 SCL1 (I2C)	5		6
7	GPIO 7 GPCLK0	7		8
	Ground	9		10
0	GPIO 0	11		12
2	GPIO 2	13		14
3	GPIO 3	15		16
	3.3 VDC Power	17		18
12	GPIO 12 MOSI (SPI)	19		20
13	GPIO 13 MISO (SPI)	21		22
14	GPIO 14 SCLK (SPI)	23		24
	Ground	25		26
30	SDA0 (I2C ID EEPROM)	27		28
21	GPIO 21 GPCLK1	29		30
22	GPIO 22 GPCLK2	31		32
23	GPIO 23 PWM1	33		34
24	GPIO 24 PCM_FS/PWM1	35		36
25	GPIO 25	37		38
	Ground	39		40
Attention! The GPIO pin numbering used in this diagram is intended for use with WiringPi / Pi4J. This pin numbering is not the raw Broadcom GPIO pin numbers.				
http://www.pi4j.com				

([click here for hi-resolution image](#))

Additional Resources

- Please visit the [usage](#) page for additional details on how to control these pins using Pi4J.
- [Click here for more information on the J8 header.](#)
- [Click here for more information the Raspberry Pi pin functions.](#)