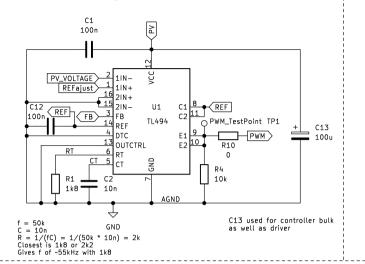
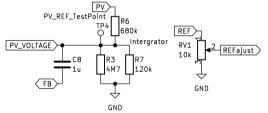
TL494 PWM @ 50kHz



Intergrator Controller



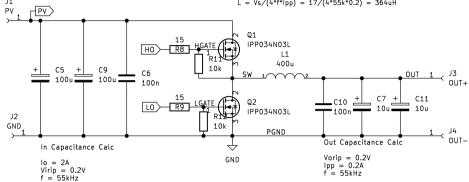
Vdiv = 2.5V Vmp = 17V t = 100m C = 1uR = t/C = 100m/1u = 100kThey equivlent is Ru/Rl = 1/(1/Ru + 1/Rl) voltage divider Rl/(Ru+Rl) * 17 = 2.5Solving for Rl & Ru Ru = 680k Rl = 117.241k

~117k Can be made with 120k//4M7

POWER STAGE

Inductor Calc

Vs = 17V peak power from PV characterisation Io = 1.5A - 2A using largest value f = 55kHz due to slelected RT lpp = 0.2A L = Vs/(4*f*lpp) = 17/(4*55k*0.2) = 364uH



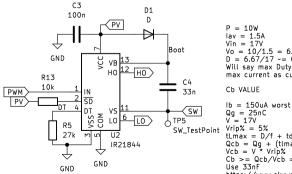
Cin = Io/(4*f*Virip) = 2/(4*55k*0.2) = 45u5Closest value that is > than 45u5 = 47ufMore Cap on input is better

Gate resistors gate driver can source about 1.9A typical more current means faster switch on. However too fast switch on using gate resistor can stop this.

The internal R of the fet is 1.6 to get ~ 1.9A @ 17V 17/1.9 = ~8.95 Chossing 10ohm will be about 1.7Apk

Co = lpp/(8*f*Vorip) = 0.2/(8*55k*0.2) = 2u3Closest value that is > than 2u3 = 3u3Cap cant be too big if we want fast responce

SYNC Driver



P = 10Wlav = 1.5A Vin = 17V Vo = 10/1.5 = 6.67V $D = 6.67/17 \sim 0.4$ Will say max Duty is 0.6 as max current as current will vary Cb VALUE

On time 20u * D = 12u Off time 20u * (1 - D) = 8uDont have to wory about this Rise Time C = 4nF Rgate = 10 Cg = 5n3 worst Cg = 513 worst
Rginternal = 1.6
t = 513 * 11.6 = 61.48n
Get dead time of 70ns for safty
Using graph from data sheet 200k = 600ns
60n/(600n/200k) = 23.3 closests that give sufficent dead time is 27k

Qg = 25nC V = 17V tLmax = D/f + tdt 0.6/50k + 70n = 12usQcb = Qg + (tlmax * IB) = 25n + (12u * 150u) = 26.8nC Vcb = V * Vrip% Cb >= Qcb/Vcb = 26.8/(17 * 0.05) = 31.5 nF

R13 used to make sure SD does not interfere with pwm

https://www.skyworksinc.com/-/media/Skyworks/SL/documents/public/application-notes/AN486.pdf