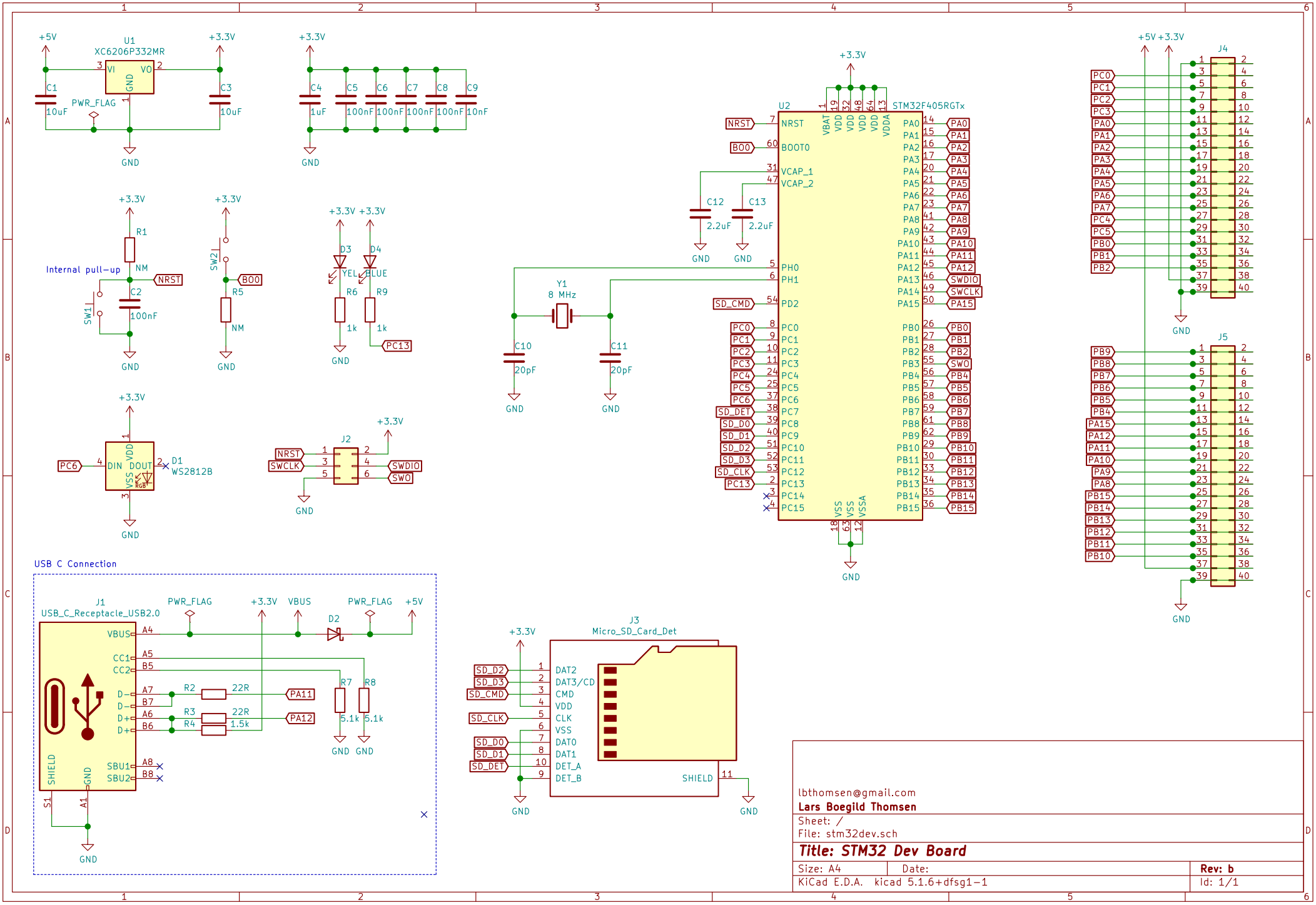
U1
XC6206P332MR
+5V
+3.3V
C1
10uF
C3
10uF
PWR_FLAG
GND
C4
1uF
C5
100nF
C6
100nF
C7
100nF
C8
100nF
C9
10nF
R1
NM
Internal pull-up
NRST
C2
100nF
SW1
SW2
BOO
R5
NM
D3
YELLOW
D4
BLUE
R6
1k
R9
1k
PC13
D1
WS2812B
PC6
DIN
DOUT
VDD
VSS
J2
SWDIO
SWO
U2
STM32F405RGTx
BOOT0
VCAP_1
VCAP_2
C12
2.2uF
C13
2.2uF
Y1
8 MHz
C10
20pF
C11
20pF
SD_CMD
SD_DET
SD_D0
SD_D1
SD_D2
SD_D3
SD_CLK
J4
J5
USB C Connection
J1
USB_C_Receptacle_USB2.0
VBUS
CC1
CC2
D-
D+
SHIELD
SBU1
SBU2
R2
22R
R3
22R
R4
1.5k
PA11
PA12
R7
5.1k
R8
5.1k
D2
J3
Micro_SD_Card_Det
DAT2
DAT3/CD
CMD
VDD
CLK
VSS
DAT0
DAT1
DET_A
DET_B
lbthomsen@gmail.com
Lars Boegild Thomsen
Sheet: /
File: stm32dev.sch
Title: STM32 Dev Board
Size: A4
Date:
Rev: b
KiCad E.D.A. kicad 5.1.6+dfsg1-1
Id: 1/1