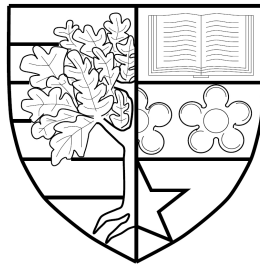


**FROM FORMAL SPECIFICATION TO FULL PROOF:  
A STEPWISE METHOD**

*by*

Lavinia Burski



Submitted for the degree of  
Doctor of Philosophy

DEPARTMENT OF COMPUTER SCIENCE  
SCHOOL OF MATHEMATICAL AND COMPUTER SCIENCES  
HERIOT-WATT UNIVERSITY

March 2016

The copyright in this thesis is owned by the author. Any quotation from the report or use of any of the information contained in it must acknowledge this report as the source of the quotation or information.

# Acronyms

**ASM** Abstract state machine.

**CGa** Core Grammatical aspect.

**DRa** Document Rhetorical aspect.

**GPSa** General Proof Skeleton aspect.

**Gpsa** General Proof Skeleton aspect.

**GpsaOL** General Proof Skeleton ordered list.

**Hol-Z** Hol-Z.

**IEC** International Electrotechnical Commission.

**MathLang** MathLang framework for mathematics.

**PPZed** Proof Power Z.

**SIL** Safety Integrity Levels.

**SMT** Satisfiability Modulo Theories.

**TSa** Text and Symbol aspect.

**UML** Unified Modeling Language.

**UTP** Unifying theories of programming.

**ZCGa** Z Core Grammatical aspect.

**ZDRa** Z Document Rhetorical aspect.

**ZMathLang** MathLang framework for Z specifications.

# Glossary

**computerisation** The process of putting a document in a computer format.

**formal methods** Mathematically rigorous techniques and tools for the specification, design and verification of software and hardware systems.

**formalisation** The process of extracting the essence of the knowledge contained in a document and providing it in a complete, correct and unambiguous format.

**halfbaked proof** The automatically filled in skeleton also known as the Half-Baked Proof.

**partial correctness** A total correctness specification  $[P] C [Q]$  is true if and only if, whenever  $C$  is executed in a state satisfying  $P$  and if the execution of  $C$  terminates, then the state in which  $C$ 's execution terminates satisfies  $Q$ .

**semi-formal specification** A specification which is partially formal, meaning it has a mix of natural language and formal parts.

**total correctness** A total correctness specification  $[P] C [Q]$  is true if and only if, whenever  $C$  is executed in a state satisfying  $P$ , then the execution of  $C$  terminates, after  $C$  terminates  $Q$  holds.

# Chapter 1

## Evaluation and Discussion

In this chapter we go through a few case studies and discuss the difference between the specification translations if any. Table 1.1 shows the specifications we have translated into Isabelle using MathLang framework for Z specifications (ZMathLang). We have classified these examples to show the different types of specifications which can be translated using the ZMathLang toolkit. In this chapter we take one example from each class and describe in more detail how the translation was done.

Examples using only terms	Examples using sets and terms
Vending Machine	Birthday Book
SteamBoiler	ClubState
<b>Incomplete translations</b>	Clubstate2
Autopilot	GenDB
A specification which fails ZCGa	ModuleReg
A specification which fails ZDRa	ProjectAlloc
	Timetable
	Videoshop
	TelephoneDirectory
	ZCGa

Table 1.1: A table showing the specifications we have translated into Isabelle using ZMathLang

We have categorised the specification into three groups; specifications which

only use terms, specifications which use both terms and set and specifications which the translation is incomplete for a variety of reasons. All the specifications we have translated are ‘state based specifications’, which means they operate within a state and to change the state their may become precondition and postconditions within the state. Some specifications are described differently such as functional specifications, however those type of specifications are out of the scope of this thesis.

## 1.1 Complexity of specifications

This section we analyse the complexity of the specifications we have translate using ZMathLang. First we check the complexity of the raw  $\text{\LaTeX}$  specification file, without any annoatation. Then we discuss the complexity of the Z Core Grammatical aspect (ZCGa) annotated specifications and Z Document Rhetorical aspect (ZDRa) annotated specifications and how this affects the translation into Isabelle.

### 1.1.1 Raw Latex Count

Table 1 how long each specification is by amount of lines of code and environments uses. We have listed the specifications in decreasing complexity of how many lines of  $\text{\LaTeX}$  the raw specification has.

Specification	Environment				Lines of $\text{\LaTeX}$
	Zed	Schema	Axdef	Total	
Steamboiler	10	34	3	47	507
ProjectAlloc	4	17	0	21	213
VideoShop	3	15	0	18	166
TelephoneDirectory	6	11	0	17	133
ClubState	4	11	1	16	129
ZCGa	2	9	0	11	128
GenDB	2	7	0	9	114
Timetable	1	6	1	8	92
BirthdayBook	3	7	0	10	83
AutoPilot	2	3	0	5	83
ClubState2	1	6	1	8	80
Vending Machine	4	7	0	11	68
ModuleReg	1	3	0	4	43

Table 1.2: How many zed, schema and axdef environments and lines of  $\text{\LaTeX}$  code makes up each specification

We list information about how many different environments and lines of  $\text{\LaTeX}$  make up each specification in table 1.2. The environment numbers count how many different types of environments exist within the specification. That is how many ‘ $\backslash\text{begin}\{\text{schema}\}\dots\backslash\text{end}\{\text{schema}\}$ ’ or ‘ $\backslash\text{begin}\{\text{zed}\}\dots$ ’ etc. We add up the total amount of environments in the specification. From the table we can see that for most of the specifications the more lines of  $\text{\LaTeX}$  there is then the total amount of environments increase. However, there are three exceptions to this trend. The ‘*BirthdayBook*’ specification, ‘*ClubState2*’ specification and ‘*Vendine Machine*’ specification. Specifications for systems can always be written in a variety of ways and still have the same meaning. Even formal specifications can be written different ways. For example one may have the following declarations:

$$t : \mathbb{N}$$

$$l : \mathbb{N}$$

However, this declaration can also be written as the following:

$$t, l :: \mathbb{N}$$

Thus removing a line. Formal specifications can also include comments written in natural language which are not part of the formal script. These extra comments about the specification may have also added to the line count in table 1.2.

### 1.1.2 ZCGa Count

In this section, we evaluate the ZCGa annotations on the specifications. We describe how many of each ZCGa annotations occurs for each specification we have translated.







Specification	ZCGa WeakTypes					
						
Steamboiler	297	26	282	595	4	0
ProjectAlloc	98	43	113	154	165	0
VideoShop	87	31	75	119	95	0
TelephoneDirectory	78	26	53	72	50	0
ClubState	75	17	51	55	51	0
ZCGa	73	27	67	35	133	0
GenDB	45	24	71	117	121	1
Timetable	35	15	53	48	114	0
BirthdayBook	26	11	24	28	19	0
AutoPilot	16	9	19	31	2	0
ClubState2	34	7	37	22	72	0
Vending Machine	16	7	21	37	0	0
ModuleReg	20	6	18	13	31	0

Table 1.3: How many of each grammatical category exists in each specification.



The amount of times a ZCGa weak type occurs in each specification is shown in table 1.3. We remind the reader the colours corresponding to each grammatical type are: `schematext` , `declaration` , `expression` , `term` , `set` and `definition` . In this instance we don't use `specification` as we assume each document contains a single specification.

In our sample set we only have one specification (GenDB) with a 'definition' annotation. This `definition` is locally defined within the specification. The '*Vending Machine*' specification only uses `terms` and therefore there are no ZCGa `term` annotations. However the '*SteamBoiler*' specification also only uses `term` yet there are 4 `set` ZCGa annotations. This is because some of the `terms` used in the specification have to be introduced by a `set`. For example in the *SteamBoiler* specification we have the following annotation:

```
\begin{zed}
\set{State} ::= \term{init} | \term{norm} |
\term{broken} | \term{stop}
\end{zed}
```

Although the set `State` is annotated as a set, it is not used in any of the schema's in the rest of the specification. It is only defined to present the terms `init`, `norm`, `broken` and `stop` which are used in the specification.

We expect there to be more `schemaText`'s than `declarations` and `expressions` combined as `schemaText` contains all `declarations`, `expressions` and `SchemaNames` however, from the table we can see that this is not always the case. For example in the *ProjectAlloc* example, there are 98 `schemaText`, 43 `declarations` and 113 `expressions`. The reason for this could be because a single `expression` can in itself contain many `expressions`. For example the following `schemaText` has been taken from the *ProjectAlloc* specification:

```
\text{\expression{\forall
\declaration{\term{lec}: \expression{\dom maxPlaces}}\
@ \expression{\term{\# (\set{\set{allocation}
\rres \set{\{\term{lec}\}})}} \leq \term{\set{maxPlaces}~\term{lec}}}}
```

In this example we can see that there contains 1 annotated `schemaText` but 3 `expressions`. Another reason why there may be more `expressions` than `schemaText` is because when annotating a specification with ZCGa, `declarations` also contain `expressions`. If we have the following example, again taken from the ProjectAlloc specification:

```
\text{\declaration{\set{studInterests}, \set{lecInterests}:
\expression{PERSON \pfun\iseq TOPIC}}}
```

The ZCGa text contains 1 annotation of `SchemaText`, 1 annotation of a `declaration`, 2 annotations of `sets` and 1 annotation of an `expression`. Since this is the case we expect to see more expressions than declarations in every specification, which is true according to table 1.3.

### 1.1.3 ZDRa Count

In this section we analyse the amount of ZDRa instances and relations are labeled for each of the specifications we translated. We give details of the amount of instances in table 1.4 and give details of the amount of relations in each specification in table 1.5.

Specification	ZDRa Instances									
	A	SS	IS	CS	OS	TS	PRE	PO	O	SI
Steamboiler	6	2	2	21	6	6	21	23	12	1
ProjectAlloc	0	1	1	5	11	0	11	6	22	1
VideoShop	0	1	1	3	10	0	13	4	20	1
TelephoneDirectory	0	1	1	4	5	5	8	5	10	1
ClubState	1	1	1	4	6	4	9	6	11	0
ZCGa	0	1	1	6	1	0	6	7	2	1
GenDB	0	1	1	4	2	0	6	5	4	1
Timetable	1	1	1	4	0	0	4	5	0	1
BirthdayBook	0	1	1	1	4	2	4	2	8	1
AutoPilot	0	2	0	1	1	0	1	1	2	0
ClubState2	1	2	1	3	0	0	3	4	0	2
Vending Machine	0	1	0	3	0	3	3	2	0	0
ModuleReg	0	1	0	2	0	0	2	2	0	1

Table 1.4: How many of each ZDRa instances exists in each specification.

From table 1.4 we can see that all specifications have either 1 or 2 statesSchema's. For state base specification it should be the case that then specification has at least 1 state. Most state based specifications have stateInvariants that must be conformed to through all the changes of the specification. However this is not a must and some specification (even from our sample) do not have any stateInvariants.

All precondition must have a corresponding postcondition or output, therefore we can say:

**Lemma 1.1.1.**  $precondition \longrightarrow postcondition \vee output$

The table supports this informatio as there are more combined postconditions and outputs then there are precondition. However not all postconditions and outputs need to have a precondition, they can be executed without one. Therefore the number of preconditions does not need to equal the total number of postcondition and outputs.

Specification	ZDRa Relations				
	initiaOf	requires	allows	totalises	uses
Steamboiler	2	28	21	24	92
ProjectAlloc	1	16	11	0	16
VideoShop	0	15	13	0	142
TelephoneDirectory	1	11	8	14	8
ClubState	1	12	9	14	12
ZCGa	1	9	6	0	7
GenDB	1	8	6	0	6
Timetable	1	6	4	0	6
BirthdayBook	1	7	4	6	5
AutoPilot	0	2	1	0	2
ClubState2	1	6	3	0	6
Vending Machine	0	2	0	2	8
ModuleReg	0	3	2	0	2

Table 1.5: How many of each ZDRa relations exists in each specification.

We can cross reference the table showing the amount of instances (table 1.4) with the table showing the relations (table 1.5). For example, the relation *initialOf* can only occur if the specification has an *initialSchema*. Not all specifications have an *initialSchema* and therefore do not have an *initialOf* relation.

There is also an equal amount of *allows* relations as there is *preconditions*. As was written previously, all preconditions must have a corresponding output or postcondition, therefore the relation ‘*allows*’ links each precondition to its corresponding postcondition or output. However, the vendingMachine specification is an exception to this as the preconditions are written as entire schema’s. For example we have the following instance in the vending machine specification:

```
\draschema{PRE3}{
\begin{schema}{some\_stock}
stock: \nat
```

```

\where
stock > 0
\end{schema}}

```

This chunk of specification describes an entire schema as a precondition. The totalising schema then joints the precondition to their corresponding output or postcondition. The specification is written in this way as it is a personal choice of writing the specification formally. All other specifications in our sample set are written in the style where the precondition and corresponding output or postcondition are written inside the same schema environment.

Obviously, the relation ‘*totalises*’ only occurs in specifications where `totaliseSchema`’s are present. Therefore the ‘*totalise*’ relation is not necessary in all specifications.

VideoShop specification is one of the largest specifications (in terms of lines of  $\text{\LaTeX}$ ) in our sample set however it has quite a small amount of relations.

## 1.2 Case Studies

This section describes a few specification case studies in which we have used the ZMathLang tool kit to translate and prove formal specifications into the Isabelle automated theorem prover. The first case study present a formal specification only using terms, the second is a formal specification where both sets and terms are used and therefore the syntax used in Isabelle is more complex. The final case study we present is a partial translation of a specification which is not fully formalised but on it’s way to becoming fully formal.

### 1.2.1 Case Study 1: A specification using only terms.

The following case study is based on the *Steamboiler* [1] specification which has been translated and proved in Isabelle using the ZMathLang framework. This case study only uses variables which are terms. The steamboiler specification is the larges from our examples. It is made up of 507 lines of  $\text{\LaTeX}$  code, 10 zed envi-

ronemnts, 34 schmes and 3 axiom definitions. When annotating with ZCGa there were 297 schematext, 26 declarations, 282 expressions, 595 terms and 4 sets. When annotated with ZDRa there were 6 axioms, 2 stateSchema's, 2 initialSchema's, 21 changeSchema's, 6 outputSchema's, 6 totaliseSchema's, 21 preconditions, 23 post-conditions, 12 outputs and 1 set of stateInvariants.

### 1.2.1.1 Natural Lanaguge Specification of the Steamboiler

The steam boiler itself is a water level and steam quantity measuring device, with four pumps and four pump controlers. There is a valve for emptying the boiler.

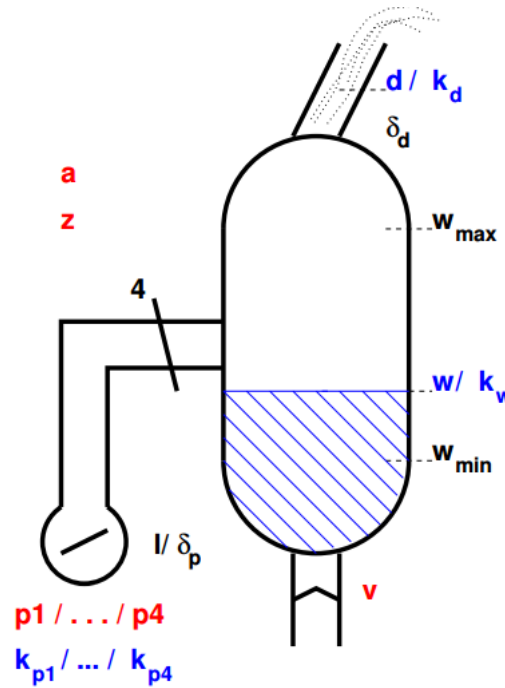


Figure 1.1: A diagram showing a theoretical Steamboiler.

An example of how the steamboiler could look is shown in figure 1.1. The variables of the steamboiler are shown in table 1.6.

find out what  $l$  does

variables	description
$w_{min}$	minimal water level
$w_{max}$	maximal water level
$l$	
$d_{max}$	maximal quantity of steam exiting the boiler
$\delta_p$	error in the value of the pumps
$\delta_d$	error in steam
$w$	water level
$d$	amount of steam exiting the boiler
$k_{p,i}$	pump $i$ works/broken
$k_w$	water level measuring device works/broken
$k_d$	steam amount measuring device works/broken
$p_i$	pump $i$ on/off
$v$	valve open/closed
$a$	boiler on/off
$z$	state init/norm/broken/stop

Table 1.6: The variables of the steamboiler and their descriptions.

The full formal specification for the steamboiler is 10 pages long which can be found in [2]. Therefore we have given small examples taken from the full specification.

## 1.2.1.2 ZMathLang steps for the steamboiler case study.

<pre> \documentclass{article} \usepackage{zmathlang}  \begin{document}  \begin{zed} State ::= init   norm   broken   stop \end{zed}  \begin{zed} OnOff ::= on   off \end{zed}  \begin{zed} OpenClosed ::= open   closed \end{zed}  Physical Constants  \begin{axdef} w_{min}: \nat \\\ w_{max}: \nat \\\ w_{opt}: \nat \\\ l: \nat \\\ d_{max}: \nat \\\ \delta_p: \nat \\\ \delta_d: \nat \\\ \where w_{min} &lt; w_{max} \end{axdef}  Measured values  \begin{schema}{Input} w?: \nat \\\ </pre>	<pre> State ::= init   norm   broken   stop  OnOff ::= on   off  OpenClosed ::= open   closed  hysical Constants   w_{min} : \N   w_{max} : \N   l : \N   d_{max} : \N   \delta_p : \N   \delta_d : \N   w_{min} &lt; w_{max}  Measured values   Input   w? : \N   d? : \N  ontrol values   Pumps   p_1, p_2, p_3, p_4 : OnOff  SteamBoiler0 Pumps v : OpenClosed a : OnOff z : State  uxiliary Schemata   PumpsOff   Pumps'   p'_1 = off \wedge p'_2 = off \wedge p'_3 = off \wedge p'_4 = off  PumpsOn Pumps'   p'_1 = on \wedge p'_2 = on \wedge p'_3 = on \wedge p'_4 = on </pre>
--	---

Figure 1.2: The formal specification  
L<sup>A</sup>T<sub>E</sub>X code for the steamboiler sys-  
tem.

We show the L<sup>A</sup>T<sub>E</sub>X code for part of the raw steamboiler specification in figure 1.2 and it's pdflatex counterpart in figure 1.3.

We then annotate the specification using ZCGa and ZDRa labels.

Figure 1.3: The formal specification  
for the steamboiler system.



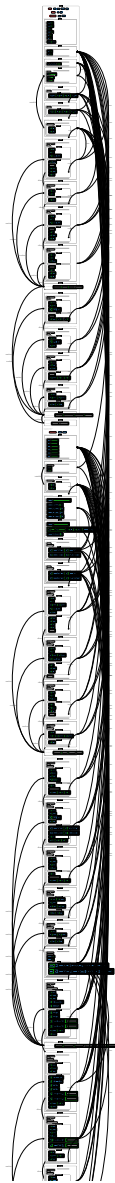


Figure 1.4: An example of the original steamboiler specification annotated in ZCGa and ZDRa.

Since we only have a warning and no errors when checking the steamboiler specification we can now generate a goto graph and dependency graphs for it.

```

Messages
Spec Grammatically Correct
Messages
Warning! Specification not correctly
totalised
Specification is Rhetorically Correct

```

Figure 1.5: The outputting result when checking the steamboiler specification with the ZCGa and ZDRa checkers.

GoTo graph of T1

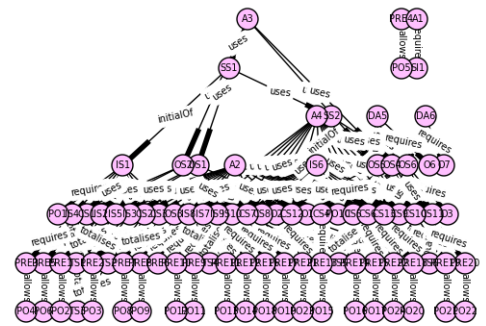


Figure 1.7: The goto graph produced for the steamboiler specification.

From the goto graph the ZMathLang tool kit automatically generates a general proof skeleton, which uses the order from the goto graph to order the instances in how they should appear in any theorem prover. Part of the skeleton for the steamboiler specification is shown in figure 1.8.

Figure 1.8: Gpsa for the steamboiler specification.

We can now translate the Gpsa into Isabelle syntax using the ZMathLang toolkit.

```

theory steamboilerSkelton
imports
Main

begin
  (*DATATYPES*)

  record SS1 =
    (*DECLARATIONS*)

  locale ln2 =
    fixes (*GLOBAL DECLARATIONS*)
    assumes SI1
    begin

    definition IS1 ::
      "(*IS1_TYPES*) => bool"
    where
      "IS1 (*IS1_VARIABLES*) == (P01)"

    definition CS7 ::
      "(*CS7_TYPES*) => bool"
    where

    definition TS3 ::
      "(*TS3_TYPES*) => bool"
    where
      "TS3 (*TS3_VARIABLES*) == (*TS3_EXPRESSION*)"

    end

    record SS2 = SS1 +

    definition IS2 ::
      "(*IS2_TYPES*) => bool"
    where
      "IS2 (*IS2_VARIABLES*) == (P010)"

    definition OS5 ::
      "(*OS5_TYPES*) => bool"
    where
      "OS5 (*OS5_VARIABLES*) == (04)"

    definition OS4 ::
      "(*OS4_TYPES*) => bool"
    where
      "OS4 (*OS4_VARIABLES*) == (03)"

    lemma CS7_L1:
      "( $\exists$  (*CS7_VARIABLESANDTYPES*).
      (PRE8)
       $\wedge$  (P09)
       $\longrightarrow$  ((SI1)
       $\wedge$  (SI1'))))"
    sorry

    lemma CS2_L2:
      "( $\exists$  (*CS2_VARIABLESANDTYPES*).
      (PRE8)
       $\wedge$  (P09)
       $\longrightarrow$  ((SI1)
       $\wedge$  (SI1'))))"
    sorry

    lemma CS5_L3:
      "( $\exists$  (*CS5_VARIABLESANDTYPES*).
      (PRE5)
       $\wedge$  (P06)
       $\longrightarrow$  ((SI1)
       $\wedge$  (SI1'))))"

```

Figure 1.9: Part of the isabelle skeleton for the steamboiler specification.

Part of the isabelle skeleton for the steamboiler specification is shown in figure 1.9. Since the steamboiler example has 2 stateSchema's the ZMathLang toolset creates 2 isabelle **records** in the theory file. The top left image shows the beginning part of the isabelle skeleton, where the first stateSchema (or record) sets the state

of the theory. Midway down the theory file the first record `ends` and a new one is added with the line `record SS2 = SS1 +`. Towards the end the isabelle skeleton there are lemma's to check the consistency for all state changing schema's (CS) in the format decribed in chapter ?? section ??. Using the ZCGa annotated specification and the steamboiler isabelle skeleton, the ZMathLang tool support can now fill in the isabelle skeleton the declarations, expressions, schemaNames etc.

```

theory steamboilerProof
imports
Main

begin
datatype State = init | norm | broken0 | stop
datatype OnOff = on | off
datatype OpenClosed = open0 | closed
datatype WorksBroken = works | broken

record SteamBoiler0 =
PSWITCH :: "State"
W_MAX :: "nat"
D_MAX :: "nat"
PAMOUNT :: "State"
W_MIN :: "nat"
A :: "OnOff"
DELTA_D :: "nat"
DELTA_P :: "nat"
L :: "nat"
V :: "OpenClosed"
Z :: "State"
W :: "nat"

V (ControlNormal0 steamboiler0 a' steamboiler0' z' v' p_1' p_2' p_3' p_4' p_5' p_6' p_7' p_8' p_9' p_10' p_11' p_12' p_13' p_14' p_15' p_16' p_17' p_18' p_19' p_20' p_21' p_22' p_23' p_24' p_25' p_26' p_27' p_28' p_29' p_30' p_31' p_32' p_33' p_34' p_35' p_36' p_37' p_38' p_39' p_40' p_41' p_42' p_43' p_44' p_45' p_46' p_47' p_48' p_49' p_50' p_51' p_52' p_53' p_54' p_55' p_56' p_57' p_58' p_59' p_60' p_61' p_62' p_63' p_64' p_65' p_66' p_67' p_68' p_69' p_70' p_71' p_72' p_73' p_74' p_75' p_76' p_77' p_78' p_79' p_80' p_81' p_82' p_83' p_84' p_85' p_86' p_87' p_88' p_89' p_90' p_91' p_92' p_93' p_94' p_95' p_96' p_97' p_98' p_99' p_100' p_101' p_102' p_103' p_104' p_105' p_106' p_107' p_108' p_109' p_110' p_111' p_112' p_113' p_114' p_115' p_116' p_117' p_118' p_119' p_120' p_121' p_122' p_123' p_124' p_125' p_126' p_127' p_128' p_129' p_130' p_131' p_132' p_133' p_134' p_135' p_136' p_137' p_138' p_139' p_140' p_141' p_142' p_143' p_144' p_145' p_146' p_147' p_148' p_149' p_150' p_151' p_152' p_153' p_154' p_155' p_156' p_157' p_158' p_159' p_160' p_161' p_162' p_163' p_164' p_165' p_166' p_167' p_168' p_169' p_170' p_171' p_172' p_173' p_174' p_175' p_176' p_177' p_178' p_179' p_180' p_181' p_182' p_183' p_184' p_185' p_186' p_187' p_188' p_189' p_190' p_191' p_192' p_193' p_194' p_195' p_196' p_197' p_198' p_199' p_200' p_201' p_202' p_203' p_204' p_205' p_206' p_207' p_208' p_209' p_210' p_211' p_212' p_213' p_214' p_215' p_216' p_217' p_218' p_219' p_220' p_221' p_222' p_223' p_224' p_225' p_226' p_227' p_228' p_229' p_230' p_231' p_232' p_233' p_234' p_235' p_236' p_237' p_238' p_239' p_240' p_241' p_242' p_243' p_244' p_245' p_246' p_247' p_248' p_249' p_250' p_251' p_252' p_253' p_254' p_255' p_256' p_257' p_258' p_259' p_260' p_261' p_262' p_263' p_264' p_265' p_266' p_267' p_268' p_269' p_270' p_271' p_272' p_273' p_274' p_275' p_276' p_277' p_278' p_279' p_280' p_281' p_282' p_283' p_284' p_285' p_286' p_287' p_288' p_289' p_290' p_291' p_292' p_293' p_294' p_295' p_296' p_297' p_298' p_299' p_300' p_301' p_302' p_303' p_304' p_305' p_306' p_307' p_308' p_309' p_310' p_311' p_312' p_313' p_314' p_315' p_316' p_317' p_318' p_319' p_320' p_321' p_322' p_323' p_324' p_325' p_326' p_327' p_328' p_329' p_330' p_331' p_332' p_333' p_334' p_335' p_336' p_337' p_338' p_339' p_340' p_341' p_342' p_343' p_344' p_345' p_346' p_347' p_348' p_349' p_350' p_351' p_352' p_353' p_354' p_355' p_356' p_357' p_358' p_359' p_360' p_361' p_362' p_363' p_364' p_365' p_366' p_367' p_368' p_369' p_370' p_371' p_372' p_373' p_374' p_375' p_376' p_377' p_378' p_379' p_380' p_381' p_382' p_383' p_384' p_385' p_386' p_387' p_388' p_389' p_390' p_391' p_392' p_393' p_394' p_395' p_396' p_397' p_398' p_399' p_400' p_401' p_402' p_403' p_404' p_405' p_406' p_407' p_408' p_409' p_410' p_411' p_412' p_413' p_414' p_415' p_416' p_417' p_418' p_419' p_420' p_421' p_422' p_423' p_424' p_425' p_426' p_427' p_428' p_429' p_430' p_431' p_432' p_433' p_434' p_435' p_436' p_437' p_438' p_439' p_440' p_441' p_442' p_443' p_444' p_445' p_446' p_447' p_448' p_449' p_450' p_451' p_452' p_453' p_454' p_455' p_456' p_457' p_458' p_459' p_460' p_461' p_462' p_463' p_464' p_465' p_466' p_467' p_468' p_469' p_470' p_471' p_472' p_473' p_474' p_475' p_476' p_477' p_478' p_479' p_480' p_481' p_482' p_483' p_484' p_485' p_486' p_487' p_488' p_489' p_490' p_491' p_492' p_493' p_494' p_495' p_496' p_497' p_498' p_499' p_500' p_501' p_502' p_503' p_504' p_505' p_506' p_507' p_508' p_509' p_510' p_511' p_512' p_513' p_514' p_515' p_516' p_517' p_518' p_519' p_520' p_521' p_522' p_523' p_524' p_525' p_526' p_527' p_528' p_529' p_530' p_531' p_532' p_533' p_534' p_535' p_536' p_537' p_538' p_539' p_540' p_541' p_542' p_543' p_544' p_545' p_546' p_547' p_548' p_549' p_550' p_551' p_552' p_553' p_554' p_555' p_556' p_557' p_558' p_559' p_560' p_561' p_562' p_563' p_564' p_565' p_566' p_567' p_568' p_569' p_570' p_571' p_572' p_573' p_574' p_575' p_576' p_577' p_578' p_579' p_580' p_581' p_582' p_583' p_584' p_585' p_586' p_587' p_588' p_589' p_590' p_591' p_592' p_593' p_594' p_595' p_596' p_597' p_598' p_599' p_600' p_601' p_602' p_603' p_604' p_605' p_606' p_607' p_608' p_609' p_610' p_611' p_612' p_613' p_614' p_615' p_616' p_617' p_618' p_619' p_620' p_621' p_622' p_623' p_624' p_625' p_626' p_627' p_628' p_629' p_630' p_631' p_632' p_633' p_634' p_635' p_636' p_637' p_638' p_639' p_640' p_641' p_642' p_643' p_644' p_645' p_646' p_647' p_648' p_649' p_650' p_651' p_652' p_653' p_654' p_655' p_656' p_657' p_658' p_659' p_660' p_661' p_662' p_663' p_664' p_665' p_666' p_667' p_668' p_669' p_670' p_671' p_672' p_673' p_674' p_675' p_676' p_677' p_678' p_679' p_680' p_681' p_682' p_683' p_684' p_685' p_686' p_687' p_688' p_689' p_690' p_691' p_692' p_693' p_694' p_695' p_696' p_697' p_698' p_699' p_700' p_701' p_702' p_703' p_704' p_705' p_706' p_707' p_708' p_709' p_710' p_711' p_712' p_713' p_714' p_715' p_716' p_717' p_718' p_719' p_720' p_721' p_722' p_723' p_724' p_725' p_726' p_727' p_728' p_729' p_730' p_731' p_732' p_733' p_734' p_735' p_736' p_737' p_738' p_739' p_740' p_741' p_742' p_743' p_744' p_745' p_746' p_747' p_748' p_749' p_750' p_751' p_752' p_753' p_754' p_755' p_756' p_757' p_758' p_759' p_760' p_761' p_762' p_763' p_764' p_765' p_766' p_767' p_768' p_769' p_770' p_771' p_772' p_773' p_774' p_775' p_776' p_777' p_778' p_779' p_780' p_781' p_782' p_783' p_784' p_785' p_786' p_787' p_788' p_789' p_790' p_791' p_792' p_793' p_794' p_795' p_796' p_797' p_798' p_799' p_800' p_801' p_802' p_803' p_804' p_805' p_806' p_807' p_808' p_809' p_810' p_811' p_812' p_813' p_814' p_815' p_816' p_817' p_818' p_819' p_820' p_821' p_822' p_823' p_824' p_825' p_826' p_827' p_828' p_829' p_830' p_831' p_832' p_833' p_834' p_835' p_836' p_837' p_838' p_839' p_840' p_841' p_842' p_843' p_844' p_845' p_846' p_847' p_848' p_849' p_850' p_851' p_852' p_853' p_854' p_855' p_856' p_857' p_858' p_859' p_860' p_861' p_862' p_863' p_864' p_865' p_866' p_867' p_868' p_869' p_870' p_871' p_872' p_873' p_874' p_875' p_876' p_877' p_878' p_879' p_880' p_881' p_882' p_883' p_884' p_885' p_886' p_887' p_888' p_889' p_890' p_891' p_892' p_893' p_894' p_895' p_896' p_897' p_898' p_899' p_900' p_901' p_902' p_903' p_904' p_905' p_906' p_907' p_908' p_909' p_910' p_911' p_912' p_913' p_914' p_915' p_916' p_917' p_918' p_919' p_920' p_921' p_922' p_923' p_924' p_925' p_926' p_927' p_928' p_929' p_930' p_931' p_932' p_933' p_934' p_935' p_936' p_937' p_938' p_939' p_940' p_941' p_942' p_943' p_944' p_945' p_946' p_947' p_948' p_949' p_950' p_951' p_952' p_953' p_954' p_955' p_956' p_957' p_958' p_959' p_960' p_961' p_962' p_963' p_964' p_965' p_966' p_967' p_968' p_969' p_970' p_971' p_972' p_973' p_974' p_975' p_976' p_977' p_978' p_979' p_980' p_981' p_982' p_983' p_984' p_985' p_986' p_987' p_988' p_989' p_990' p_991' p_992' p_993' p_994' p_995' p_996' p_997' p_998' p_999' p_1000' p_1001' p_1002' p_1003' p_1004' p_1005' p_1006' p_1007' p_1008' p_1009' p_1010' p_1011' p_1012' p_1013' p_1014' p_1015' p_1016' p_1017' p_1018' p_1019' p_1020' p_1021' p_1022' p_1023' p_1024' p_1025' p_1026' p_1027' p_1028' p_1029' p_1030' p_1031' p_1032' p_1033' p_1034' p_1035' p_1036' p_1037' p_1038' p_1039' p_1040' p_1041' p_1042' p_1043' p_1044' p_1045' p_1046' p_1047' p_1048' p_1049' p_1050' p_1051' p_1052' p_1053' p_1054' p_1055' p_1056' p_1057' p_1058' p_1059' p_1060' p_1061' p_1062' p_1063' p_1064' p_1065' p_1066' p_1067' p_1068' p_1069' p_1070' p_1071' p_1072' p_1073' p_1074' p_1075' p_1076' p_1077' p_1078' p_1079' p_1080' p_1081' p_1082' p_1083' p_1084' p_1085' p_1086' p_1087' p_1088' p_1089' p_1090' p_1091' p_1092' p_1093' p_1094' p_1095' p_1096' p_1097' p_1098' p_1099' p_1100' p_1101' p_1102' p_1103' p_1104' p_1105' p_1106' p_1107' p_1108' p_1109' p_1110' p_1111' p_1112' p_1113' p_1114' p_1115' p_1116' p_1117' p_1118' p_1119' p_1120' p_1121' p_1122' p_1123' p_1124' p_1125' p_1126' p_1127' p_1128' p_1129' p_1130' p_1131' p_1132' p_1133' p_1134' p_1135' p_1136' p_1137' p_1138' p_1139' p_1140' p_1141' p_1142' p_1143' p_1144' p_1145' p_1146' p_1147' p_1148' p_1149' p_1150' p_1151' p_1152' p_1153' p_1154' p_1155' p_1156' p_1157' p_1158' p_1159' p_1160' p_1161' p_1162' p_1163' p_1164' p_1165' p_1166' p_1167' p_1168' p_1169' p_1170' p_1171' p_1172' p_1173' p_1174' p_1175' p_1176' p_1177' p_1178' p_1179' p_1180' p_1181' p_1182' p_1183' p_1184' p_1185' p_1186' p_1187' p_1188' p_1189' p_1190' p_1191' p_1192' p_1193' p_1194' p_1195' p_1196' p_1197' p_1198' p_1199' p_1200' p_1201' p_1202' p_1203' p_1204' p_1205' p_1206' p_1207' p_1208' p_1209' p_1210' p_1211' p_1212' p_1213' p_1214' p_1215' p_1216' p_1217' p_1218' p_1219' p_1220' p_1221' p_1222' p_1223' p_1224' p_1225' p_1226' p_1227' p_1228' p_1229' p_1230' p_1231' p_1232' p_1233' p_1234' p_1235' p_1236' p_1237' p_1238' p_1239' p_1240' p_1241' p_1242' p_1243' p_1244' p_1245' p_1246' p_1247' p_1248' p_1249' p_1250' p_1251' p_1252' p_1253' p_1254' p_1255' p_1256' p_1257' p_1258' p_1259' p_1260' p_1261' p_1262' p_1263' p_1264' p_1265' p_1266' p_1267' p_1268' p_1269' p_1270' p_1271' p_1272' p_1273' p_1274' p_1275' p_1276' p_1277' p_1278' p_1279' p_1280' p_1281' p_1282' p_1283' p_1284' p_1285' p_1286' p_1287' p_1288' p_1289' p_1290' p_1291' p_1292' p_1293' p_1294' p_1295' p_1296' p_1297' p_1298' p_1299' p_1300' p_1301' p_1302' p_1303' p_1304' p_1305' p_1306' p_1307' p_1308' p_1309' p_1310' p_1311' p_1312' p_1313' p_1314' p_1315' p_1316' p_1317' p_1318' p_1319' p_1320' p_1321' p_1322' p_1323' p_1324' p_1325' p_1326' p_1327' p_1328' p_1329' p_1330' p_1331' p_1332' p_1333' p_1334' p_1335' p_1336' p_1337' p_1338' p_1339' p_1340' p_1341' p_1342' p_1343' p_1344' p_1345' p_1346' p_1347' p_1348' p_1349' p_1350' p_1351' p_1352' p_1353' p_1354' p_1355' p_1356' p_1357' p_1358' p_1359' p_1360' p_1361' p_1362' p_1363' p_1364' p_1365' p_1366' p_1367' p_1368' p_1369' p_1370' p_1371' p_1372' p_1373' p_1374' p_1375' p_1376' p_1377' p_1378' p_1379' p_1380' p_1381' p_1382' p_1383' p_1384' p_1385' p_1386' p_1387' p_1388' p_1389' p_1390' p_1391' p_1392' p_1393' p_1394' p_1395' p_1396' p_1397' p_1398' p_1399' p_1400' p_1401' p_1402' p_1403' p_1404' p_1405' p_1406' p_1407' p_1408' p_1409' p_1410' p_1411' p_1412' p_1413' p_1414' p_1415' p_1416' p_1417' p_1418' p_1419' p_1420' p_1421' p_1422' p_1423' p_1424' p_1425' p_1426' p_1427' p_1428' p_1429' p_1430' p_1431' p_1432' p_1433' p_1434' p_1435' p_1436' p_1437' p_1438' p_1439' p_1440' p_1441' p_1442' p_1443' p_1444' p_1445' p_1446' p_1447' p_1448' p_1449' p_1450' p_1451' p_1452' p_1453' p_1454' p_1455' p_1456' p_1457' p_1458' p_1459' p_1460' p_1461' p_1462' p_1463' p_1464' p_1465' p_1466' p_1467' p_1468' p_1469' p_1470' p_1471' p_1472' p_1473' p_1474' p_1475' p_1476' p_1477' p_1478' p_1479' p_1480' p_1481' p_1482' p_1483' p_1484' p_1485' p_1486' p_1487' p_1488' p_1489' p_1490' p_1491' p_1492' p_1493' p_1494' p_1495' p_1496' p_1497' p_1498' p_1499' p_1500' p_1501' p_1502' p_1503' p_1504' p_1505' p_1506' p_1507' p_1508' p_1509' p_1510' p_1511' p_1512' p_1513' p_1514' p_1515' p_1516' p_1517' p_1518' p_1519' p_1520' p_1521' p_1522' p_1523' p_1524' p_1525' p_1526' p_1527' p_1528' p_1529' p_1530' p_1531' p_1532' p_1533' p_1534' p_1535' p_1536' p_1537' p_1538' p_1539' p_1540' p_1541' p_1542' p_1543' p_1544' p_1545' p_1546' p_1547' p_1548' p_1549' p_1550' p_1551' p_1552' p_1553' p_1554' p_1555' p_1556' p_1557' p_1558' p_1559' p_1560' p_1561' p_1562' p_1563' p_1564' p_1565' p_1566' p_1567' p_1568' p_1569' p_1570' p_1571' p_1572' p_1573' p_1574' p_1575' p_1576' p_1577' p_1578' p_1579' p_1580' p_1581' p_1582' p_1583' p_1584' p_1585' p_1586' p_1587' p_1588' p_1589' p_1590' p_1591' p_1592' p_1593' p_1594' p_1595' p_1596' p_1597' p_1598' p_1599' p_1600' p_1601' p_1602' p_1603' p_1604' p_1605' p_1606' p_1607' p_1608' p_1609' p_1610' p_1611' p_1612' p_1613' p_1614' p_1615' p_1616' p_1617' p_1618' p_1619' p_1620' p_1621' p_1622' p_1623' p_1624' p_1625' p_1626' p_1627' p_1628' p_1629' p_1630' p_1631' p_1632' p_1633' p_1634' p_1635' p_1636' p_1637' p_1638' p_1639' p_1640' p_1641' p_1642' p_1643' p_1644' p_1645' p_1646' p_1647' p_1648' p_1649' p_1650' p_1651' p_1652' p_1653' p_1654' p_1655' p_1656' p_1657' p_1658' p_1659' p_1660' p_1661' p_1662' p_1663' p_1664' p_1665' p_1666' p_1667' p_1668' p_1669' p_1670' p_1671' p_1672' p_1673' p_1674' p_1675' p_1676' p_1677' p_1678' p_1679' p_1680' p_1681' p_1682' p_1683' p_1684' p_1685' p_1686' p_1687' p_1688' p_1689' p_1690' p_1691' p_1692' p_1693' p_1694' p_1695' p_1696' p_1697' p_1698' p_1699' p_1700' p_1701' p_1702' p_1703' p_1704' p_1705' p_1706' p_1707' p_1708' p_1709' p_1710' p_1711' p_1712' p_1713' p_1714' p_1715' p_1716' p_1717' p_1718' p_1719' p_1720' p_1721' p_1722' p_1723' p_1724' p_1725' p_1726' p_1727' p_1728' p_1729' p_1730' p_1731' p_1732' p_1733' p_1734' p_1735' p_1736' p_1737' p_1738' p_1739' p_1740' p_1741' p_1742' p_1743' p_1744' p_1745' p_1746' p_1747' p_1748' p_1749' p_1750' p_1751' p_1752' p_1753' p_1754' p_1755' p_1756' p_1757' p_1758' p_1759' p_1760' p_1761' p_1762' p_1763' p_1764' p_1765' p_1766' p_1767' p_1768' p_1769' p_1770' p_1771' p_1772' p_1773' p_1774' p_1775' p_1776' p_1777' p_1778' p_1779' p_1780' p_1781' p_1782' p_1783' p_1784' p_1785' p_1786' p_1787' p_1788' p_1789' p_1790' p_1791' p_1792' p_1793' p_1794' p_1795' p_1796' p_1797' p_1798' p_1799' p_1800' p_1801' p_1802' p_1803' p_1804' p_1805' p_1806' p_1807' p_1808' p_1809' p_1810' p_1811' p_1812' p_1813' p_1814' p_1815' p_1816' p_1817' p_1818' p_1819' p_1820' p_1821' p_1822' p_1823' p_1824' p_1825' p_1826' p_1827' p_1828' p_1829' p_1830' p_1831' p_1832' p_1833' p_1834' p_1835' p_1836' p_1837' p_1838' p_1839' p_1840' p_1841' p_1842' p_1843' p_1844' p_1845' p_1846' p_1847' p_1848' p_1849' p_1850' p_1851' p_1852' p_1853' p_1854' p_1855' p_1856' p_1857' p_1858' p_1859' p_1860' p_1861' p_1862' p_1863' p_1864' p_1865' p_1866' p_1867' p_1868' p_1869' p_1870' p_1871' p_1872' p_1873' p
```

has filled in the lemma's to prove which are sanity checks for the specification. It fills in the lemma's with the correct syntax so that the user only needs to delete the word 'sorry' prove the properties in order to get a proof of their specification.

```

lemma (in thesteamboiler) SNormalStop0_L1:
  "( $\exists$  steamboiler0 :: SteamBoiler0.
 $\exists$  a' :: OnOff.
 $\exists$  steamboiler0' :: SteamBoiler0.
 $\exists$  w_max' :: nat.
 $\exists$  w_min' :: nat.
 $\exists$  z' :: State .
 $\exists$  v' :: OpenClosed.
  (z = norm)
 $\wedge$  (w < w_min  $\vee$ 
  w > w_max)
 $\wedge$  (a' = off  $\wedge$ 
  z' = stop)
 $\longrightarrow$  (w_min < w_max
 $\wedge$  (w_min' < w_max')))"
by (smt State.distinct(9))

lemma (in thesteamboiler) SInitStop0_L2:
  "( $\exists$  steamboiler0 :: SteamBoiler0.
 $\exists$  a' :: OnOff.
 $\exists$  steamboiler0' :: SteamBoiler0.
 $\exists$  w_max' :: nat.
 $\exists$  w_min' :: nat.

```

Figure 1.11: Manually proven lemma for the steamboiler specification.

Using the lemma's which have been generated in figure 1.10 we have proved all of these lemmas for the steamboiler specification, part of which is shown in figure 1.11. By doing so, we have now proven that none of the state changing schemas conflict with the state invariants of the specification. To do this we have manually deleted the 'sorry' command, used the Isar tool 'sledgehammer' which has indicated that to prove this particular lemma (shown in figure 1.11) it can be proven by `smt State.distinct(9)`. Therefore it is true that the 'SNormalStop0' schema does not conflict with the state Invariants. We did this step manually for all remaining lemmas, the full proof of the steamboiler specification can be found in [2].

### 1.2.2 Case Study 2: A specification using both terms and sets.

This case study based is on the *ModuleReg* specification which uses both terms and sets. The specification has been translated into Isabelle using the ZMathLang framework. The entire ZMathLang works for the ModuleReg example is shown in chapter ??.

The *ModuleReg* specification is our smallest example with 43 lines of L<sup>A</sup>T<sub>E</sub>X code, 1 *zed* environment and 3 *schema*'s. There are 20 labels of *schemaText*, 6 *declarations*, 18 *expressions*, 13 *terms*, and 31 *sets*. Since there are *stateInvariants* for the *modulereg* specification, ZMathLang was able to generate lemma's to prove for the 2 *changeSchemas*. There is also 1 *stateSchema*, 2 *preconditions* and 2 *postconditions*. There are 3 *requires* relations, 2 *allows* and 2 *uses*.

Since the *modulereg* specification is quite small but did have stateInvariants which ZMathLang could prove are satisfied throughout the specification, we decided it would be a could example to show the full workings of. This is shown in chapter ??.

### 1.2.3 Case Study 3: A semi formal specification.

In this case study we present the *AutoPilot* specification. The specification is a semi formal specification and has been partially translated into Isabelle. The parts which have been translated are written formally and have been annotated accordingly. This gives an example of a specification which is written in natural language and is on it's way to being formalised.

We have taken the natural language specification for an autopilot system from [3] and started to formalise it.

The mode-control panel contains four buttons for selecting modes and three displays for dialing in or displaying values. The system supports the following four modes:

- attitude control wheel steering (att\_cws)
- flight path angle selected (fpa\_sel)
- altitude engage (alt\_eng)
- calibrated air speed (cas\_eng)

Only one of the first three modes can be engaged at any time. However, the cas\_eng mode can be engaged at the same time as any of the other modes. The pilot engages a mode by pressing the corresponding button on the panel. One of the three modes, att\_cws, fpa\_sel, or alt\_eng, should be engaged at all times. Engaging any of the first three modes will automatically cause the other two to be disengaged since only one of these three modes can be engaged at a time.

There are three displays on the panel: and altitude [ALT], flight path angle [FPA], and calibrated air speed [CAS]. The displays usually show the current values for the altitude, flight path angle, and air speed of the aircraft. However, the pilot can enter a new value into a display by dialing in the value using the knob next to the display. This is the target or "pre-selected" value that the pilot wishes the aircraft to attain. For example, if the pilot wishes to climb to 25,000 feet, he will dial 25,000 into the altitude display window and then press the alt\_eng button to engage the altitude mode. Once the target value is achieved or the mode is disengaged, the display reverts to showing the "current" value.

If the pilot dials in an altitude that is more than 1,200 feet above the current altitude and then presses the alt\_eng button, the altitude mode

The mode-control panel contains four buttons for selecting modes and three displays for dialing in or displaying values. The system supports the following four modes:

- attitude control wheel steering (att\_cws)
- flight path angle selected (fpa\_sel)
- altitude engage (alt\_eng)
- calibrated air speed (cas\_eng)

*events ::= press\_att\_cws | press\_cas\_eng | press\_alt\_eng | press\_fpa\_sel*

Only one of the first three modes can be engaged at any time. However, the cas\_eng mode can be engaged at the same time as any of the other modes. The pilot engages a mode by pressing the corresponding button on the panel. One of the three modes, att\_cws, fpa\_sel, or alt\_eng, should be engaged at all times. Engaging any of the first three modes will automatically cause the other two to be disengaged since only one of these three modes can be engaged at a time.

*mode\_status ::= off | engaged*

*off\_eng*  
*mode : mode\_status*  
*mode = off ∨ mode = engaged*

*AutoPilot*

Figure 1.12: An example of the original Autopilot specification.

Figure 1.13: An example of the Autopilot specification partially formalised.

### 1.2.3.1 ZMathLang steps for the autopilot case study.

We give the informal specification in figure 1.12 and one which we are beginning to formalised in figure 1.13. We have highlighted in red the parts which we have formalised in figure 1.13. The formalised parts of the semi formal specification are taken from the text in the informal specification.

We then annotate the partial formal specification in ZCGa annotations and ZDRa annotations taken from chapters ?? and ?? respectively. Once annotated we can check the annotated document for ZCGa and ZDRa errors.

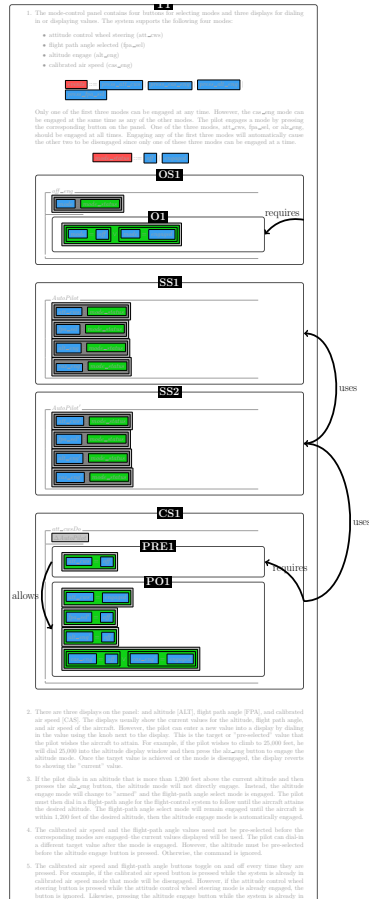


Figure 1.14: An example of the original Autopilot specification annotated in ZCGa and ZDRa.

Even though the specification is not fully formalised we can still annotate it with ZCGa and ZDRa and check for the correctness of the parts which have been annotated (shown in figures 1.14 and 1.15). When checking with ZDRa we have a warning message telling the user that the specification is not correctly totalised. That is there is a precondition outstanding with not postcondition counter part. This does not matter for now as we can still carry on with the translation.

When checking the specification for ZDRa, ZMathLang has also produced a dependency graph and goto graphs (shown in figures 1.16 and 1.17):

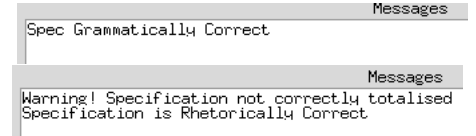


Figure 1.15: The outputting result when checking the autopilot specification with the ZCGa and ZDRa checkers.



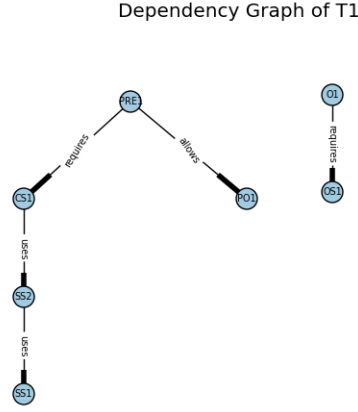


Figure 1.16: The dependency graph produced for the autopilot specification.

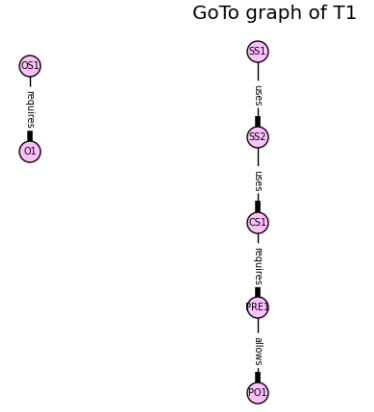


Figure 1.17: The goto graph produced for the autopilot specification.

With the dependency graph (figure 1.16) we can say that *SS2* uses *SS1*, *CS1* uses *SS2*, *PRE1* requires *CS1* and allows *PO1*. Which makes up the main tree dependencies. *OS1* and *O1* are separate as they do not have any relations with any parts of the main tree, the only dependency they have is on each other where *O1* requires *OS1*.

We can say that the dependency graph *describes* the relation between instances and the goto graph (figure 1.17) *orders* the instances in a way as to parse through a theorem prover.

We can now generate a general proof skeleton for the Autopilot specification even though it is not fully formalised (shown in figure 1.18). We can clearly see that the arrow has changed direction for the *OS1* and *O1* relationship from the dependency graph. Again since these two instances are not dependent on any part of the main tree they are separate. However in the dependency graph described the relation that *O1* *requires* *OS1* (*O1* root and *OS1* child) the goto graph flips this relationship as in a theorem prover we would need *OS1* to appear before *O1* since *O1* requires *OS1* to exist. We can also say that *SS2* uses *SS1* therefore *SS2* needs *SS1* to exist for itself to exist. Then *CS1* uses *SS2* therefore *CS1* needs *SS2* to exist for itself to exist.

exit. We can say that *PRE1* requires *CS1* and allows *PO1*. Therefore *PO1* needs *PRE1* to exist before it is allowed to exist itself.

```
stateSchema SS1
outputSchema OS1
output O1
stateSchema SS2
changeSchema CS1
precondition PRE1
postcondition P01
```

Figure 1.18: Gpsa for the Autopilot specification.

Since the Autopilot specification has passed the ZCGa and ZDRa checks we can then generate a Gpsa for the specification using the goto graph produced in the previous stage. The way this is done is described in section ???. Note that even though there is a *changeSchema* instance, there are no *stateInvariants* in the specification (as yet). Therefore ZMathLang does not generate any lemma's to prove in this case since ZMathLang only checks for consistency across the specification and thus need state invariants to be present.

```

theory gpsaln2
imports
Main

begin
(*DATATYPES*)

record SS1 =
(*DECLARATIONS*)

locale ln2 =
fixes (*GLOBAL DECLARATIONS*)
begin

definition OS1 ::
  "(*OS1_TYPES*) => bool"
where
  "OS1 (*OS1_VARIABLES*) == (O1

definition CS1 ::
  "(*CS1_TYPES*) => bool"
where
  "CS1 (*CS1_VARIABLES*) ==
    (PRE1
    ^ (P01)"

end
end

```

Figure 1.19: The Isabelle skeleton produced for the autopilot specification.

```

theory 5
imports
Main

begin
datatype events = press_att_cws
| press_cas_eng | press_alt_eng |
  press_fpa_sel
datatype mode_status = off | engaged

record AutoPilot =
  ALT_ENG :: "mode_status"
  CAS_ENG :: "mode_status"
  ATT_CWS :: "mode_status"
  FPA_SEL :: "mode_status"

locale theautopilot =
fixes alt_eng :: "mode_status"
and cas_eng :: "mode_status"
and att_cws :: "mode_status"
and fpa_sel :: "mode_status"
begin

definition off_eng ::
  "mode_status => bool"
where
  "off_eng mode == (mode = off ∨ mode =

definition att_cwsDo ::
  "mode_status => mode_status => mode_st
  mode_status => bool"
where
  "att_cwsDo fpa_sel' cas_eng' att_cws'
  alt_eng' ==
    (att_cws = off)
    ∧ (att_cws' = engaged)
    ∧ (fpa_sel' = off)
    ∧ (alt_eng' = off)
    ∧ (cas_eng' = off ∨
      cas_eng' = engaged)"

end
end

```

Figure 1.20: The autopilot specification in Isabelle syntax.

ZMathLang can automatically translate the Gpsa into Isabelle syntax (figure 1.19), this is now an Isabelle skeleton. The Isabelle skeleton has not yet taken the ZCGa information as one can get to this step with just the ZDRa annotated document. Once the Isabelle is filled in (figure 1.20) we have the annotated specification in Isabelle form. This can now give the user an idea of how to input their specification into Isabelle syntax, without them having prior knowledge of Isabelle. It is important to note that this is as far as the ZMathLang translation goes. Since there are no state Invariants with this case study no lemma's to check for consistency have been generated. The user can add the state Invariants in their raw  $\text{\LaTeX}$  specification, or fully formalise their specification. Another way to fully prove their specification is to add other properties to the Isabelle document.

## 1.3 Analysing examples

In this section we analyse the examples we have successfully translated into Isabelle and proved the sanity of the specification.

We remind the reader of figure ?? in chapter ?. ZMathLang is able assist the user with the translation of specification up to the point where sanity properties are produced but not proven. In our largest case study (section 1.2.1) the user did not have to look through all the state changing schema's and write the sanity checks for all of them. The properties were already generated for each changeSchema however, the user did have to go through each property and prove it. In total, there were 21 changeSchema's and 1 set of stateInvariants, therefore there was 21 properties which the user had to prove manually.

### 1.3.1 SteamBoiler

In our largest example there were 21 changeSchema's and 1 set of stateInvariants, therefore there was 21 properties which the user had to prove manually.

To prove the sanity of the steamboiler specification, we went through the consistency lemmas (automatically generated) one by one and manually prove them. We started of with unproven lemma's with the command '*sorry*' at the end such as the lemma shown in figure 1.21.

```

Lemma (in thesteamboiler) SNormalStop0_L1:
  "( $\exists$  steamboiler0 :: SteamBoiler0.
 $\exists$  a' :: OnOff.
 $\exists$  steamboiler0' :: SteamBoiler0.
 $\exists$  w_max' :: nat.
 $\exists$  w_min' :: nat.
 $\exists$  z' :: State .
 $\exists$  v' :: OpenClosed.
  (z = norm)
 $\wedge$  (w < w_min  $\vee$ 
  w > w_max)
 $\wedge$  (a' = off  $\wedge$ 
  z' = stop)
 $\longrightarrow$  (w_min < w_max
 $\wedge$  (w_min' < w_max')))"
sorry

```

Figure 1.21: The ‘SNormalStop0’ lemma taken from the steamboiler halfbaked proof.

We then delete the ‘*sorry*’ command and if sledgehammer is set up to be automatic, the user can sometimes leave their cursor at the end of the lemma and ‘*auto sledgehammer*’ finds a proof which is displayed in the output terminal. In our case, this has happened for the ‘SNormalStop0’ lemma which is displayed in figure 1.22.

```

proof (prove): depth 0
goal (1 subgoal):
  1.  $\exists$  steamboiler0 a' steamboiler0' w_max' w_min' z' v'.
      z = norm  $\wedge$  (w < w_min  $\vee$  w_max < w)  $\wedge$  a' = off  $\wedge$  z' = stop  $\longrightarrow$ 
      w_min < w_max  $\wedge$  w_min' < w_max'
Auto Sledgehammer ("cvc4") found a proof: by (smt State.distinct(9)).

```

Figure 1.22: Auto sledgehammer finding a proof for one of the lemma’s in the steamboiler specification using the SMT solver ‘*cvc4*’.

In this particular lemma we are proving the property that the SNormal\_Stop) schema does not conflict with the state invariants of the specification either before or after the state has been changed. Some other lemma’s in the steamboiler example (such as the SInitNormal1\_L9 lemma) could be proven by the isabelle command ‘*blast*’ as shown in figure 1.23.

```

∃ p_2' :: OnOff.
∃ p_1' :: OnOff.
∃ steamboiler0' :: SteamBoiler0.
∃ w_max' :: nat.
∃ w_min' :: nat.
∃ z' :: State .
∃ v' :: OpenClosed.
∃ a' :: OnOff.
(z = init)
∧ (d = 0)
∧ (k_w = works ∧
    k_d = works)
∧ (w ≥ w_min + d_max)
∧ (w ≤ w_max)
∧ (z' = norm)
∧ (v' = closed)
∧ (a' = on)
∧ (s' = w)
∧ ((PumpsOff p_4' steamboiler0 p_3' p_2' p_1' steamboiler0'))
→ (w_min < w_max
    ∧ (w_min' < w_max'))))
by blast

```

Figure 1.23: An example of a lemma in the steamboiler specification being proved by blast.

Proving by ‘*blast*’ is obviously less complex then the proof needed for lemma `SNormalStop0_L1` in figure 1.21, however if we look back to figure ?? in chapter ?? we can see that ‘*blast*’ covers less properties then ‘*sledgehammer*’. Therefore for the lemma’s in the steamboiler specification we have proved 2 lemma’s by blast and 19 using sledgehammer.

It is important to note that a single lemma can be proven in a variety of ways, so even though we have chosen to prove our specification in certain ways other users may choose to use other tools to prove their theorems and lemmas. Even though we have proved 19 lemmas by sledgehammer in the steamboiler specification, we might have been able to prove all the lemmas by sledgehammer but chosen to prove 2 by blast to show variety.

### 1.3.2 ModuleReg

The `modulereg` is one of our smallest examples, however with 1 set of stateinvariants and 2 changeSchemas, ZMathLang automatically produces 2 lemma's to check the sanity of the specification. An example of one of these lemmas is shown in figure 1.24. This is one of the lemma's automatically generated and thus we have the 'sorry' command at the end to show that it needs manual input from the user to complete the proof.

```

Lemma RegForModule_L1:
  "( $\exists$  degModules :: MODULE set.
 $\exists$  students :: PERSON set.
 $\exists$  taking :: (PERSON * MODULE) set.
 $\exists$  p :: PERSON.
 $\exists$  degModules' :: MODULE set.
 $\exists$  students' :: PERSON set.
 $\exists$  taking' :: (PERSON * MODULE) set.
 $\exists$  m :: MODULE.
  ((p  $\in$  students)
 $\wedge$  (m  $\in$  degModules)
 $\wedge$  ((p, m)  $\notin$  taking)
 $\wedge$  (taking' = taking  $\cup$  {(p, m)})
 $\wedge$  (students' = students)
 $\wedge$  (degModules' = degModules))
 $\wedge$  (Domain taking  $\subseteq$  students)
 $\wedge$  (Range taking  $\subseteq$  degModules)
 $\wedge$  (Domain taking'  $\subseteq$  students')
 $\wedge$  (Range taking'  $\subseteq$  degModules'))"
sorry

```

Figure 1.24: An example of one of the lemma's to check for consistency in the `modulereg` specification.

To prove this lemma we remove the 'sorry' command or put our cursor at the end of the lemma ready to input our methods to start the proof. In this case 'Auto sledgehammer' again found a proof using the 'cvc4' SMT solver (shown in figure 1.25). With this lemma we are aiming to prove the sanity of the specification where the changeSchema `RegForModule` does not conflict with the stateInvariants either before or after the state has been changed.

```

proof (prove): depth 0

goal (1 subgoal):
1.  $\exists \text{degModules students taking } p \text{ degModules' students' taking' } m.$ 
    $(p \in \text{students} \wedge$ 
    $m \in \text{degModules} \wedge$ 
    $(p, m) \notin \text{taking} \wedge \text{taking}' = \text{taking} \cup \{(p, m)\} \wedge \text{students}' = \text{students} \wedge \text{degModules}' = \text{degModules}) \wedge$ 
    $\text{Domain taking} \subseteq \text{students} \wedge$ 
    $\text{Range taking} \subseteq \text{degModules} \wedge \text{Domain taking}' \subseteq \text{students}' \wedge \text{Range taking}' \subseteq \text{degModules}'$ 
Auto Sledgehammer ("cvc4") found a proof: by (smt Domain_empty Domain_insert Range.intros Range_empty Range_
Range_insert Un_empty Un_insert_right empty_iff empty_subsetI empty_subsetI insert_mono insert_mono
singletonI singletonI singleton_insert_inj_eq' singleton_insert_inj_eq').

```

Figure 1.25: Output shown when proving the lemma ‘RegForModule’ shown in figure 1.24 .

By clicking on the auto solving method shown in figure 1.25 we can now complete the proof for the RegForModule\_L1 lemma. This is shown

```

Lemma RegForModule_L1:
"( $\exists \text{degModules} :: \text{MODULE set}.$ 
 $\exists \text{students} :: \text{PERSON set}.$ 
 $\exists \text{taking} :: (\text{PERSON} * \text{MODULE}) \text{ set}.$ 
 $\exists p :: \text{PERSON}.$ 
 $\exists \text{degModules}' :: \text{MODULE set}.$ 
 $\exists \text{students}' :: \text{PERSON set}.$ 
 $\exists \text{taking}' :: (\text{PERSON} * \text{MODULE}) \text{ set}.$ 
 $\exists m :: \text{MODULE}.$ 
 $((p \in \text{students})$ 
 $\wedge (m \in \text{degModules})$ 
 $\wedge ((p, m) \notin \text{taking})$ 
 $\wedge (\text{taking}' = \text{taking} \cup \{(p, m)\})$ 
 $\wedge (\text{students}' = \text{students})$ 
 $\wedge (\text{degModules}' = \text{degModules})$ 
 $\wedge (\text{Domain taking} \subseteq \text{students})$ 
 $\wedge (\text{Range taking} \subseteq \text{degModules})$ 
 $\wedge (\text{Domain taking}' \subseteq \text{students}')$ 
 $\wedge (\text{Range taking}' \subseteq \text{degModules}'))"$ 
by (smt Domain_empty Domain_insert Range.intros Range_empty
Range_insert Un_empty Un_insert_right empty_iff empty_subsetI
empty_subsetI insert_mono insert_mono singletonI singletonI
singleton_insert_inj_eq' singleton_insert_inj_eq')

```

Figure 1.26: The ‘RegForModule’ lemma proved using Auto sledgehammer methods.

The second lemma in the moduleReg specification we managed to prove using ‘blast’ thus having a complete proof for the complexity of the modulereg specification.

We can see that the complexity of the proof used for the RegForModule\_L1 lemma in the modulereg specification is larger than the complexity of the proof



for the `SNormalStop0_L1` in the steamboiler specification. Although we used ‘*Auto sledgehammer*’ to assist proving the lemma’s there are 16 methods used in proving the `RegForModule_L1` lemma (`Domain_empty`, `Range_empty` etc.) compared with 1 method used in proving the `SNormalStop0_L1` lemma (`State.distinct(9)`). Again we can say that there might of been an alternate way to prove these particular lemma’s however we have chosen to prove them in this way to show variation. Since there are more state changing schema’s in the steamboiler specification there are also more lemma’s to prove with the steamboiler then there is in the modulereg specification to obtain a fully proven specification which checks the complexity of the system.

### 1.3.3 Vending Machine

The vending machine example has 3 state changing schemas (shown in table 1.4) however since it does not have any labeled stateInvariants, ZMathLang can not automatically produce any properties to prove the consistency of the specification. If we refer back to figure ?? in chapter ?? it shows that the ZMathLang toolkit goes slightly past the point of ‘*specification in isabelle with no proof*’ however the automation of ZMathLang can only go past that point **if** there are changeschema’s and stateInvariants labelled. Otherwise the ZMathLang toolkit can only translate the specification into isabelle syntax with no lemma’s or properties to prove. Thus it is up to the user to carry on manually inputting their properties to obtain a fully proven specification.

### 1.3.4 Other examples

Each specification has a different amount of lemma’s which the user needs to prove depending on how many ‘stateInvariants’ and ‘changeSchema’s’ there are. If the specification does not have any stateInvariants then ZMathLang will not produce any lemmas to prove for the sanity of the specification.

We show the amount of lemmas and total amount of tactics needed to prove the sanity of each specification in table 1.7. Some lemma’s need only one tactic

Specification	Amount of generated lemmas	Total amount of tactics
Steamboiler	20	41
ProjectAlloc	5	18
VideoShop	3	3
TelephoneDirectory	4	8
ClubState	4	4
ZCGa	6	6
GenDB	4	13
Timetable	4	4
BirthdayBook	1	1
AutoPilot	0	0
ClubState2	3	4
Vending Machine	0	0
ModuleReg	2	18

Table 1.7: A table to show the amount of automatically generated lemmas and total amount of tactics used for each specification.

to be proved whilst other need a few. There are various different ways to prove these lemmas and it all comes down to the personal preference of the user. We have mainly used Isabelle's '*sledgehammer*' tool to assist us with our proving.

#### 1.3.4.1 SteamBoiler

The steamboiler has in total 20 lemmas to check the specifications sanity as it has 20 changing state schemas. The specification has 1 set of stateInvariants and we used 41 tactics to prove our lemmas.

The steamboiler state invariants is shown in the expression part of the stateSchema. The state invariants for this specification is that the minimum water level (`w_min`) must be smaller than the maximum water level (`w_max`). Therefore, the state invariants are written after '`assumes`' in the specification (`w_min < w_max`).

When ZMathLang produces the proof obligations to check the sanity of the specification it must also check that the changeSchema does not conflict with the prime state invariants. Thus, the proof obligations to check for all changing state schema will check that the preconditions and postconditions of each changeSchema still imply that (`w_min < w_max`) and (`w_min' < w_max'`).

For example to prove our first lemma `SNormalStop0_L1`, we wish to make sure that the `SNormalStop0` changeSchema does not conflict with the stateInvariants and

the stateInvariants prime.

<pre> lemma (in thesteamboiler) SNormalStop0_L1: "(\&lt;exists&gt; steamboiler0 :: SteamBoiler0. \&lt;exists&gt; a' :: OnOff. \&lt;exists&gt; steamboiler0' :: SteamBoiler0. \&lt;exists&gt; w_max' :: nat. \&lt;exists&gt; w_min' :: nat. \&lt;exists&gt; z' :: State. \&lt;exists&gt; v' :: OpenClosed. (z = norm) \&lt;and&gt; (w &lt; w_min w &gt; w_max) \&lt;and&gt; (a' = off \&lt;and&gt; z' = stop) \&lt;longrightarrow&gt; (w_min &lt; w_max \&lt;and&gt; (w_min' &lt; w_max')))" by (smt State.distinct(9)) </pre>	<p>begin</p> <p>Name of lemma</p> <p>list of all variables and types</p> <p>preconditions and postconditions of SNormalStop0 schema</p> <p>implies stateInvariants and stateInvariants prime</p>
---	--

Since there are 2 records in our steamboiler specification we have to manually write the line `(in thesteamboiler)` which lets isabelle know that the `changeSchema` uses variables from the first record. The `SNormalStop0_L1` lemma is proved using 2 tactics `smt` and `State.distinct(9)`. We remind the reader that although we have used these 2 tactics to prove our lemma there may be other ways in which to do so.

Our most complex proof is for the `SNormalContinue1_L11` lemma in which we use 3 tactics to prove the property that the `SNormalContinue1` `changeSchema` does not conflict with the state Invariants. The tactics we use here are `smt`, `OnOff.distinct(1)` and `pswitch.simps(1)`.

### 1.3.4.2 ProjectAlloc

The projectAlloc specification has 5 changeSchema's and 1 set of stateInvariants therefore it has 5 lemmas which ZMathLang has automatically generated to check for the consistency. To prove these lemmas we have in total used 18 tactics.

The lemmas to check that the changeSchema's did not conflict with the stateInvariants with were as follows:

```
\<exists> variables and types.

Preconditions

\<and>

PostConditions

\<longrightarrow>

(((dom studInterests) \<inter> (dom lecInterests) = {}))
\<and> (dom allocation \<subseq> dom studInterests)
\<and> (ran allocation \<subseq> dom lecInterests)
\<and> (dom maxPlaces = dom lecInterests)
\<and> (\<forall> lec \<in> dom maxPlaces.
(card ({l. the (allocation l) = lec})) \<leq> the (maxPlaces lec))
\<and> ((dom studInterests') \<inter> (dom lecInterests') = {})
\<and> (dom allocation' \<subseq> dom studInterests')
\<and> (ran allocation' \<subseq> dom lecInterests')
\<and> (dom maxPlaces' = dom lecInterests')
\<and> (\<forall> lec \<in> dom maxPlaces'.
(card ({l. the (allocation' l) = lec})) \<leq> the (maxPlaces' lec))))"
```

Our most complex lemma to prove is the AddLecturer\_L5 lemma which we used 9 tactics. The most simple lemma to prove was AddStudent\_L3 and DeAllocate\_L2 where we just used 1 tactic (fastforce and auto respectively).

#### 1.3.4.3 VideoShop

#### 1.3.4.4 TelephoneDirectory

#### 1.3.4.5 ClubState

#### 1.3.4.6 ZCGa

#### 1.3.4.7 GenDB

#### 1.3.4.8 Timetable

#### 1.3.4.9 BirthdayBook

#### 1.3.4.10 ClubState2

#### 1.3.4.11 ModuleReg

### 1.4 Reflection and Discussion

#### 1.4.1 How far can ZMathLang toolkit take us and what is left.

#### 1.4.2 Assumptions and limitations of the ZMathLang toolkit

##### Assumptions

- if 2 records (2 state schema). need to manually write the line to whichever the lemma needs
- Specification = 1 theory. Can't do more than 1 specification in 1 document.
- we assume the user wishes to check for consistency. As stated in 2proofs, the properties to prove is down to stakeholders

##### Limitations

- If we totalise preconditions e.g. `\totalises{TS#}{PRE#}` and all preconditions have been totalised then no warning. If we totalise schemas with preconditions within them e.g. `\totalises{TS#}{CS#}` then the ZDRa checker doesn't pick up on it.

- when viewing goto and dep graph if there are a lot of nodes they are all bundled together. Would be better if spaced out more.

## 1.5 Conclusion

Complete Evaluation and discussion chapter

# Bibliography

- [1] B. Beckert. An Example for Specification in Z: Steam Boiler Control. Universitat Koblenz-Landau, Lecture Slides, 2004.
- [2] L. Burski. ZMathLang Website. <http://www.macs.hw.ac.uk/~lb89/zmathlang/examples>, June 2016.
- [3] R. W. Butler. An introduction to requirements capture using PVS: Specification of a simple autopilot. NASA Technical Memorandum 110255, NASA Langley Research Center, Hampton, VA, May 1996.