별첨 3. Biweekly 보고서

Biweekly Research Progress Report

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Period	:	Week 14~15	
WBS	:	Spectrum Channel Reduction Block making	

Research Results in This Biweek

Producing a deep learning model that applies 1D-conv to the PCB dataset
Generate Data and Model Comparison

Research Items in Next Biweek

- Organize Data, visualize and Documentation

Issues and Overall Progress

Conclusion Based on Interim Research Results
- Successfully Designed Spectrum Channel Reduction Block

Progress Analysis for WBS
- Overall progress: 95% complete
- Documentation and data organization, visualization

Countermeasures for Potential Delays
- The size of the data is too large(640x640x214), which takes a lot of time to learn and test the model

Advisor Meeting Outcomes
- Advisor suggested applying other small models, that may avoid overfitting problems