

# Detecting Global Variables in Denotational Specifications

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Sufficient criteria are given for replacing all occurrences of the store argument in a Scott-Strachey denotational definition of a programming language by a single global variable. The criteria and transformation are useful for transforming denotational definitions into compilers and interpreters for imperative machines, for optimizing applicative programs, and for judging the suitability of semantic notations for describing imperative languages. An example transformation of a semantics of a **repeat**-loop language to one which uses a global store variable is given to illustrate the technique.

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## 1. INTRODUCTION

As the descriptive methods of denotational semantics become better understood, the task of automatically converting a denotational definition of a language into a processor coded in a low level imperative language also becomes feasible. Early work in the area emphasized generality [7, 10], and the generated processors were not practical to use because they were too large and too slow. Of the restrictions suggested to make the task easier, the most promising has been the use of combinators [2] as a semantic notation. Each combinator maps to a sequence of instructions in the target language, making the task of processor generation a matter of syntax directed translation. Unfortunately, it is not clear what underlying properties the combinators should possess to make them useful for the task, and so no ideal set has been agreed upon [3, 6, 9, 11–13, 18, 25, 26].

This paper examines one critical property that any denotational definition of a sequential programming language needs to possess: the sequential processing of that language's semantic store argument. Sufficient criteria are defined for statically checking that a store argument in a denotational definition is handled

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in a fashion which allows it to be modeled as a global variable in any implementation of the definition. This property is important in producing efficient implementations, as it encourages the natural introduction of side-effect-producing commands and control structures into the target language. The stated criteria are general enough to apply to any style of denotational definition—direct, continuation, or combinator—as they are defined upon typed lambda calculus expressions, which serve as the “assembly language” for all forms of denotational notation. The method is also applicable to developing optimizing compilers for applicative languages.

## 2. SINGLE-THREADING

The sequential processing property to be studied is called *single-threading* [21, 23]. It is best introduced by an example. The syntax of a simple **repeat**-loop language is given in Figure 1. A denotational semantics for the language maps the syntactic structures to mathematical entities such as functions and numbers. These entities are defined in Figure 2 using semantic algebras. Each algebra describes a domain of objects and the operations which utilize the domain. Operations upon primitive domains are also taken as primitive (e.g., *plus* in the algebra of natural numbers *Nat*), but all others are defined as functions using Strachey-style lambda calculus notation (e.g., *update* in the algebra *Store*). The semantic algebras are utilized in Figure 3 to give meaning to the syntax via the valuation functions. (All abstraction forms  $(\lambda x.M)$  are *strict*—call by value-like—and the function domains to which they belong are *lifted* [17].)

Aside from the mathematical insight that a denotational definition provides, the main benefit of such a definition is that the semantics notation can be treated operationally as well. Figure 4 shows an operational-like interpretation of a sample program when started with an *empty* store. The valuation equations are handled as rewriting rules [5], simplifying the program plus its input store to an output store. The operations of the semantic algebras are treated as encapsulated functions, and so the steps taken in the evaluation of, say, (*access*[*x*]*s*1) to *zero*, are not explicitly shown.

The reader who has some experience with denotational semantics will recognize that the definition of Figure 3 appears reasonable for an imperative language because the semantic store argument is treated in a sequential fashion when passed as a parameter. This is confirmed in the example of Figure 4, where apparent multiple copies of stores in lines (4), (7), and (8) have the same value—a single shared copy would suffice. Further, store updating via the *update* operation only occurs in those configurations where the store is not otherwise accessed. This suggests that the individual instances of the store argument can be replaced by access rights to a single global variable, and the *update* operation can be made to cause a side effect. A semantic definition whose store argument can be replaced by access rights to a single global variable while preserving operational properties is said to be *single-threaded* (in its store). In Section 3, sufficient criteria for single-threading are introduced.

The example will be transformed to make use of a variable to represent the store arguments. First, the store semantic algebra of Figure 1 is converted to first

**P:** Program  
**C:** Command  
**E:** Expression  
**I:** Identifier  
**N:** Numeral  
 $P ::= C.$   
 $C ::= I := E \mid C_1; C_2 \mid \text{if } E \text{ then } C_1 \text{ else } C_2 \mid \text{repeat } C \text{ until } E$   
 $E ::= E_1 + E_2 \mid I \mid N$

Fig. 1. Abstract syntax.

I.  $t: Tr$  (truth values)  
      $true: Tr$   
      $false: Tr$   
      $not: Tr \rightarrow Tr$   
 II.  $n: Nat$  (natural numbers)  
      $err: Nat$   
      $zero: Nat$   
      $one: Nat$   
     ...  
      $plus: Nat \rightarrow Nat \rightarrow Nat$   
      $equals: Nat \rightarrow Nat \rightarrow Tr$   
 III.  $i: Id = Identifier$   
      $[A]: Id$   
      $[B]: Id$   
     ...  
      $equalid: Id \rightarrow Id \rightarrow Tr$   
 IV.  $s: Store = Id \rightarrow Nat$   
      $empty: Store$   
      $empty = \lambda i. err$   
      $access: Id \rightarrow Store \rightarrow Nat$   
      $access\ i\ s = s(i)$   
      $update: Id \rightarrow Nat \rightarrow Store \rightarrow Store$   
      $update\ i\ n\ s = \lambda i'. i' equalid\ i \rightarrow n \sqcup s(i')$

Fig. 2. Semantic algebras.

**P:**  $Program \rightarrow Store \rightarrow Store$   
      $P[C.] = C[C]$   
**C:**  $Command \rightarrow Store \rightarrow Store$   
      $C[I := E] = \lambda s. update[I](E[E]s)s$   
      $C[C_1; C_2] = C[C_2] \circ C[C_1]$   
      $C[\text{if } E \text{ then } C_1 \text{ else } C_2] = \lambda s. (E[E]s\ equals\ zero \rightarrow C[C_1] \sqcup C[C_2])s$   
      $C[\text{repeat } C \text{ until } E] = f \circ C[C]$   
         where  $f = \lambda s. (not\ (E[E]s\ equals\ zero) \rightarrow f \circ C[C] \sqcup skip)s$   
**E:**  $Expression \rightarrow Store \rightarrow Nat$   
      $E[E_1 + E_2] = \lambda s. (E[E_1]s)\ plus\ (E[E_2]s)$   
      $E[I] = \lambda s. access\ [I]s$   
      $E[N] = \lambda s. N[N]$   
**N:**  $Numeral \rightarrow Nat$  (omitted)

Fig. 3. Valuation functions.

- (1)  $P[x := 0; y := x + 1.] \text{ empty}$
- (2)  $= C[x := 0; y := x + 1] \text{ empty}$
- (3)  $= C[y := x + 1](C[x := 0] \text{ empty})$
- (4)  $= C[y := x + 1](\text{update}[x](E[0] \text{ empty}) \text{ empty})$
- (5)  $= \dots = C[y := x + 1](\text{update}[x] \text{ zero empty})$
- (6)  $= C[y := x + 1] s1, \text{ where } s1 = \lambda i'. i' \text{ equalid}[x] \rightarrow \text{zero} \sqcap \text{empty}(i')$
- (7)  $= \text{update}[y](E[x + 1] s1) s1$
- (8)  $= \dots = \text{update}[y](\text{access}[x] s1 \text{ plus } (E[1] s1)) s1$
- (9)  $= \dots = \text{update}[y](\text{zero plus one}) s1$
- (10)  $= \text{update}[y](\text{one}) s1$
- (11)  $= s2, \text{ where } s2 = \lambda i'. i' \text{ equalid}[y] \rightarrow \text{one} \sqcap s1(i')$

Fig. 4. An example derivation.

IV.'  $s: \text{Store} = (Id \times Nat)^*$   
 $\text{empty}: \text{Store}$   
 $\text{empty} = \langle \rangle$   
 $\text{access}: Id \rightarrow \text{Store} \rightarrow Nat$   
 $\text{access } i \text{ } s = \text{let lookup } i' s' =$   
 $\quad \text{null } s' \rightarrow \text{err}$   
 $\quad \sqcap \text{let } (j, n) = \text{hd } s' \text{ in}$   
 $\quad \quad i' \text{ equalid } j \rightarrow n$   
 $\quad \quad \sqcap \text{lookup } i' (\text{tl } s')$   
 $\quad \text{in lookup } i \text{ } s$   
 $\text{update}: Id \rightarrow Nat \rightarrow \text{Store} \rightarrow \text{Store}$   
 $\text{update } i \text{ } n \text{ } s = (i, n) \text{ cons } s$

Fig. 5. Result of defunctionalization.

order form by converting the functional data domain to a nonfunctional one. The step demonstrates that a store is indeed implementable as a data structure on a conventional machine. There exist many choices of first order representations of functions, and for simplicity, lists of  $Nat$ ,  $Id$  ordered pairs are used. The operations in the  $\text{Store}$  algebra are altered to deal with the new representation. The mechanics of this step have been documented elsewhere [4, 8, 20, 24] and are not given here. Figure 5 contains the new semantic algebra for the store; all other parts of the definition remain the same.

Introduction of the global variable occurs in two steps. First, the  $\text{Store}$  algebra is converted to a SIMULA-like class; a single  $\text{Store}$ -typed variable resides within. The operations of the class have permission to access the variable directly, and in the case of the  $\text{Store}$ -valued operations  $\text{empty}$  and  $\text{update}$ , to alter the variable's value using a newly introduced assignment construct  $\leftarrow$ . This transformation means that the store is no longer needed as an explicit argument to the class' operations. Second, all explicit occurrences of  $\text{Store}$ -typed variables are explicitly removed from the valuation functions; even the lambda abstractions on stores disappear. The results are shown in Figure 6. Although the stores have been removed, access rights to the store variable must be remembered. One method for doing so is within the type system of the semantics notation. For example,

$$C[I := E] = \text{update}[I] E[E]$$

IV." **var**  $s$ :  $Store = (Id \times Nat)^*$   
 $empty = (s \leftarrow \langle \rangle)$   
 $access\ i = (\dots \text{ as in Figure 5.} \dots)$   
 $update\ i\ n = (s \leftarrow (i, n) \text{ cons } s)$   
**P**:  $Program \rightarrow Store \rightarrow Store$   
 $P[C.] = C[C]$   
**C**:  $Command \rightarrow Store \rightarrow Store$   
 $C[I := E] = update[I] E[E]$   
 $C[C_1; C_2] = C[C_2] \circ C[C_1]$   
 $C[\text{if } E \text{ then } C_1 \text{ else } C_2] = E[E] \text{ equals zero} \rightarrow C[C_1] \sqcup C[C_2]$   
 $C[\text{repeat } C \text{ until } E] = f \circ C[C]$   
 where  $f = not(E[E] \text{ equals zero}) \rightarrow f \circ C[C] \sqcup skip$   
**E**:  $Expression \rightarrow Store \rightarrow Nat$   
 $E[E_1 + E_2] = E[E_1] \text{ plus } E[E_2]$   
 $E[I] = access[I]$   
 $E[N] = N[N]$

Fig. 6. Result of variable introduction.

$P[x := 0; y := x + 1.]empty$   
 $= P[x := 0; y := x + 1.], \quad \text{and } s0 = \langle \rangle$   
 $= C[x := 0; y := x + 1]$   
 $= C[y := x + 1](C[x := 0])$   
 $= C[y := x + 1](update[x] E[0])$   
 $= \dots = C[y := x + 1](update[x] zero)$   
 $= C[y := x + 1], \quad \text{and } s1 = \langle ([x], zero) \rangle$   
 $= update[y] E[x + 1]$   
 $= \dots = update[y]((access[x]) \text{ plus } E[1])$   
 $= \dots = update[y](zero \text{ plus one})$   
 $= update[y] one$   
 $= \langle \rangle, \quad \text{and } s2 = \langle ([y], one), ([x], zero) \rangle$

Fig. 7. The evaluation revisited.

is typed as (let  $S = Store, I = Id, N = Nat$ ):

$$C[I := E]^{S \rightarrow S} = (((update^{I \rightarrow N \rightarrow S \rightarrow S}[I]^I)^{N \rightarrow S \rightarrow S}(E[E]^{S \rightarrow N})^N)^{S \rightarrow S})^{S \rightarrow S}.$$

Coercions such as  $(E[E]^{S \rightarrow N})^N$  denote access rights to the store variable which have been granted; here,  $E[E]$  is allowed to evaluate as if it had an explicit store parameter. An alternative is to explicitly represent the access rights by dummy placeholders  $()$ ; the previous equation would appear as

$$C[I := E] = \lambda().update[I] (E[E]()) ().$$

The two formats are equivalent. The evaluation of Figure 4 is repeated using the new notation in Figure 7.

### 3. CRITERIA FOR SINGLE THREADING

Criteria which are sufficient for ensuring that a global variable can be introduced into a denotational definition are now given. The proof of sufficiency of the criteria is dependent upon the use of a subtree replacement system [5, 15] as an

operational semantics for the semantic notation. An operator definition  $fx_1 \dots x_n = M$  is treated as a rewrite rule ( $\delta$ -rule [2])  $fx_1 \dots x_n > M$ . An instance  $R = (fN_1 \dots N_n)$  in an expression  $E$  can be rewritten to  $M$  instantiated with arguments  $N_i$  for each occurrence of  $x_i$  in  $M$ . The result is substituted into  $E$ . A fundamental rewrite rule for the lambda calculus-based semantic notation is the binding rule ( $\beta$ -rule)  $(\lambda x.M)N > [N/x]M$ : argument  $N$  is substituted for all free occurrences of identifier  $x$  in  $M$ .

There exist a number of strategies for applying the rewrite rules to an expression. The one used in this paper is a form of call by value. A subexpression is *active* if it is not properly contained within an abstraction. Second, an expression is in *restricted normal form* (rnf) if it is closed (has no unbound variables) and no active subexpression of it matches the left-hand side of a rewrite rule. A *redex* is an active, closed expression that matches the left-hand side of a rewrite rule and whose proper, active subexpressions are in rnf. The call-by-value rewrite system evaluates an expression by rewriting its redexes until no more exist. If the original expression was closed, the final result (if one exists) will be in rnf.

The rule set induced from a Strachey-style denotational definition is confluent [2, 5, 15], and the strategy of choosing closed, active redexes with rnf arguments is sufficient for attaining rnfs for closed expressions [15]. Since redexes must be closed expressions, the confluence problems that other call-by-value systems possess [16] are avoided.

Two assumptions about the denotational definitions studied are made. First, the semantic data types (domain expressions) are built only from a set of primitive types (e.g., *Tr*, *Nat*, ...) and the function space constructor  $\rightarrow$ . Recursive types are allowed. This keeps the single-threading criteria simpler; product spaces are added later in the paper. Second, the *Store* data type, the candidate for conversion, is first order (nonfunctional). This can be accomplished by using the transformation noted in Section 2.

The criteria for single threading may now be given. Recalling Figure 4, an occurrence of an identifier  $s:Store$  in an expression  $E$  is a form of access right to the store's current value; a *Store*-typed redex is an update right. No update right may be a redex in an expression that also contains access rights. Say that a *Store*-typed identifier is a *trivial Store*-typed expression; all other *Store*-typed expressions are *nontrivial* (*Store*-typed expressions may be nested).

**Definition 2.1.** A closed expression  $F$  is single-threaded (in its store argument) if all subexpressions  $E$  of  $F$  possess the properties:

A. Noninterference

- (i) If  $E$  is *Store*-typed, then if  $E$  contains multiple, disjoint active occurrences of *Store*-typed expressions, then they are the same trivial identifier.
- (ii) If  $E$  is not *Store*-typed, all occurrences of active *Store*-typed expressions in  $E$  are the same trivial identifier.

B. Immediate evaluation

- (i) If  $E = (\lambda x.M): Store \rightarrow D$ , then all active *Store*-typed identifiers in  $M$  are occurrences of  $x$ .

- (a)  $C[C_1; C_2] = \lambda s. C[C_2](C[C_1]s)$
- (b)  $E[E_1 + E_2] = \lambda s. E[E_1]s \text{ plus } E[E_2]s$
- (c)  $P[\text{procedure } C \text{ using } I] = \lambda n. \lambda s. C[C](\text{update}[I]n s)$   
 where  $P: \text{Procedure} \rightarrow \text{Nat} \rightarrow \text{Store} \rightarrow \text{Store}$
- (d)  $M[\text{plus1}] = \lambda n. \text{succ } n$   
 where  $M: \text{Operator} \rightarrow \text{Nat} \rightarrow \text{Nat}$
- (e)  $C[C_1 \text{ op } C_2] = \lambda s. C[C_1]s \text{ combine } C[C_2]s$
- (f)  $E[\text{valof } C \text{ is } E] = \lambda s. E[E](C[C]s)$
- (g)  $Q[\text{procedure } C] = \lambda s. (\lambda s'. C[C]s)$   
 where  $Q: \text{Procedure} \rightarrow \text{Store} \rightarrow (\text{Store} \rightarrow \text{Store})$
- (h)  $C[\text{call } P(E)] = \lambda s. F[P](\lambda n. E[E]s)s,$   
 where  $F: \text{Procedure} \rightarrow (\text{Nat} \rightarrow \text{Nat}) \rightarrow \text{Store} \rightarrow \text{Store}$

Fig. 8. Examples.

- (ii) If  $E = (\lambda x. M): C \rightarrow D$ , and  $C$  is not *Store*-typed, then  $M$  contains no active *Store*-typed expressions.

It is informative to view examples of compliance with and violation of the requirements, as these provide intuition as to why the criteria are the proper ones. See Figure 8. Expressions (a)–(d) are single-threaded. In particular, expression (a) complies with property (Ai) of Definition 2.1 for its *Store*-typed expressions are nested, not disjoint. The call-by-value reduction strategy forces a lock-stepped evaluation. Expression (b) shows the proper use of multiple access rights, complying with property (Aii) of Definition 2.1, and expression (c) complies with property (Bi), of Definition 2.1, for the occurrence of the store variable  $s$  is properly bound to the enclosing  $\lambda s$ , guaranteeing that the call-time store will be used with the procedure upon invocation. Expression (d) clearly satisfies property (Bii). Property (Ai) is violated by expression (e), for the disjoint active occurrences of  $C[C_1]s$ , and  $C[C_2]s$  suggest that  $C_1$  and  $C_2$  will each need local stores to properly complete the noninterfering evaluation. Expression (f) violates property (Aii), which creates a problem when it is used in an expression such as  $[(\text{valof } (C) \text{ is } E_1) + E_2]$ : a local store for  $E_1$  is needed. Expression (g) violates property (Bi), for the procedure object saves its declaration-time store for evaluation and ignores its call-time one. Finally, expression (h) violates property (Bii), for the “thunk”  $(\lambda n. E[E]s)$  created at procedure call time saves the call-time store for later use. All of expressions (e)–(h) can be intuitively seen as difficult to implement; Definition 2.1 provides the means for systematically detecting all such expressions.

Definition 2.1 needs to be supported by an additional restriction to ensure that an expression which uses  $(\delta-)$  operators is indeed single-threaded; for example, expression (c) of Figure 9 is single-threaded provided that the operators *update* and that  $C[C]$  both act in a single-threaded fashion. Say that an operator  $f$ , defined as  $fx_1 \dots x_n = e$ , is *acceptable* if  $\lambda x_1 \dots \lambda x_n. e$  is single-threaded. The operations of the *Store* algebra are taken as acceptable by definition, since they are assumed to be primitive and deal with the internal representation of the store.

The main results are as follows:

**THEOREM 2.2.** *An expression  $E$  which is single-threaded in its store argument and uses acceptable operators can be implemented by converting the Store algebra into a variable-containing class and by transforming  $E$  as directed in Section 2.*

$\text{Replace: } \mathbf{I} \rightarrow \mathbf{L}$   
 $\text{Replace} = \text{replace} \circ \text{subst}$   
 where  $\text{subst}(E, \nu) = [\nu / ( )]E$ ,  
 that is, all active occurrences of  $( )$  in  $E$  are replaced by  $\nu$ ,  
 and  
 $\text{replace}(f) = f$   
 $\text{replace}(x) = x$   
 $\text{replace}(( )) = s : \text{Store}$   
 $\text{replace}(\lambda x. M) = \lambda x. \text{replace } M$   
 $\text{replace}(\lambda ( ). M) = \lambda s : \text{Store}. \text{replace } M$   
 $\text{replace}((MN)) = (\text{replace } M \text{ replace } N)$

$\text{Convert: } \mathbf{L} \rightarrow \mathbf{I}$   
 $\text{Convert} = \text{init} \circ \text{convert}$   
 where  $\text{init}(E) = ([ ( ) / k ] E, k)$ ,  
 that is,  $[ ( ) / k ] E$  is  $E$  with the active occurrences of  $\text{rnf}$   
 $\text{Store-typed constant}(s) k$  replaced by  $( )$ .  
 and  
 $\text{convert}(f) = f$   
 $\text{convert}(x : C) = x : C,$  if  $C \neq \text{Store}$   
 $\text{convert}(s : \text{Store}) = ( )$   
 $\text{convert}(\lambda x : C. M) = \lambda x : C. \text{convert } M,$  if  $C \neq \text{Store}$   
 $\text{convert}(\lambda s : \text{Store}. M) = \lambda ( ). \text{convert } M$   
 $\text{convert}((MN)) = (\text{convert } M \text{ convert } N)$

Fig. 9. Definitions of *Replace* and *Convert*.

**COROLLARY 2.3.** *A denotational definition whose semantic algebras use acceptable operators and whose valuation equations are acceptable can be implemented with a Store-containing class.*

The proof of Theorem 2.2 is an interpreter equivalence proof and is outlined in Section 4. Corollary 2.3 can be used to justify both interpreters and compilers for a language defined denotationally; these options are discussed in Section 5.

#### 4. THE IMPLEMENTATION PROOF

The proof of Theorem 2.2 requires a precise formulation of the semantic notation, the intermediate notation with classes, the respective semantic algebras, rewriting rules, and replacement strategies. This task requires more space than what is available here, so the reader is referred to a companion paper [22] for the details. The main ideas are given here and are straightforward.

If an expression  $M$  rewrites in one step to an expression  $N$ , abbreviate the action by stating  $M > N$ . If  $M$  writes to  $N$  in zero or more steps, say  $M \geq N$ . The following two properties of single-threaded expressions are critical:

**LEMMA 3.1.** *If expression  $E$  is single-threaded and  $E \geq E'$ , then all abstractions in  $E'$  are single-threaded.*

**PROOF.** By induction on the number of rewrites to reach  $E'$  from  $E$ . The basis is immediate. For the inductive step, let  $E \geq E''$ , and  $E'' > E'$  by the contraction of a redex  $R$  in  $E''$ . Those abstractions in  $E'$  disjoint from  $R$  in  $E''$



remain single-threaded. If  $R = (\lambda x.M)N$  is a  $\beta$ -redex, consider  $[N/x]M$ : if  $N$  is *Store*-typed, then it will not be substituted for  $x$  into the body of any abstraction in  $M$ , for to do so would imply that the abstraction does not have property B of Definition 2.1. Hence all abstractions in  $M$  remain as they were—single threaded. In the case that  $N$  is not *Store*-typed, since  $N$  is in rnf, it contains no active *Store*-typed subexpressions (this fact requires a small, separate proof), so  $N$ 's substitution for  $x$  maintains properties A and B in  $[N/x]M$ . On the other hand, if  $R = (fN_1 \dots N_m)$  is a  $\delta$ -redex, then since  $f$  is an acceptable operator and all of  $N_1, \dots, N_m$  are in rnf, reasoning similar to that just given shows that  $[N_m/x_m] \dots [N_1/x_1]M$  contains only single-threaded abstractions.  $\square$

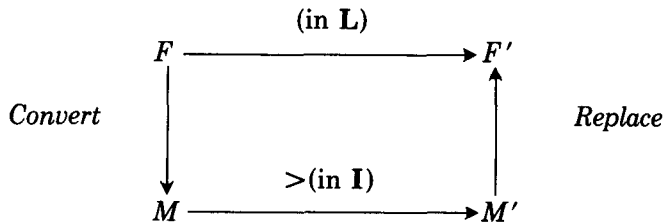
**LEMMA 3.2.** *If  $E$  is single threaded,  $E \geq E'$ , and there exist multiple, disjoint occurrences of active Store-typed expressions in  $E'$ , then all are in rnf and possess the same value.*

**PROOF.** The proof is again an inductive one, and the basis is immediate. For the inductive step, say that  $E \geq E'' > E'$  and let  $R$  be the redex contracted. If  $R$  is a *Store*-typed redex, by the inductive hypothesis it must be the only one. Regardless of  $R$ 's internal structure, by Lemma 3.1, by property A of Definition 2.1, and by the definition of acceptable  $\delta$ -operator,  $E'$  has the needed property. If  $R$  is non*Store*-typed, then consider  $\beta$ -redex  $R = (\lambda x.M)N$ . If  $N$  is non*Store*-typed, then no new, active *Store*-typed expressions are found in  $[N/x]M$ , by Lemma 3.1 and property (Bii) of Definition 2.1. If  $N$  is *Store*-typed, then by Lemma 3.1 and properties (Aii) and (Bi) of Definition 2.1, all active *Store*-typed expressions in  $M$  are occurrences of  $x$ ; hence no new clashes are introduced by  $[N/x]M$ . The reasoning is similar for a  $\delta$ -redex.  $\square$

Another useful property of a single-threaded expression  $E$  is that it can be written ( $\alpha$ -converted [2]) in a format which uses but one *Store*-typed identifier  $s$ . The lone identifier suffices throughout any derivation from  $E$ , that is, no *Store*-typed identifiers ever need to be renamed when doing rewritings. Because of this fact and Lemma 3.2, an obvious way to efficiently simulate the evaluation of a single-threaded expression  $E$  is to replace the active *Store*-typed subexpressions in  $E$  by occurrences of a marker  $(\ )$ . Since the subexpressions are in rnf and have the same value, a global variable  $\nu$  holds one copy of the value. The simulation represents an expression  $E'$  by a pair  $(E'', \nu)$ , where  $E''$  is  $E'$  with active store values  $\nu$  replaced by  $(\ )$ . Evaluation proceeds as before, but whenever a *Store*-typed redex  $R$  in  $E'$  is contracted to an rnf value, the result is placed in  $\nu$  and  $R$  is replaced by  $(\ )$  in  $E'$  (cf. Figure 7). Let  $\mathbf{L}$  represent the original semantic notation and  $\mathbf{I}$  be the one using the markers and global variable. A map *Convert*:  $\mathbf{L} \rightarrow \mathbf{I}$  translates an  $\mathbf{L}$ -expression to an  $\mathbf{I}$ -expression, and *Replace*:  $\mathbf{I} \rightarrow \mathbf{L}$  reinserts the variable's value in place of active markers to build an  $\mathbf{L}$ -expression. The functions are defined in Figure 9. The operational equivalence proof is formalized as follows:

**THEOREM 3.3.** *For a single-threaded expression  $F$  the following holds:  $\text{Convert}(F) \geq M'$  (in  $\mathbf{I}$ ),  $M'$  is in rnf, and  $\text{Replace}(M')$  equals  $F'$ , iff  $F \geq F'$  (in  $\mathbf{L}$ ) and  $F'$  is in rnf.*

The proof is a standard tracking proof [4, 27] of the **L** system's rewrites by the **I** system, where the induction step is the commutative diagram:



Lemma 3.2 is the key to making the diagram commute.

## 5. APPLICATIONS AND EXTENSIONS

The primary application for the criteria of Definition 2.1 was mentioned in the introduction—the development of a processor for programs written in a language defined by a denotational definition. Processors can be either interpreters or compilers. An interpreter uses the valuation equations to direct the processing steps taken by an abstract machine which uses representations of the semantic notation and the global variable. The evaluation history of a program on the interpreter resembles the sequence seen in Figure 7. If a compiler-oriented viewpoint is taken, the compiler treats the transformed denotational equations as a translation scheme. A program is completely translated to an expression in the semantic notation and then evaluated. A structural induction over the syntax of the input language justifies Corollary 2.3 and the compiler viewpoint.

Another use of the criteria is in the optimization of applicative programs. In conventional computer architectures, typical implementations of applicative programs are inefficient and waste memory. The single-threading criteria complements data flow analysis [14] for detecting situations in which destructive updates apply and storage can be better managed.

It is also possible to use Definition 2.1 to judge if a proposed combinator set for defining implementable sequential languages is useful: map each combinator to its underlying lambda expression and check for single-threadedness. Application of the criteria to existing systems [13, 26] makes apparent that the implicit sequential handling of the store is a key feature of the combinators.

An obvious extension to the results of Section 2 is to admit product domains into the type system of the semantic notation. This can be handled in a number of ways; one easy approach is to admit the pairing constructor  $(a, b): A \times B$ , for  $a:A$  and  $b:B$ , into the notation along with its associated subscripting operations *fst* and *snd*. A pair is in *rnf* iff its components are. Treat any pair  $(M, N)$ , where either  $M$  is *Store*-typed or  $N$  is *Store*-typed, as also being “*Store*-typed.” Definition 2.1 and its consequences carry through as before. By using tuples, multiple global variables can be created. Since disjoint unions can be simulated with tuples, Definition 2.1 can deal with them as well. Finally, by creating local semantic algebras as needed, local variables within fixed scopes of the semantic definition are possible. These extensions suggest that the basic single-threadedness property has wide and useful application.

## 6. CONCLUSIONS

Simple, statically checkable criteria for determining the suitability of an imperative implementation of a function expression's store argument have been given. The criteria are useful for directing the implementation of denotational definitions as well as detecting possible optimizations in functional programs. The criteria are also helpful for analyzing the suitability of combinator notations for specifying implementations of languages. Finally, the hybrid imperative/applicative notation seen in Figure 6 provides insight into the relationship between functional and imperative languages and shows a useful transition between the two.

The example in Section 1 was in direct semantics style [23], as this format is most removed from sequential imperative languages. Most implementation-oriented notations use continuation style—now it is easy to see why. One of the primary features of a continuation semantics is its tail-recursive passing of stores. In any configuration of a partially interpreted program expression in continuation form, there is exactly one active occurrence of a store. In fact, a properly written continuation semantics definition of a programming language will contain no occurrences of *Store*-typed expressions at all! Such a definition is trivially single threaded. The results reported here may be viewed as a generalization of conventional continuation style sequentiality.

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