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## Tight lower bounds for certain parameterized NP-hard problems

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## Abstract

Based on the framework of parameterized complexity theory, we derive tight lower bounds on the computational complexity for a number of well-known NP-hard problems. We start by proving a general result, namely that the parameterized weighted satisfiability problem on depth-t circuits cannot be solved in time  $n^{O(k)}m^{O(1)}$ , where n is the circuit input length, m is the circuit size, and k is the parameter, unless the (t-1)-st level W[t-1] of the W-hierarchy collapses to FPT. By refining this technique, we prove that a group of parameterized NP-hard problems, including weighted sat, hitting set, set cover, and feature set, cannot be solved in time  $n^{O(k)}m^{O(1)}$ , where n is the size of the universal set from which the k elements are to be selected and m is the instance size, unless the first level W[1] of the W-hierarchy collapses to FPT. We also prove

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that another group of parameterized problems which includes weighted q-sat (for any fixed  $q \geqslant 2$ ), clique, independent set, and dominating set, cannot be solved in time  $n^{\mathrm{O}(k)}$  unless all search problems in the syntactic class SNP, introduced by Papadimitriou and Yannakakis, are solvable in subexponential time. Note that all these parameterized problems have trivial algorithms of running time either  $n^k m^{\mathrm{O}(1)}$  or  $\mathrm{O}(n^k)$ . © 2005 Elsevier Inc. All rights reserved.

## 1. Introduction

Solving well-known NP-hard problems with small parameter values has found important applications recently in areas such as computational biology. For instance, the Computational Biochemistry Research Group at the ETH Zürich has successfully applied the algorithms for the VERTEX COVER problem (determine whether a given graph G has a vertex cover of size k) to their research in multiple sequence alignments [21,22], where the parameter value k can be bounded by 100. In the study of motif finding problem in computational biology, Pevzner and Sze [20] proposed a graph theoretical formulation that requires finding cliques of size k, where a typical value of k is 20. This approach has been followed by a steady stream of combinatorial approaches trying to improve the performance of motif finding algorithms (see, e.g., Buhler and Tompa [2] and their references).

However, from the parameterized complexity point of view [11], these two problems are very different. The VERTEX COVER problem is *fixed-parameter tractable* in the sense that it can be solved in time  $f(k)n^c$ , where f(k) is a function of the parameter k and c is a fixed constant. After many rounds of improvement, the best known algorithm for VERTEX COVER runs in time  $O(1.285^k + kn)$  [7], which has been implemented and is quite practical for parameter values up to 400 [4]. On the other hand, the CLIQUE problem is W[1]-hard, and thus, it is unlikely to be fixed-parameter tractable. The best known algorithm for finding a clique of size k in a graph of n vertices runs in time  $O(n^{0.8k})$  [17], based on a combination of exhaustive search and the best matrix multiplication algorithm [8]. Obviously, such an algorithm is not practically feasible even for parameter values as small as 20.

On the surface, the W[1]-hardness of CLIQUE implies that any algorithm of running time  $O(n^h)$  solving the problem *must* have the degree h of the polynomial  $n^h$  a function of the parameter k unless W[1] = FPT. However, this does not exclude the possibility that CLIQUE becomes feasible for small values of the parameter k. For instance, if CLIQUE is solvable by an algorithm running in time  $O(n^{\lg \lg k})$ , then such an algorithm is still feasible for moderately small values of k.

In this paper, we show that using the notion of W-hardness, we can prove much stronger lower bounds on the computational complexity for a large group of NP-hard parameterized problems, including the well-known problems weighted sat, dominating set, hitting set, set cover, feature set, independent set, and clique. These problems share a common property that each instance of size m of the problems has a universal set U of size n, and we are looking for a subset of k elements in U that meets certain given conditions. Note that all these problems have trivial algorithms of running time  $n^k m^{O(1)}$ , which simply enumerate all subsets of size k in the universal set U to find a subset satisfying the given conditions in case it exists.

<sup>&</sup>lt;sup>1</sup> An immediate question that might come to mind is whether such a W[1]-hard problem exists. The answer is affirmative: by re-defining the parameter, it is not difficult to construct W[1]-hard problems that are solvable in time  $O(n^{\lg \lg k})$ .

We start by developing a general result showing that in each level of the W-hierarchy, there is a natural parameterized problem that has time complexity  $n^{\Omega(k)}m^{O(1)}$  unless an unlikely collapse occurs in the parameterized complexity theory. More specifically, we prove that for every  $t \ge 2$ , the weighted satisfiability problem on depth-t circuits cannot be solved in time  $n^{O(k)}m^{O(1)}$ , where n is the circuit input length and m is the circuit size, unless the (t-1)-st level W[t-1] of the W-hierarchy collapses to FPT. By refining this technique and employing proper reductions, we are able to prove that, unless the first level W[1] of the W-hierarchy collapses to FPT, a group of W[2]-hard parameterized problems cannot be solved in time  $n^{O(k)}m^{O(1)}$ , where n is the size of the universal set from which the k elements are to be selected and m is the instance size. This group of W[2]-hard problems includes the well-known problems: WEIGHTED SAT, HITTING SET, SET COVER, and FEATURE SET. Note that these results demonstrate that, under the assumption  $W[1] \neq \text{FPT}$ , the existence of algorithms which perform much better than the exhaustive search algorithms for these problems is unlikely.

The general techniques mentioned above do not apply to the case t=1, and hence, do not imply computational lower bounds for W[1]-hard problems that are not (or are not known to be) W[2]-hard. We develop new techniques to derive computational lower bounds on W[1]-hard problems based on a stronger assumption. Consider the optimization class SNP introduced by Papadimitriou and Yannakakis [18], which consists of all search problems expressible by second-order existential formulas whose first-order part is universal. Impagliazzo and Paturi [15] introduced the notion of SERF-completeness for the class SNP and identified a class of problems which is complete for SNP under SERF-reduction, such that the subexponential time solvability for any of these SERF-complete problems implies that all SNP problems are solvable in subexponential time. The class SNP contains many well-known NP-hard problems, including 3-sat, vertex cover, and independent set, for which extensive efforts have been made in the last three decades to develop subexponential time algorithms with no success [24].

Therefore it seems convincing to assume that not all SNP problems are solvable in subexponential time. This assumption actually implies  $W[1] \neq \text{FPT}[1]$ . Under this stronger assumption, all computational lower bounds we derive for the W[2]-hard problems mentioned above still hold. Moreover, under this stronger assumption, we are now able to derive computational lower bounds for certain W[1]-hard parameterized problems that are not (or not known to be) W[2]-hard: we prove that a group of problems including the following problems cannot be solved in time  $n^{O(k)}$ : WEIGHTED q-SAT (for any fixed  $q \geqslant 2$ ), INDEPENDENT SET, and CLIQUE. Again these lower bounds prove that one cannot expect to have algorithms that perform much better than the exhaustive search algorithms for these problems.

Before we close this section, we point out that besides the area of parameterized complexity theory, the topics related to the above research have also been investigated from many other angles. Exact algorithms that provide computational upper bounds for NP-hard problems have been extensively studied in the last three decades (see survey [24] and its references). For computational lower bounds, Papadimitriou and Yannakakis [19] introduced the class LOGSNP. Problems in this class specifically set (or implicitly force) the parameter value k to be equal to lg n. A group of problems for which no polynomial time algorithms are known, such as TOURNAMENT DOMINATING SET, and VC-DIMENSION, are proved to be LOGSNP-hard, in the sense that if any of these problems can be solved in polynomial time then all problems in LOGSNP are solvable in polynomial time. Thus,

LOGSNP-hardness provides a computational lower bound for these problems. Feige and Kilian [12] studied the computational complexity of finding a clique of size  $k = \lg n$ , and showed that if this problem can be solved in polynomial time then nondeterministic computation can be simulated by deterministic computation in subexponential time. Moreover, they showed that if the k-clique problem, where  $k = \Theta(\lg^c n)$  for some constant c, can be solved in time  $O(n^h)$ , where  $h = k^{1-\epsilon}$  for some  $\epsilon > 0$ , then nondeterministic circuits can be simulated by randomized or non-uniform deterministic circuits of subexponential size (see [12] for the formal definitions). Compared to this result, our results on clique have a weaker assumption, i.e., clique can be solved in time  $n^{O(k)}$ , but our conclusion, i.e., all problems in SNP are subexponential time solvable, is not necessarily weaker.

#### 2. Preliminaries

A parameterized problem Q is a subset of  $\Omega^* \times \mathbb{N}$ , where  $\Omega$  is a fixed alphabet and  $\mathbb{N}$  is the set of all non-negative integers. Therefore, each instance of the parameterized problem Q is a pair (x,k), where the second component, i.e., the non-negative integer k, is called the *parameter*. We say that the parameterized problem Q is *fixed-parameter tractable* [11] if there is a (parameterized) algorithm that decides whether an input (x,k) is a member of Q in time  $O(f(k)|x|^c)$ , where c is a fixed constant and f(k) is a recursive function independent of the input length |x|. Let FPT denote the class of all fixed-parameter tractable problems.

To study the fixed-parameter tractability, the *fpt-reduction* has been introduced [11]: a parameterized problem Q is *fpt-reducible* to a parameterized problem Q' if there is an algorithm M that transforms each instance (x,k) of Q into an instance (x',g(k)) (g is a function of k only) of Q' in time  $O(f(k)|x|^c)$ , where f and g are recursive functions and c is a constant, such that  $(x,k) \in Q$  if and only if  $(x',g(k)) \in Q'$ .

Based on the notion of fpt-reducibility, a hierarchy of parameterized complexity, the W-hierarchy, has been introduced. At the 0th level of the hierarchy lies the class FPT, and at the ith level, the class W[i] for i > 0 (see [11] for the formal definition of the class W[i]). A parameterized problem Q is W[i]-hard if every problem in W[i] is fpt-reducible to Q, and is W[i]-complete if in addition Q is in W[i]. If any W[i]-hard problem is in FPT, then W[i] = FPT, which, to the common belief, is very unlikely [11].

A circuit is a directed acyclic graph. The nodes of in-degree 0 are called *inputs*, and are labeled either by *positive literals*  $x_i$  or by *negative literals*  $\overline{x}_i$ . The nodes of in-degree larger than 0 are called *gates* and are labeled with Boolean operators AND or OR. A special gate of out-degree 0 is designated as the *output* node. A circuit is said to be *monotone* (respectively *antimonotone*) if all its input literals are positive (respectively negative). The *depth* of a circuit is the maximum distance from an input node to the output gate of the circuit. A circuit represents a Boolean function in a natural way. Using the results in [5], every circuit can be re-structured into an equivalent circuit with the same monotonicity and number of input variables, same depth, and such that all inputs are in level 0, all AND and OR gates are organized into alternating levels with edges only going from a level to the next level, and with at most a polynomial increase in the circuit size. Thus, without loss of generality, we will implicitly assume that circuits are in this leveled form. A circuit is a  $\Pi$ -circuit if its output gate is an AND gate, and is a  $\Pi_h$ -circuit if it has depth h and its output gate is an AND gate. We say that

a truth assignment  $\tau$  to the input variables of a circuit C satisfies a gate g in C if  $\tau$  makes the gate g have value 1, and that  $\tau$  satisfies the circuit C if  $\tau$  satisfies the output gate of C. The weight of an assignment  $\tau$  is the number of variables assigned value 1 by  $\tau$ . A propositional formula F is said to be t-normalized where  $t \ge 1$ , if F is the products-of-sums-of-products . . . of literals with t alternations [11]. From a t-normalized formula F with t input variables, we can naturally correspond an equivalent  $\Gamma_t$  circuit  $\Gamma_t$  with  $\Gamma_t$  input variables.

The t-normalized satisfiability problem where  $t \ge 1$ , abbreviated sat[t] henceforth, is defined as follows: given a t-normalized formula F over n variables whose size is m, decide if F is satisfiable. For instance, the 2-normalized satisfiability problem is the same as the satisfiability problem sat. The weighted t-normalized satisfiability problem is defined as follows: given a t-normalized formula F and a positive integer k, decide if F has a satisfying assignment of weight k. From the weighted t-normalized satisfiability problem we can define the weighted monotone t-normalized satisfiability problem (respectively weighted antimonotone t-normalized satisfiability problem) by requiring all the input literals of the formula to be positive (respectively negative) [11]. It is known that for t > 1, the weighted t-normalized satisfiability problem for even t, and the weighted antimonotone t-normalized satisfiability problem for even t, and the weighted antimonotone t-normalized satisfiability problem for odd t, are all W[t]-complete [11]. Also, it is known that the weighted antimonotone 2-satisfiability (each clause contains at most two literals) is W[1]-complete [11].

The above problems can be naturally extended to the circuit model. The WEIGHTED t-NORMALIZED SATISFIABILITY corresponds to the WEIGHTED t-NORMALIZED CIRCUIT SATISFIABILITY, abbreviated  $\mathrm{WCS}[t]$ : given a  $\Pi_t$  circuit C and a positive integer k, decide if C has a satisfying assignment of weight k. Similarly, the WEIGHTED MONOTONE t-NORMALIZED SATISFIABILITY corresponds to the WEIGHTED MONOTONE t-NORMALIZED CIRCUIT SATISFIABILITY, abbreviated MONOTONE  $\mathrm{WCS}[t]$ , and the WEIGHTED ANTIMONOTONE t-NORMALIZED CIRCUIT SATISFIABILITY, abbreviated ANTIMONOTONE  $\mathrm{WCS}[t]$ . Since these problems are natural extensions of the above problems to the circuit model, and since every t-normalized formula can be transformed into an equivalent  $\Pi_t$  circuit with the same number and monotonicity of the input variables, and with no more than a polynomial increase in the size, we have  $\mathrm{WCS}[t]$ , MONOTONE  $\mathrm{WCS}[t]$  for even t, and ANTIMONOTONE  $\mathrm{WCS}[t]$  for odd t, where t > 1, are all W[t]-complete. Moreover, ANTIMONOTONE 2-WCS[2], where each gate at level 1 is required to have fan-in bounded by two, is W[1]-complete.

## 3. Lower bounds for the W-hierarchy

In this section, we give lower bounds on the time complexity of the wcs[t] problem, where  $t \ge 2$ , which is complete for the class W[t] in the W-hierarchy. These lower bounds directly imply lower bounds for many natural parameterized problems including weighted sat, dominating set, hitting set, and set cover. We will break our main theorem into two intermediate theorems. We shall present both theorems because these theorems, per se, are of interest.

**Theorem 3.1.** For any  $t \ge 2$ , if WCS[t] can be solved in time  $n^{o(k)}h(m)$  then SAT[t] can be solved in time  $2^{o(n)}h'(m)$ , where h and h' are two polynomials.

**Proof.** Fix  $t \ge 2$ , and suppose that wcs[t] can be solved in time  $n^{o(k)}h(m)$ . This means that there exists an unbounded non-decreasing function r(k), such that wcs[t] can be decided in time bounded by  $n^{k/r(k)}h(m)$ . We will show that sat[t] can be solved in time  $2^{o(n)}h'(m)$ . Let F be a t-normalized formula over n variables and of size m. We can naturally associate an equivalent  $\Pi_t$  circuit  $C_F$  with n input variables, whose size is polynomial in m, such that F is satisfiable if and only if  $C_F$  is. Note also that  $C_F$  can be constructed from F in time polynomial in m. We will construct an instance  $(C_F',k)$  of wcs[t] such that  $C_F$  is satisfiable if and only if  $C_F'$  has a satisfying assignment of weight k. The construction distinguishes two cases depending on the parity of t.

Case 1. t is even. In this case the gates in level 1 of  $C_F$  are or gates. Suppose that  $x_1, \ldots, x_n$  are the input variables to  $C_F$ . Let  $r = \lfloor \lg n \rfloor$ ,  $b = \lceil n/r \rceil$ , and  $s = 2^r$ . We divide the *n* input variables  $x_1, \ldots, x_n$  into b blocks  $B_1, \ldots, B_b$ , where block  $B_i$  consists of input variables  $x_{(i-1)r+1}, \ldots, x_{ir}$ , for  $i = 1, \dots, b-1$ , and  $B_b$  consists of input variables  $x_{(b-1)r+1}, \dots, x_n$ . Denote by the size of a block the number of variables in the block, and note that  $|B_i|$ , i = 1, ..., b-1, is exactly r, and  $|B_b| = r' \le r$ . We form the b blocks  $B'_1, \ldots, B'_b$ , each block  $B'_i, i = 1, \ldots, b-1$ , consists of exactly s new variables  $z_i^1, \dots, z_i^s$ , and  $B_b'$  consists of  $s' = 2^{p'}$  variables  $z_b^1, \dots, z_b^{s'}$ . Blocks  $B_1', \dots, B_b'$  will be used to decode the input variables in blocks  $B_1, \ldots, B_b$  in the following manner. The input variable  $z_i^j$  in block  $B_i^j$ ,  $i=1,\ldots,b, j=1,\ldots,|B_i'|$ , will be used to indicate an assignment to the input variables in  $B_i$  such that if  $z_i^j = 1$ , then the variables in  $B_i$  will be assigned the bits in the binary representation of the number j. Since for every i = 1, ..., b, we have  $|B_i'| = 2^{|B_i|}$ , it is clear that there is a bijection between the assignments to the variables in  $B'_i$  of weight 1 (with respect to the variables in  $B'_i$ ) and the possible binary configurations of the input variables in  $B_i$ , given by the above description. It follows that there is a bijection between all possible truth assignments to the input variables in  $C_F$ , and all truth assignments to the input variables in  $B'_i$ , i = 1, ..., b, in which exactly one variable in each block  $B'_i$  is assigned the value 1. The circuit  $C'_F$  is now constructed from  $C_F$  by removing the input literals of  $C_F$ and adding the new input variables in the blocks  $B'_1, \ldots, B'_b$ . The new input variables are connected to the or gates at level 1 in  $C_F$  as follows. Suppose that the positive (respectively negative) literal lcorresponding to variable  $x_q (q \in \{1, ..., n\})$  is connected to an or gate g at level 1 in  $C_F$ , and suppose that  $x_q$  is the pth variable in block  $B_i$ ,  $i \in \{1, ..., b\}$ . Then all input variables  $z_i^j$ ,  $j = 1, ..., |B_i'|$ , in block  $B'_i$  such that the pth bit in the binary representation of j is 1 (respectively 0), are connected to gate g. We also add some "enforcement" circuitry to  $C_F'$  to ensure that at least one new variable  $z_i^J$ in every block  $B'_i$ , i = 1, ..., b,  $j = 1, ..., |B'_i|$ , is set to 1. This can be achieved as follows. For every block  $B'_i$  where i = 1, ..., b: add an or gate  $g'_i$ , connect every variable in  $B'_i$  to  $g'_i$ , and connect  $g'_i$  to the output gate of  $C_F$ . This completes the construction of  $C_F'$ . Clearly,  $C_F'$  has size h''(m) for some polynomial h'', and can be constructed from  $C_F$  in time polynomial in m. Moreover, since  $t \ge 2$ , and the enforcement circuitry requires no more than depth 2 to be implemented,  $C'_F$  is also a  $\Pi_t$ circuit. It is not difficult to verify that F is satisfiable if and only if  $C_F$  is satisfiable, if and only if  $C'_F$  has a satisfying assignment of weight b. Note that any satisfying assignment to  $C'_F$  of weight b must satisfy exactly one input variable in each block  $B'_i$ , i = 1, ..., b. The reason being that the number of blocks is exactly b, and the enforcement circuitry guarantees that at least one variable in every block is set to 1 in any satisfying assignment. Since  $C'_F$  is a  $\Pi_t$  circuit, it follows that  $(C'_F, b)$ is an instance of wcs[t]. The number of input variables N to  $C_F'$  is bounded by  $b \cdot s = \lceil n/r \rceil \cdot 2^{\lfloor \lg n \rfloor} \le 1$  $n^2$ . The parameter k in the instance  $(C'_F, b)$  is equal to  $b = \lceil n/r \rceil = \lceil n/\lfloor \lg n \rfloor \rceil \leqslant 2n/\lg n$  when n is

large enough (if n is bounded by a constant the problem can be solved in constant time). By the hypothesis, we can decide  $(C_F', k = b)$  in time bounded by  $N^{k/r(k)}h(h''(m)) \leq n^{4n/r'(n)\lg n}h(h''(m))$ , where  $r'(n) = r(\lceil n/\lfloor \lg n \rfloor \rceil)$  is an unbounded non-decreasing function of n. Since  $n^{4n/r'(n)\lg n} \in 2^{o(n)}$ , and since the construction of  $C_F$  and  $C_F'$  from F can be done in polynomial time in m, it follows that deciding whether  $C_F'$  has a truth assignment of weight k, and hence whether F is satisfiable, can be done in time  $2^{o(n)}h'(m)$ , where h' is a polynomial.

Case 2. t is odd. Since t > 1,  $t \ge 3$ , and the gates at level 1 in  $C_F$  are AND gates. The decomposition of the *n* input variables in  $C_F$  into *b* blocks  $B_i$ , i = 1, ..., b, and the construction of the blocks  $B'_i$ , proceed exactly as in Case 1. The enforcement circuitry which ensures that exactly one variable  $z_i^J$  in block  $B_i^J$  is set to true also remains the same. Since  $t \ge 3$ , this enforcement circuitry can still be implemented without affecting the level structure of  $C_F$  (this enforcement circuitry needs two levels to be implemented: AND-of-oR's). The only part in the construction of  $C_F'$  that is different from the above construction, and is a bit trickier, is how to connect the new variables and their negations to the AND gates at level 1 in  $C_F$ . Let g be a level-1 AND gate in  $C_F$ . Let  $S_i$ , i = 1, ..., b be the set of literals connected to g whose variables are in block  $B_i$ (some  $S_i$ 's may be empty). If g is satisfied, then all literals in  $S_i$ , i = 1, ..., b, must receive the value 1. Let  $S'_i$ , i = 1, ..., b, be the set of variables  $z_i^j$  in block  $B'_i$ , such that if the input variables in  $B_i$  are assigned the corresponding bits in the binary representation of j (i.e., the pth input variable in  $B_i$  is assigned the pth bit in the binary representation of j), all literals in  $S_i$  receive the value 1. Let  $S_i'' = \{z_i^j \in B_i' \mid z_i^j \notin S_i'\}$ , and  $\overline{S_i''} = \{\overline{z}_i^j \mid z_i^j \in S_i''\}$ , where  $\overline{z}_i^j$  is the negation of the input variable  $z_i^j$ . For every level-1 gate g in  $C_F$ , and for every i = 1, ..., b, we remove the input literals from  $S_i$  to gate g in  $C_F$ , and connect the literals in  $S_i''$  to g. Let  $C_F'$  be the resulting circuit. Then clearly  $C'_F$  is a  $\Pi_t$  circuit. We argue next that the bijection described above between truth assignments to the input variables in  $C_F$  and those to the input variables in  $C_F'$  that assign exactly one variable in every block  $B'_i$ , i = 1, ..., b, the value 1, associates with every truth assignment  $\tau$  to  $C_F$  a truth assignment  $\tau'$  to  $C_F'$  such that  $\tau$  satisfies  $C_F$  if and only if  $\tau'$  satisfies  $C_F'$ . The only difference between  $C_F$  and  $C_F'$  is the input gates and their connections to level-1 gates. So it suffices to argue that the truth value of a level-1 gate g in  $C_F$  with respect to  $\tau$  is the same as its truth value in  $C'_F$  with respect to  $\tau'$ . Now gate g receives the value 1 by  $\tau$  in  $C_F$ if and only if all literals in  $S_i$ , i = 1, ..., b, are set to 1 by  $\tau$ . This is true if and only if one of the variables in  $S'_i$  is set to 1 by  $\tau'$  in  $C'_F$ . Since  $\tau'$  assigns exactly one variable in every block the value 1, the latter condition is true if and only if all the variables in  $S_i''$  receive the value 0 by  $\tau'$  in  $C_F'$ , which is in turn true if and only if all literals in  $\overline{S_i''}$  receive the value 1 by  $\tau'$  in  $C_F'$ . This is true if and only if g receives the value 1 by  $\tau'$  in  $C_F'$ . The proof from this point on proceeds in exactly the same fashion as in Case 1.

We conclude that SAT[t] can be decided in  $2^{o(n)}h'(m)$  time. This completes the proof.  $\Box$ 

**Theorem 3.2.** For any  $t \ge 2$ , if SAT[t] can be solved in time  $2^{o(n)}h(m)$  for some polynomial h, then W[t-1] = FPT.

**Proof.** If t = 2, the theorem states that if sat can be solved in time  $2^{o(n)}h(m)$  then W[1]=FPT. This result was established by Cai and Juedes [3]. Thus, we can assume that  $t \ge 3$ . The proof builds on

the techniques employed in [3]. Suppose that SAT[t] is solvable in time  $2^{O(n)}h(m)$ . Then there exists an unbounded non-decreasing function s(n) such that SAT[t] can be solved in time bounded by  $2^{n/s(n)}h(m)$ . We distinguish two cases based on the parity of t.

Case 1. t is odd. We consider the MONOTONE WCS[t-1] problem. Since this problem is complete for W[t-1], it suffices to show that this problem can be solved in time f(k)h'(m) where f is a function independent of the circuit size m, and h' is a polynomial. Let (C,k) be an instance of MONOTONE wcs[t-1], where C has n input variables and size m. Since t-1 is even, the gates at level 1 in C are or gates. Let  $x_1, \ldots, x_n$  be the input variables to C. We will construct a circuit C' from C with  $k \lceil \lg n \rceil$  input variables, such that C has a weight k assignment if and only if C' is satisfiable. The input variables in C' are divided into k blocks  $B_1, \ldots, B_k$ , where block  $B_i$ , i = 1, ..., k, consists of  $r = \lceil \lg n \rceil$  input variables  $z_i^1, ..., z_i^r$ . Also, for every input variable  $z_i^j, i \in \{1, ..., k\}, j \in \{1, ..., r\}$ , we associate the input literal  $\bar{z}_i^j$  to denote its negation. Informally speaking, each block  $B_i$  will contain the encoding of an input variable whose value is 1 in a weightk assignment to C. We show how to connect the new input variables and their negations to the level-1 or gates in C. Let g be a level-1 or gate in C. Let  $x_p$  be an input to g, and let  $b_1b_2...b_r$ be the binary representation of the number p (if there are fewer than r bits in the binary representation of p, we pad the binary representation of p with the appropriate number of 0's on the left to make it consist of exactly r bits). We introduce k new AND gates  $g_p^1, \ldots, g_p^k$ . Each gate  $g_p^i$ ,  $i = 1, \ldots, k$ , has exactly r inputs, and its input comes only from input variables in block  $B_i$  and their negations. Informally speaking, each gate  $g_p^i$  will be satisfied if and only if block  $B_i$  contains the binary representation of p, and hence, encodes  $x_p$ . The input to gate  $g_p^i$  is determined as follows. For j = 1, ..., r, if  $b_j = 0$ , then connect  $\overline{z}_i^j$  to  $g_p^i$ , and if  $b_j = 1$ , then connect  $z_i^j$  to  $g_p^i$ . Now replace the connection from  $x_p$  to g by the connections from all gates  $g_p^i$ , i = 1, ..., k, to g. We repeat this process for every level-1 gate g in C and every input variable in  $\{x_1, \ldots, x_n\}$  to g. Clearly, this construction only adds a single level to the circuit C consisting of AND gates, and hence, the resulting circuit is a  $\Pi_t$  circuit. We also add enforcement circuitry to ensure that the k blocks  $B_i$ , i = 1, ..., k, encode distinct k variables. This can be simply achieved by adding a circuitry that performs a bitwise xor operation to the corresponding variables in every two blocks, which can be done by adding a 3-level AND-of-or-of-AND subcircuits to every two blocks (note that the last AND can be merged with the output AND gate of the circuit if t = 3). Clearly, the resulting circuit is still a  $\Pi_t$  circuit. Moreover, the size of C is only increased by a polynomial factor in its original size. Let  $C'_F$  be the circuit resulting from this construction. From the above discussion we know that C' is a  $\Pi_t$  circuit of size h'(m) for some polynomial h'. Since the k input blocks in C' basically encode the k input variables in C with value 1 in a weight-k assignment to C, it is not difficult to verify that C has a weight-k truth assignment if and only if C' is satisfiable. Now C' is an instance of SAT[t] with kr input variables. It follows that we can decide if C' is satisfiable in time bounded by  $T(n) = 2^{kr/s(kr)}h(h'(m)) = 2^{k\lceil \lg n \rceil/s(k\lceil \lg n \rceil)}h(h'(m)) \le 2^{k(\lg n + 1)/s'(n)}h''(m)$ , for some unbounded non-decreasing function s'(n), and some polynomial h''. Thus,  $T(n) \in 2^{o(\lg n)k} h''(m)$ , and wcs[t-1] is solvable in time  $2^{o(\lg n)k}h''(m)$  for some polynomial h''. It follows that wcs[t-1] is fixed parameter tractable (see Lemma 2.1 in [3] for a proof of this fact), and hence, W[t-1] = FPT.

Case 2. t is even, and hence  $t-1 \ge 3$  is odd. We consider the ANTIMONOTONE wcs[t-1] problem, which is complete for W[t-1]. The proof proceeds in a very similar fashion to the proof of

Case 1 above. Let (C,k) be an instance of ANTIMONOTONE WCS[t-1], and note that the gates at level 1 in C are AND gates. Let  $\bar{x}_1, \dots, \bar{x}_n$  be the input literals to C, and let r and  $B_i$ ,  $i = 1, \dots, k$ , be as defined above. Again, block  $B_i$  will be used to encode the indices of the input variables in C that are set to 1 in a weight-k assignment to C. Let g be a gate at level-1 in C, and suppose that  $\bar{x}_p$ , where  $p \in \{1, ..., n\}$ , is connected to g. Now  $\bar{x}_p$  is 1 if and only if  $x_p$  is 0, if and only if none of the blocks  $B_i$ , i = 1, ..., k contains the binary representation of p. Thus, in C' we will connect the new input variables to g as follows. We introduce k new or gates  $g_p^1, \ldots, g_p^k$ . Each gate  $g_p^i$ , i = 1, ..., k, has exactly r inputs, and its input comes only from input variables in block  $B_i$  and their negations. Informally speaking, each gate  $g_p^i$  will be satisfied if and only if block  $B_i$  does not contain the binary representation of p, and hence, does not encode  $x_p$ . Suppose the binary representation of p is  $b_1b_2...b_r$ . For i=1,...,k, the input to  $g_p^i$  is determined as follows. For  $j=1,\ldots,r$ , if  $b_j=0$ , then connect  $z_i^j$  to  $g_p^i$ , and if  $b_j=1$ , then connect  $\overline{z}_i^j$  to  $g_p^i$ . Now replace the connection from  $\overline{x}_p$  to g by the connections from all gates  $g_p^i$ ,  $i=1,\ldots,k$  to g, and repeat that for every level-1 gate in C and every original input literal to that gate. This adds an or-level to C, thus increasing the number of levels in C by 1, and resulting in a  $\Pi_t$ circuit. Now we can add the enforcement circuitry to ensure that all k blocks encode k distinct input variables. This can be simply achieved by adding a circuitry that performs a bitwise xor operation to the corresponding variables in every two blocks. The resulting circuitry that tests that no two blocks are the same can be implemented by an or-of-AND-of-AND-of-OR subcircuit (the last AND gate can be identified with the output gate of C if t = 4). Since  $t \ge 4$ , the resulting circuit C' is a  $\Pi_t$  circuit whose size is not more than a polynomial in the size of C. The proof from this point on proceeds in exactly the same fashion as in Case 1 above.

It follows that W[t-1] = FPT. This completes the proof.  $\square$ 

**Theorem 3.3.** For any  $t \ge 2$ , if wcs[t] is solvable in time  $n^{o(k)}p(m)$  for some polynomial p, then W[t-1] = FPT.

**Proof.** The proof follows immediately from Theorem 3.1 and Theorem 3.2.

**Theorem 3.4.** If MONOTONE WCS[2] is solvable in time  $n^{o(k)}p(m)$  for some polynomial p, then W[1] = FPT.

**Proof.** Observe that when t is even, the circuit  $C'_F$  in the proof of Theorem 3.1 is monotone. Combining this observation with Theorem 3.1 and Theorem 3.2, the result follows.  $\Box$ 

We consider the following W[2]-complete problems [9,11].

#### WEIGHTED SAT

Given a formula F in CNF over N variables whose size is m, decide if there exists a weight-k satisfying assignment for F.

#### RED/BLUE DOMINATING SET

Given a bipartite graph G = (V, E), where  $V = V_{\text{red}} \cup V_{\text{blue}}$  with  $|V_{\text{red}}| = N$ , and a positive integer k, decide if there exists a subset  $V' \subseteq V_{\text{red}}$  of cardinality k such that V' dominates  $V_{\text{blue}}$  (i.e., every vertex in  $V_{\text{blue}}$  is adjacent to some vertex in V').

#### HITTING SET

Given a set  $U = \{u_1, \dots, u_N\}$ , a collection  $S = \{S_1, \dots, S_m\}$ , where  $S_i \subseteq U$ ,  $i = 1, \dots, m$ , and a positive integer k, decide if there exists a subset  $H \subseteq U$  with cardinality k, such that  $H \cap S_i \neq \emptyset$  for  $i = 1, \dots, m$ .

#### SET COVER

Given a set S, a collection  $\mathcal{F} = \{C_1, \dots, C_N\}$  of subsets of S such that  $\bigcup_{i=1}^N C_i = S$ , and a positive integer k, decide if there is a subcollection C of F with cardinality k such that  $\bigcup_{C_i \in C} C_i = S$ .

#### FEATURE SET

Given a set of m examples  $X = \{x^{(1)}, \dots, x^{(m)}\}$ , where  $x^{(i)} = (x_1^{(i)}, \dots, x_N^{(i)}, t^{(i)}) \in \{0, 1\}^{N+1}$ , and an integer k > 0, decide if there exists a *feature set*  $S \subseteq \{1, \dots, N\}$  of cardinality k, such that for all pairs of examples  $i \neq j$ , if  $t^{(i)} \neq t^{(j)}$ , then there exists  $l \in S$  with  $x_l^{(i)} \neq x_l^{(j)}$ .

**Theorem 3.5.** If any of the WEIGHTED SAT, RED/BLUE DOMINATING SET, HITTING SET, SET COVER, or FEATURE SET problems can be solved in time  $N^{o(k)}p(|I|)$ , then W[1] = FPT, where |I| is the input size, N is the size of the universal set from which the k-element solution is to be chosen, and p is a polynomial.

**Proof.** The result for WEIGHTED SAT follows directly from Theorem 3.1 with t = 2, by observing that weighted sat is exactly weighted 2-normalized satisfiability, which corresponds to wcs[2]. We show the result for RED/BLUE DOMINATING SET next. By Theorem 3.4, it suffices to show that if RED/BLUE DOMINATING SET is solvable in time  $N^{o(k)}p(|G|)$ , where G is the input graph and  $N=|V_{\text{red}}|$ , then MONOTONE WCS[2] is solvable in time  $n^{o(k)}q(m)$  where n is the number of input variables, and m is the circuit size. Let (C,k) be an instance of MONOTONE WCS[2] where C has n input variable  $\{x_1,\ldots,x_n\}$ , and size m. Observe that C has a weight-k satisfying assignment if an only if there exists a weight-k assignment to the input variables in C such that all level-1 gates in C are satisfied. The last statement is true if and only if each level-1 gate in C has at least one variable of value 1 that is connected to it (since all level-1 gates in C are or gates). Let  $g_1, \ldots, g_r$  be the level-1 gates in C. We construct the bipartite graph  $G = (V_{\text{red}} \cup V_{\text{blue}}, E)$  as follows. For each input variable  $x_i$ ,  $i=1,\ldots,n$ , we associate a vertex  $x_i \in V_{\text{red}}$ . For each gate  $g_i, j=1,\ldots,r$ , we associate a vertex  $g_i$ in  $V_{\text{blue}}$ . Now a vertex  $x_i$ ,  $i=1,\ldots,n$ , is connected to a vertex  $g_j$ ,  $j=1,\ldots,r$  in G if and only if  $x_i$  is an input to gate  $g_i$  in C. From the above discussion, it is easy to see that C has a weight-k satisfying assignment if and only if G has a subset  $V' \subseteq V_{\text{red}}$  with |V'| = k, such that V' dominates  $V_{\text{blue}}$ . Thus, by solving the instance (G, k) of the RED/BLUE DOMINATING SET we can solve the instance (C,k) of MONOTONE WCS[2]. Since the construction of G from C can be done in time polynomial in m, it follows that if RED/BLUE DOMINATING SET can be solved in time  $N^{o(k)}p(|G|)$ , then MONOTONE wcs[2] can be solved in time  $n^{o(k)}q(m)$  for some polynomial q (note that N=n and |G| is polynomial

Now we show the result for the HITTING SET problem. It suffices to show that if HITTING SET can be solved in time  $N^{o(k)}p(|I|)$  then RED/BLUE DOMINATING SET can be solved in time  $n^{o(k)}q(|I'|)$ , where N is the number of elements in the universe U, |I| the size of the input instance I of HITTING SET, n the number of vertices in  $V_{\text{red}}$ , |I'| the size of the input instance I' of RED/BLUE DOMINATING SET, and p and q are two polynomials. Let  $I' = (G = ((V_{\text{red}} \cup V_{\text{blue}}), E), k)$ , be an instance of RED/BLUE DOMINATING SET, where  $|V_{\text{red}}| = n$ , we construct an instance  $I = ((U = \{u_1, \ldots, u_N\}, S = \{S_1, \ldots, S_m\}), k)$ 

of HITTING SET as follows. The elements in U are the vertices in  $V_{\text{red}}$ . For every vertex  $v_j$ , j = 1, ..., m, in  $V_{\text{blue}}$  we associate a set  $S_j$  consisting of all neighbors of  $v_j$ . It is easy to see that |I| is polynomial in |I'|, and that the construction can be carried out in time polynomial in |I'|. It can be easily verified that S has a hitting set of size k if and only G has a subset  $V' \subseteq V_{\text{red}}$  of k vertices that dominates  $V_{\text{blue}}$ . Noting that N = n, the statement follows.

To show that the same result holds for set cover we reduce HITTING SET to SET COVER. Let  $I = ((U = \{u_1, \dots, u_N\}, S = \{S_1, \dots, S_m\}), k)$  be an instance of HITTING SET, we construct an instance  $I' = ((S', \mathcal{F}), k)$  of SET COVER as follows:  $S' = \{S_1, \dots, S_m\}$  and  $\mathcal{F} = \{C_1, \dots, C_N\}$  where  $C_i = \{S_j \in S' \mid u_i \in S_j\}$ . It is not difficult to see that S has a hitting set of size k if and only if S' has a set cover of size k. Noting that I' has size polynomial in I, and the construction of I' from I takes polynomial time in |I|, the statement follows.

Finally, the result for the feature set problem follows from the reduction from set cover to feature set given in [23].  $\Box$ 

## 4. Lower bounds for some W[1]-hard problems

In this section, we prove that the existence of  $n^{o(k)}$  time algorithms for many parameterized problems like independent set, clique, and weighted q-sat implies that all problems in the class SNP can be solved in subexponential time. The class SNP [18] contains many well-known NP-hard problems including q-sat, q-colorability, q-set cover, vertex cover, and independent set [15]. In particular, 3-sat is a special case of q-sat, and the existence of  $n^{o(k)}$  time algorithms for the above-mentioned parameterized problems would imply that 3-sat is solvable in  $2^{o(n)}$ . It is commonly believed that it is unlikely that all problems in SNP are solvable in subexponential time. We start with the following theorem, which is due to Nemhauser and Trotter [16]. This version of the theorem appears in [7].

**Theorem 4.1** ([7]). Given an instance (G,k) of VERTEX COVER, there is a polynomial time algorithm which either reports that G does not have a vertex cover of size k, or produces a subgraph G' of G with at most 2k' vertices, where  $k' \leq k$ , such that G has a vertex cover of size k if and only if G' has a vertex cover of size k'.

**Theorem 4.2.** If the parameterized INDEPENDENT SET problem can be solved in time  $n^{o(k)}$ , where n is the number of vertices in the graph, then all problems in SNP can be solved in subexponential time.

**Proof.** Assume that there is an algorithm A which determines whether there exists an independent set of size k in a graph G with n vertices in  $O(n^{f(k)})$  steps, where  $f(k) \le k/r(k)$  for some unbounded nondecreasing function r(k). We will show that the VERTEX COVER problem can be solved in time  $2^{o(k)}p(n)$ , for some polynomial p (note that this will imply that VERTEX VOVER can be solved in time  $2^{o(n)}$ , which is subexponential). Since the VERTEX COVER problem is complete for the class SNP under SERF reductions, this will show that all problems in SNP can be solved in subexponential time [15].

Let (G = (V, E), k) be an instance of VERTEX COVER. By Theorem 4.1, we can assume that G has at most  $n \le 2k$  vertices. We partition the n vertices of G into  $k' = \lceil \frac{n}{\lceil \log k \rceil} \rceil$  blocks  $B_1, B_2, \ldots, B_{k'}$  each of size bounded by  $\lceil \lg k \rceil$ . Observe that G has a vertex cover of size k if and only if there exists a way

to partition k into  $k_1, \ldots, k_{k'}$  (i.e.,  $k = k_1 + k_2 + \cdots + k_{k'}$ ), and there are subsets  $V_i' \subseteq B_i$ ,  $i = 1, \ldots, k'$  with  $|V_i'| = k_i$ , such that  $\bigcup_{i=1}^{k'} V_i'$  is a vertex cover for G. Since  $|B_i| \le \lceil \lg k \rceil$ , this approach converts the single question "does G have a vertex cover of size k?" into at most

$$\lceil \lg k \rceil^{k'} \leqslant \lceil \lg k \rceil^{\lceil \frac{2k}{\lceil \lg k \rceil} \rceil}$$

$$= 2^{\lceil \frac{2k}{\lceil \lg k \rceil} \rceil \cdot \lg (\lceil \lg k \rceil)}$$

$$= 2^{o(k)}$$

more restrictive questions of the type "does G have a vertex cover V' of size  $k = k_1 + k_2 + \cdots + k_{k'}$  with  $|B_i \cap V'| = k_i$ ?". Hence, we can determine whether G has a vertex cover of size k by answering at most  $2^{o(k)}$  questions individually.

To answer each of the  $2^{o(k)}$  questions, we use the algorithm A for INDEPENDENT SET. Given G, k, and  $k_1, \ldots, k_{k'}$  such that  $k = k_1 + k_2 + \cdots + k_{k'}$ , we construct a graph  $G^* = (V^*, E^*)$  as follows. For each block of vertices  $B_i$  in G, and for each subset  $B_{ij} \subseteq B_i$  with  $|B_{ij}| = k_i$ , add a vertex  $v_{ij}$  to  $V^*$  if  $B_{ij}$  is a vertex cover of  $G(B_i)$  (the subgraph of G induced by G). Add edges to G0 so that the collection of the vertices G1 associated with block G2, G3 if G4 if G5 if G6 as a vertex cover for  $G(B_i \cup B_k)$ . This completes the construction of G4. To determine if G4 has a vertex cover of size G6 with the properties mentioned above, it suffices to use algorithm G1 to determine if G4 has an independent set of size G2. We prove the correctness of this claim.

Assume that  $G^*$  has an independent set I of size k'. Since  $G^*$  has k' disjoint cliques, exactly one vertex from each set  $V_i^* = \{v_{ij} \mid v_{ij} \in V^*\}$  is in I. Let  $V' = \bigcup_{v_{ij} \in I} B_{ij}$ . Since  $|B_{ij}| = k_i$ , and at most one  $B_{ij}$  is included in V', it follows that  $|V' \cap B_i| = k_i$ , and |V'| = k. Thus, it suffices to prove that V' is a vertex cover of G. Let  $(u, v) \in E$ , and let  $u \in B_i$  and  $v \in B_k$ . If i = k, then it must be the case that either u or  $v \in V'$ . To see this, note that there exists a  $v_{ij} \in I \subseteq V^*$ , which means that  $B_{ij} \subseteq V'$  by the definition of V'. Since  $v_{ij} \in V^*$ ,  $B_{ij}$  is a vertex cover of  $G(B_i)$ , and either u or v must be in  $B_{ij} \subseteq V'$ . Suppose now that  $i \neq k$ , and let  $v_{ij}$ ,  $v_{kl}$  be the two vertices in  $V_i^*$  and  $V_j^*$ , respectively, that are in I. Then it must be the case that  $u \in B_{ij}$  or  $v \in B_{kl}$ , otherwise  $B_{ij} \cup B_{kl}$  is not a vertex cover of  $G(B_i \cup B_k)$ , which would imply that there is an edge between  $v_{ij}$  and  $v_{kl}$  in  $G^*$ , contradicting the fact that I is an independent set of  $G^*$ . It follows that either u or v is in V'. This shows that V' is a vertex cover of G. To prove the converse, assume that G has a vertex cover V' of size  $k = k_1 + k_2 + \cdots + k_{k'}$  with  $|B_i \cap V'| = k_i$ . Let  $I = \{v_{ij} \mid B_{ij} = B_i \cap V'\}$ . It is clear that  $I \subseteq V^*$  and |I| = k', since for each i, i has i vertices and it is a vertex cover of i is an independent set in i because for each i, i has i vertices and it is a vertex cover of i is an independent set in i because for each i, i has i vertices and it is a vertex cover of i is an independent set in i because for each i has i vertices and it is a vertex cover of i is an independent set in i because for each i has i vertices and it is a vertex cover of i is an independent set in i because for each i has i vertices and it is a vertex cover of i is an independent set in i

Therefore, we can use algorithm A to determine whether G has a vertex cover V' of size  $k = k_1 + k_2 + \cdots + k_{k'}$ , by checking whether  $G^*$  has an independent set I of size k'. The graph  $G^*$  has a most  $2k \cdot k' \leq 4k^2$  vertices because  $|B_i| \leq \lceil \lg k \rceil$ , and there are at most  $\binom{\lceil \lg k \rceil}{k_i} \leq 2^{\lceil \lg k \rceil} \leq 2^{\lg k+1} \leq 2k$ , possible subsets  $B_{ij}$  of size  $k_i$ . Therefore, the time taken by applying the algorithm A to the instance  $(G^*, k')$  is of the order

$$(4k^{2})^{f(k')} = 2^{f(k') \cdot \lg(4k^{2})}$$

$$\leq 2^{k'/r(k')(2+2\lg k)}$$

$$\leq 2^{(2k/(s(k)\lg k)+1/s(k))(2+2\lg k)}$$

$$= 2^{o(k)}$$

where s(k) = r(k') is an unbounded non-decreasing function of k. The inequality before the last uses the fact that  $k' = \left\lceil \frac{n}{\lceil \log k \rceil} \right\rceil \le 2k/\lg k + 1$ .

Noting that the time needed to construct  $G^*$  is  $O(k^4)$ , and that applying Theorem 4.1 takes polynomial time in n, it follows that the VERTEX COVER problem can be solved in time  $q(n) + 2^{o(k)} \cdot 2^{o(k)} \cdot k^4 \le 2^{o(k)} p(n)$ , where p and q are polynomials. This completes the proof.  $\square$ 

Consider the following parameterized problems.

#### WEIGHTED q-SAT:

Given a CNF formula F on n variables with at most q literals per clause, where  $q \ge 2$ , determine if there is a weight-k assignment to the variables that satisfies F.

#### DOMINATING CLIQUE:

Given a graph G and a positive integer k, decide if G has a dominating set of size k that is also a clique.

#### GRAPH k-CUT:

Given a graph G = (V, E), an edge weighting function  $w : E \longrightarrow \mathbb{N}$ , and positive integers k and b, where k is the parameter, determine if there is a set of edges  $C \subseteq E$  with  $\sum_{e \in C} w(e) \le b$  such that the graph formed from G by removing the edges in C has at least k connected components.

Note that all the above problems are W[1]-hard [10,11]. The Graph k-cut problem has been widely studied, and it is known to have a polynomial time algorithm for every fixed k. In particular, Goldschmidt and Hochbaum [13,14] proved that Graph k-cut can be solved in time  $O(n^{k^2})$  for every fixed k. Downey et al. [10] recently showed that Graph k-cut is W[1]-hard. Note also that the universal set in the weighted q-sat problem is the set of input variables in the formula, and the universal set in each of the dominating set, dominating clique, and Graph k-cut problems, is the set of vertices in the graph.

**Theorem 4.3.** If clique, weighted q-sat (for any  $q \ge 2$ ), dominating set, dominating clique, or graph k-cut can be solved in time  $n^{o(k)}$  then all problems in SNP can be solved in subexponential time, where n is the size of the universal set from which the k elements are to be selected.

**Proof.** It is well-known that a graph G with n vertices has a clique of size k if and only if the complement of G,  $\overline{G}$ , has an independent set of size k. Hence, if CLIQUE has a  $n^{o(k)}$  time algorithm, then parameterized independent set has a  $n^{o(k)}$  time algorithm. Applying Theorem 4.2 completes the proof. Similarly, the reduction from independent set to weighted 2-sat is straightforward. Given a graph G with n vertices and an integer k, we can convert the instance (G,k) of independent set

to an instance (F,k) of WEIGHTED 2-SAT as follows. For each vertex  $v_i$  in G, create a Boolean variable  $x_i$ . For each edge  $(v_i,v_j) \in E$ , create the clause  $(\neg x_i \lor \neg x_j)$ . The formula F created by taking the conjunction of all these clauses, is an instance of WEIGHTED 2-SAT with at most  $O(n^2)$  clauses. Moreover, F has a weight-k satisfying assignment if and only if G has an independent set of size K. Hence, if WEIGHTED 2-SAT has an  $N^{O(k)}$  time algorithm, then INDEPENDENT SET can be solved in  $N^{O(k)}$  time.

To prove the statement for the dominating set problem, we use the polynomial time reduction given in [11] from WEIGHTED SAT to DOMINATING SET. Cleary any reduction from WEIGHTED SAT to DOMINATING SET is also a reduction from WEIGHTED q-SAT to DOMINATING SET (note that in the WEIGHTED q-SAT problem the formula is assumed to be in the CNF form) since the WEIGHTED q-SAT problem is a restriction of the WEIGHTED SAT problem to instances in which every clause contains at most q literals. The reduction in [11] constructs from a CNF formula F of n variables and m clauses a graph G of  $O(n^3 + m)$  edges, such that F has a satisfying assignment of weight k if and only if G has a dominating set of 2k vertices. Observing that the number of clauses m in an instance of WEIGHTED q-SAT is always  $O(n^q)$ , which is polynomial in n, it follows that if the DOMINATING SET problem is solvable in  $n^{O(k)}$  time then the WEIGHTED q-SAT problem is also solvable in  $n^{O(k)}$  time.

To show the statement for the dominating clique problem, it suffices to prove that if dominating clique can be solved in time  $n^{o(k)}$ , then dominating set can be solved in time  $n^{o(k)}$ . Assume that dominating clique can be solved in time  $n^{o(k)}$ . Given a graph G and an integer K, we can determine whether G has a dominating set of size K as follows. Construct a graph G' = (V', E'), where  $V' = \{v, v' \mid v \in V\}$  and E' is defined as follows. First, add all edges of the form (u, v) to E'. Hence, G' contains a clique of size K. Furthermore, add all edges of the form K0, where K1 is defined as follows. First, add all edges of the form K2, where K3 is defined as follows. First, add all edges of the form K4 is a dominating clique of size K5 if and only if K6 has a dominating set of size K6.

Suppose that G has a dominating set D of size k. We show that D is a dominating clique of size k in G'. First D is clearly a clique in G', by construction. Also, D dominates all of the vertices in V because these vertices form a clique. Moreover, D dominates all of the other vertices in V'. To see why the preceding statement is true, let  $v' \in V' - V$ . Since D is a dominating set for G, the vertex V is dominated by D in G, and either an adjacent vertex U to V in G is in D, or V itself is in D. In the former case there exists an edge from  $U \in D$  to U' in U' and in the latter case there exists an edge from  $U \in D$  to U' in U' and U' is dominated by U' in U' and U' is a dominating clique in U'.

Conversely, if G' has a dominating clique D of size k, then at most one of the vertices in D can come from V' - V since the vertices in V' form an independent set. Furthermore, if D contains exactly one vertex  $v' \in V' - V$ , then since v' cannot dominate any vertex in V' - V except v', and since all the vertices in V that are dominated by v' in G' are also dominated by v which also dominates v', the vertex v' can be replaced by v and we still get a dominating set of size k for G' consisting only of vertices in V. Thus, we can assume without loss of generality, that D contains only vertices from V. Now to see that D is a dominating set in G, let v be a vertex in V. The vertex v' in G' must be dominated by some vertex  $u \in D$ , and hence, there is an edge from  $u \in D$  to v' in G'. From the construction of G', either u = v, or u is adjacent to v in G. In both cases, the vertex v is dominated by u in G, and consequently, D is a dominating set in G.

The above polynomial time reduction from the DOMINATING SET problem to the DOMINATING CLIQUE problem shows that we can determine whether a graph G of size n has a dominating set

of size k by determining whether a graph G' of size 2n has a dominating clique of size k. Thus, if DOMINATING CLIQUE can be solved in time  $n^{o(k)}$  then we can solve DOMINATING SET in time  $n^{o(k)}$ .

We finally show the statement for the Graph k-cut problem. It suffices to employ the results in [10]. In [10], it was shown that there exists a polynomial time many-one reduction from clique to graph k-cut that transforms an instance (G,k) of clique to an instance (G',k+1,b,w) of graph k-cut, where G' has  $(n+2) \cdot n^4$  vertices, and k+1 is the parameter. As shown in [10], G has a clique of size k if and only if b edges can be deleted from G' to create k+1 connected components in the graph. Now, if graph k-cut can be solved in time  $n^{o(k)}$ , then we can solve the clique problem by performing the reduction from clique to graph k-cut mentioned above, and then solving the graph k-cut problem in time  $n^{o(k)}$ . The total time for this procedure is the time needed to construct G' (a fixed polynomial in n) plus  $((n+2)(n^4))^{o(k+1)}$  which is  $n^{o(k)}$ . This completes the proof.  $\square$ 

#### 5. Conclusion

In the current paper, we have established very strong computational lower bounds on the time complexity of many natural NP-hard problems including: WEIGHTED SAT, SET COVER, FEATURE SET, WEIGHTED q-SAT, INDEPENDENT SET, CLIQUE, and DOMINATING SET. We showed that an algorithm with time complexity  $n^{o(k)}p(|I|)$  for some polynomial p (n is the size of the universal set from which the k elements are to be chosen, and |I| is the instance size), for any of the above problems would lead to unlikely consequences in complexity theory such as W[1] = FPT or all SNP problems are solvable in subexponential time. Observing that all the above problems can be solved in time  $n^k p(|I|)$  for some polynomial p by simply enumerating all subsets of size k from the universal set, the results in the current paper provide a strong evidence that significant improvement on the straightforward exhaustive search algorithms for the above problems seems unlikely. We also mention that the results in the current paper can be naturally extended to many other problems via standard reductions similar to the ones given in Theorem 4.3 (see [6]).

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