

The Essence of Region Abstraction for Timed Pushdown Automata

Yuya Uezato and Yasuhiko Minamide

University of Tsukuba

uezato@score.cs.tsukuba.ac.jp, minamide@cs.tsukuba.ac.jp

Abstract. Abdulla et al. introduced a highly infinite model of computation *timed pushdown automata* and showed the surprising result that the reachability problem is decidable. While the idea of their *region abstraction* is intuitive, they needed the elaborated proofs by lacking the good correspondence between concrete and abstract domain.

By our thorough analysis of their construction, we capture its essence and propose *synchronized recursive timed automata*, which have new and unusual transitions compared to existing models. Even if our model is obtained by investigating their region abstraction, we significantly simplify the decidability proof of the reachability problem by considering backward simulation.

1 Introduction

Abdulla et al. introduced timed pushdown automata (TPDA), which extend both the theories of pushdown systems and timed automata [1]. TPDA are highly infinite from the two dimensions: the unboundedness of the stack, and the unboundedness and denseness of clock values (reals). From this infiniteness, their result of showing the decidability of the fundamental decision problem “reachability problem” of TPDA is quite surprising.

Their construction is based on the *region abstraction* technique known in the theory of timed automata [3]. It is nonstandard and intriguing to accommodate pushdown extensions of timed automata by: (i) lazy time elapsing technique, which is used to simulate TPDA with pushdown systems, (ii) a region keeps the fractional parts of clocks beyond the bound, (iii) the special clock, reference clock, which plays some important roles. On the other hand, the details of their construction are involved and their proofs are elaborated. Hence the considering further extensions is difficult.

By our thorough analysis of their construction, we extract the essence of the construction and propose a new model *synchronized recursive timed automata (SRTA)*. TPDA have two kinds of clocks, global clocks and local clocks. However, SRTA have only one kind of clocks that can behave as both global and local clocks, and this yields the uniform treatment of clocks and our simplified proofs. Our model has unusual transitions compared to the timed pushdown automata of Abdulla et al. and the recursive timed automata introduced by Trivedi and Wojtczak in [10], and Benerecetti et al. in [4]. The transitions in our model can

be seen as more primitive, hence we can simulate the transitions in their models by using our transitions.

We significantly simplify the decidability proof of the reachability problem by considering backward simulation. The result comes from (i) clarifying the essence of the construction of Abdulla et al. and (ii) dividing our proofs into the two steps. We first translate SRTA to an intermediate model by adopting their lazy time elapsing technique *without* applying the region abstraction, and then apply the region abstraction to obtain a finite model from the intermediate one. The proof of simulation properties of the second step is derived by combining the fundamental properties of regions. We obtain a simpler decidability proof while our formalization and construction are based on the Abdulla's work. Because of the lack of space, we omit all proofs of lemmas and theorems. They can be found in the longer version¹ of this paper.

The recursive timed automata (RTA) introduced in [10, 4] are related to SRTA. During of transitions of RTA, the time elapses for the clocks on the stack top only. The combination of this local-time elapse operation with diagonal constraints is very powerful. Indeed, they showed that the class of RTA is Turing-complete in general and the reachability problem of RTA is undecidable.

Cai and Ogawa [7] gave an alternative decidability proof of TPDA by applying the theory of well-structured transition systems [2, 8].

Basic Terminology. \mathbb{Q}^+ is the set of positive rationals, $\mathbb{Q}^+ \triangleq \{r \in \mathbb{Q} : 0 \leq r\}$. For every rational $r \in \mathbb{Q}^+$, $[r]$ and $\{r\}$ denote the integral part and the fractional part of r , respectively. For example, $[1.5] = 1$ and $\{1.5\} = 0.5$. Let $M \in \mathbb{N}$ be a natural number. The subsets bounded by M , \mathbb{N}_M and \mathbb{Q}_M^+ , are defined as $\mathbb{N}_M \triangleq \{n \in \mathbb{N} : n \leq M\}$, $\mathbb{Q}_M^+ \triangleq \{r \in \mathbb{Q}^+ : r \leq M\}$. $A \cup B$ is used for the union of two disjoint sets A and B . Capital letters X, Y, \dots are used to denote a finite set of clocks.

Concrete Valuations. We write \mathcal{V}_X for the set of concrete valuations over X : $\mathcal{V}_X \triangleq X \rightarrow \mathbb{Q}^+$ and omit the subscript when X is clear from the context. Greek characters ν, μ, λ, \dots are used to denote concrete valuations.

Let $\nu \in \mathcal{V}_X$ be a concrete valuation over clock set X . For $x, y \in X$ and $r \in \mathbb{Q}^+$, we write $\nu[x := r]$ and $\nu[x := y]$ for the valuations defined by:

$$\nu[x := r](y) \triangleq \begin{cases} r & \text{if } y = x \\ \nu(y) & \text{otherwise,} \end{cases} \quad \nu[x := y] \triangleq \nu[x := \nu(y)].$$

In order to move the clocks of ν forward by $\delta \in \mathbb{Q}^+$, we use $(\nu + \delta)(x) \triangleq \nu(x) + \delta$ and define the partial-ordering $\nu \leq \nu'$ by $\exists \delta \in \mathbb{Q}^+. \nu' = \nu + \delta$.

2 Synchronized Recursive Timed Automata

We introduce a model of computation, *synchronized recursive timed automata*, which captures the essence of the region abstraction considered by Abdulla et al [1]. We first define M-bounded constraints and collapsed valuations.

¹ <http://score.cs.tsukuba.ac.jp/~uezato/icalp2015-long.pdf>

M-Bounded Constraints. Let X be a clock set and $M \in \mathbb{N}$ be a natural number. The M -bounded constraints $\mathcal{C}_{X,M}$ are obtained by the following grammar:

$$\varphi ::= [x] \bowtie k \mid \{x\} \bowtie \{y\} \mid \{x\} = 0 \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \neg \varphi$$

where $\bowtie \in \{<, =, \neq, >\}$, $x, y \in X$, and $k \in \mathbb{N}_M$.

The constraints are the logical characterization of the region considered by Abdulla et al.

Definition 1 (M-Bounded Collapsed Valuations). The set of M -bounded collapsed valuations over X is $\Lambda_X \triangleq X \rightarrow (\mathbb{N}_M^\infty \times \mathbb{Q}_{[0,1)}^+)$ where $\mathbb{N}_M^\infty \triangleq \mathbb{N}_M \cup \{\infty\}$ and $\mathbb{Q}_{[0,1)}^+ \triangleq \mathbb{Q}^+ \cap [0, 1)$. Upright greek letters ν, μ, λ, \dots are used to denote collapsed valuations. We use term *valuations* to denote collapsed valuations rather than concrete valuations. We omit X and write Λ if X is clear from the context.

Abusing the notation, we use $v.r$ to denote $(v, r) \in \mathbb{N}_M^\infty \times \mathbb{Q}_{[0,1)}^+$. Moreover $[v.r]$ and $\{v.r\}$ are used to denote v and r , respectively. ■

Let $r \in \mathbb{Q}^+$ and $\nu \in \mathcal{V}_X$. The collapsed value \hat{r} and valuation $\hat{\nu}$ are obtained by:

- If $r \leq M$, then $\hat{r} \triangleq ([r], \{r\})$. Otherwise, $\hat{r} \triangleq (\infty, \{r\})$.
- By using this collapse operation, $\hat{\nu}$ is defined by $\hat{\nu}(x) \triangleq \hat{r}$ where $r = \nu(x)$.

The fractional part is kept for the constraints $\{x\} = 0$ and $\{x\} \bowtie \{y\}$. Clearly, for any valuation ν , there exists a concrete valuation ν such that $\nu = \hat{\nu}$.

Satisfiability $\nu \models \varphi$. Let ν be a valuation over X and $\varphi \in \mathcal{C}_X$. We define the satisfiability $\nu \models \varphi$ through the atomic cases: (i) $\nu \models [x] \bowtie k$ if $[\nu(x)] \bowtie k$, (ii) $\nu \models \{x\} \bowtie \{y\}$ if $\{\nu(x)\} \bowtie \{\nu(y)\}$, (iii) $\nu \models \{x\} = 0$ if $\{\nu(x)\} = 0$.

Time passage and quasi-ordering. We define the time-passage $\nu + \delta$ for a valuation ν and $\delta \in \mathbb{Q}^+$ by $\nu + \delta \triangleq \widehat{\nu + \delta}$ where ν is a witness such that $\nu = \hat{\nu}$. $\nu + \delta$ is well-defined because the result does not depend on the choice of a witness. We define the *quasi-ordering* $\nu \preceq \nu'$ if there exists $\delta \in \mathbb{Q}^+$ such that $\nu' = \nu + \delta$. For $\nu = \{x \mapsto \infty.0\}$ and $\mu = \{x \mapsto \infty.5\}$, $\nu \preceq \mu$ and $\nu \succcurlyeq \mu$. However, $\nu \neq \mu$.

Updating $\nu[x := (v, r)]$ and $\nu[x := y]$. The updating $\nu[x := (v, r)]$ and $\nu[x := y]$ are defined in the same way as concrete valuations.

Restriction $\nu|Y$. For a valuation $\nu \in \Lambda_X$ and $Y \subseteq X$, we use $\nu|Y \in \Lambda_Y$ to denote the restricted valuation of ν whose domain is Y . We write $\nu \oplus \mu$ instead of $\nu \cup \mu$ for valuations $\nu \in \Lambda_{Y_1}$ and $\mu \in \Lambda_{Y_2}$ such that $\nu|Y_1 \cap Y_2 = \mu|Y_1 \cap Y_2$.

Definition 2 (Synchronized Recursive Timed Automata). A synchronized recursive timed automata (SRTA) is a structure (Q, Γ, M, Δ) where Q is a finite set of control locations, Γ is a finite set of clocks, M is a natural number, and $\Delta \subseteq Q \times Act \times Q$ is a finite set of transition rules. The set Act is given by:

$$Act ::= \mathbf{time} \mid \mathbf{push} \mid \mathbf{return} \mid \mathbf{dig}(x, y) \mid \mathbf{upd}(X, \varphi) \quad \text{where } x, y \in \Gamma, X \subseteq \Gamma, \varphi \in \mathcal{C}_{\Gamma, M}.$$

Remark: We assume the special clock \mathcal{C} belongs to Γ . It is called *reference clock* in the Abdulla's construction and it plays some important roles. ■

We present the essence of Abdulla's construction by introducing the collapsed semantics of SRTA.

Definition 3 (Collapsed Semantics $\mathcal{S}_{\text{coll}}$). A configuration is a pair $\langle q, \omega \rangle$ of a control location q and a stack ω whose element is a valuation $\mathbf{v} \in \Lambda_\Gamma$. The set of configurations $\text{Conf}(\mathcal{S}_{\text{coll}})$ is $Q \times \Lambda^*$.

For $\tau \in \text{Act}$, we define the action $\omega \xrightarrow{\tau} \omega'$ with $\omega, \omega' \in \Lambda^*$ by case analysis on τ as follows (we omit τ from $\omega \xrightarrow{\tau} \omega'$ and write $\omega \rightarrow \omega'$):

Case time: $\omega \mathbf{v} \rightarrow (\omega \mathbf{v}) + \delta$ where $\delta \in \mathbb{Q}^+$ and $(\mathbf{v}_1 \dots \mathbf{v}_n) + \delta \triangleq (\mathbf{v}_1 + \delta) \dots (\mathbf{v}_n + \delta)$.

Case push: $\omega \mathbf{v} \rightarrow \omega \mathbf{v}' 0_\Lambda$ where $0_\Lambda(x) \triangleq 0$ and $\mathbf{v}' = \mathbf{v}[\mathbb{C} := 0]$.

Case return: $\omega \mathbf{v} \mu \rightarrow \omega \mu$.

Case dig(x, y): $\omega \mathbf{v} \mu \rightarrow \omega \mathbf{v} (\mu[x := \mathbf{v}(y)])$.

Case upd(X, φ): $\omega \mathbf{v} \rightarrow \omega \mathbf{v}'$ where $\mathbf{v}' \in \mathbf{v}[X \leftarrow \varphi]$. For $X = \{x_1, x_2, \dots, x_n\}$,

$$\mathbf{v}[X \leftarrow \varphi] \triangleq \{\mathbf{v}' : r_i \in \mathbb{Q}^+, \mathbf{v}' = \mathbf{v}[x_1 := r_1, x_2 := r_2, \dots, x_n := r_n], \mathbf{v}' \models \varphi\}.$$

The transition relation $\xrightarrow{\tau} \subseteq \text{Conf}(\mathcal{S}_{\text{coll}}) \times \text{Conf}(\mathcal{S}_{\text{coll}})$ is defined as follows:

$$\langle q_1, \omega_1 \rangle \xrightarrow{\tau} \langle q_2, \omega_2 \rangle \quad \text{if } \langle q_1, \tau, q_2 \rangle \in \Delta \text{ and } \omega_1 \xrightarrow{\tau} \omega_2. \quad \blacksquare$$

By **time**, the valuations in the stack evolve synchronously. The **push** transition resets the reference clock and pushes 0_Λ onto the stack. This reset is crucial in Abdulla's construction. The **return** transition removes the second valuation \mathbf{v} , and then the top valuation μ stays on the top. The **dig**(x, y) transition is peculiar and we pass the value of y in the second valuation \mathbf{v} into x in the top valuation μ . The **upd**(X, φ) transition updates and checks the top valuation \mathbf{v} at the same time. The top valuation \mathbf{v} can be checked without modification by **upd**(\emptyset, φ).

Usage of the dig transition. By combining the **dig** and **return** transitions, the usual **pop** transition is implemented as follows:

$$\omega \mathbf{v} \mu \xrightarrow{\text{pop}} \omega \mathbf{v} \text{ is simulated by } \omega \mathbf{v} \mu \xrightarrow{\text{dig}(x_1, x_1)} \dots \xrightarrow{\text{dig}(x_n, x_n)} \omega \mathbf{v} \mathbf{v} \xrightarrow{\text{return}} \omega \mathbf{v},$$

where $\Gamma = \{x_1, \dots, x_n\}$. With the same idea, we can simulate the pass-by-value and pass-by-reference appearing in recursive timed automata [10, 4].

Encoding Stack Symbols. Let us see the example with stack symbols $\{a, b\}$:

$$\omega \langle a, \mathbf{v} \rangle \xrightarrow{\text{push}(b)} \omega \langle a, \mathbf{v} \rangle \langle b, 0_\Lambda \rangle \xrightarrow{\text{test}(b)} \omega \langle a, \mathbf{v} \rangle \langle b, 0_\Lambda \rangle.$$

This pushes the stack symbol b and tests it against b . This is encoded by:

$$\omega' \mathbf{v}' \xrightarrow{\text{push}} \omega' \mathbf{v}' 0_\Lambda \xrightarrow{\text{upd}(\{\#, \#_a, \#_b\}, \psi_b)} \omega' \mathbf{v}' \mu \xrightarrow{\text{upd}(\emptyset, \{\# \} = \{\#_b\})} \omega' \mathbf{v}' \mu,$$

where $\psi_b \triangleq \{\# \} \neq \{\#_a\} \wedge \{\# \} = \{\#_b\}$ and b is encoded in the fractional parts.

Reachability Problem of SRTA. Let $q_{\text{init}}, q_{\text{final}} \in Q$ be control locations. The *reachability problem from q_{init} to q_{final}* is to decide whether there is a computation from $\langle q_{\text{init}}, 0_\Lambda \rangle$ to $\langle q_{\text{final}}, \omega \rangle$ for some stack $\omega \in \Lambda^*$.

2.1 Comparison Study: Encoding Timed Pushdown Automata

We review the definition of TPDA introduced by Abdulla et al. [1], and see how a TPDA is simulated as a SRTA. It should be noted that the following definition is slightly revised from the original, since in their model a clock constraint is of the form $c \in? I$ that checks whether the value of c belongs to an interval I .

A TPDA is a structure $(Q, \Sigma, \perp, \mathcal{X}, M, \Delta)$ where Σ is a finite set of stack symbols, $\perp \in \Sigma$ is the identified stack symbol, \mathcal{X} is a finite set of global clocks, and $\Delta \subseteq Q \times Disc \times Q$ is a finite set of transition rules. The set $Disc$ is given by:

$$Disc ::= \text{push}(\sigma, \phi) \mid \text{pop}(\sigma, \phi) \mid \text{upd}(X, \varphi) \quad \text{where } \sigma \in \Sigma, \phi \in \mathcal{C}_\sigma, X \subseteq \mathcal{X}, \varphi \in \mathcal{C}_\mathcal{X}.$$

A stack element is a pair $\langle \sigma, r \rangle$ of a stack symbol $\sigma \in \Sigma$ and a rational $r \in \mathbb{Q}^+$, and a configuration is a triple $\langle q, \nu, w \rangle$ of a location $q \in Q$, a concrete valuation of global clocks $\nu \in \mathcal{V}_\mathcal{X}$, and a stack $w \in (\Sigma \times \mathbb{Q}^+)^*$.

For $\langle p, \tau, q \rangle \in \Delta$, we define transitions by case analysis on τ as follows:

Case $\text{push}(\sigma, \phi)$: $\langle p, \nu, w \rangle \rightsquigarrow \langle q, \nu, w \langle \sigma, r \rangle \rangle$ where $r \in \mathbb{Q}^+$ with $\{\sigma \mapsto r\} \models \phi$.

Case $\text{pop}(\sigma, \phi)$: $\langle p, \nu, w \langle \sigma, r \rangle \rangle \rightsquigarrow \langle q, \nu, w \rangle$ if $\{\sigma \mapsto r\} \models \phi$.

Case $\text{upd}(X, \varphi)$: $\langle p, \nu, w \rangle \rightsquigarrow \langle q, \nu', w \rangle$ where $\nu' \in \nu[X \leftarrow \varphi]$.

In the above, we use the satisfiability $\nu \models \varphi$ and $\nu[X \leftarrow \varphi]$ for concrete valuations. They are defined in a similar way to collapsed valuations. The $\text{push}(\sigma, \phi)$ transition pushes $\langle \sigma, r \rangle$, which satisfies ϕ . The $\text{pop}(\sigma, \phi)$ transition pops the top $\langle \sigma, r \rangle$ if r satisfies ϕ .

Their formulation does not permit the explicit **time**-transition but the corresponding transition $\xrightarrow{\text{time}}$ invokes *spontaneously*: $\langle p, \nu, w \rangle \xrightarrow{\text{time}} \langle p, \nu + \delta, w + \delta \rangle$ for some $\delta \in \mathbb{Q}^+$. The transition relation \rightsquigarrow is defined as follows:

$$\langle p, \nu, w \rangle \rightsquigarrow \langle q, \nu', w' \rangle \quad \text{if } \exists \langle p, \tau, q \rangle \in \Delta. \langle p, \nu, w \rangle \xrightarrow{\tau} \langle q, \nu', w' \rangle \text{ or } \langle p, \nu, w \rangle \xrightarrow{\text{time}} \langle q, \nu', w' \rangle.$$

Reachability Problem of TPDA. Let $q_{\text{init}}, q_{\text{final}} \in Q$ be control locations.

The *reachability problem* from q_{init} to q_{final} is to decide whether there is a computation from $\langle q_{\text{init}}, 0_\nu, \langle \perp, 0.0 \rangle \rangle \rightsquigarrow^* \langle q_{\text{final}}, \nu, w \rangle$ for some valuation $\nu \in \mathcal{V}_\mathcal{X}$ and stack $w \in (\Sigma \times \mathbb{Q}^+)^*$.

We use a SRTA $(Q', \mathcal{X} \cup \{\#, \mathbb{C}\}, M, \Delta')$ with the stack symbols Σ and define the correspondence between configurations of RTA and the collapsed semantics:

- $\langle q, \nu, w \langle \sigma, r \rangle \rangle \sim \langle q, \omega \langle \sigma, \nu \rangle \rangle$ if $\widehat{\nu} = \nu|_\mathcal{X}$ and $w \langle \sigma, r \rangle \approx \omega \langle \sigma, \nu \rangle$.
- $\langle \sigma, r \rangle \approx \langle \sigma, \nu \rangle$ if $\widehat{r} = \nu(\#)$. $w \langle \sigma, r \rangle \approx \omega \langle \sigma, \nu \rangle$ if $\langle \sigma, r \rangle \approx \langle \sigma, \nu \rangle$ and $w \approx \omega$.

The valuation ν of the global clocks is kept in the top valuation ν in SRTA. We can encode TPDA into the corresponding SRTA based on the above correspondence so that the following are equivalent:

- There exists a computation q_{init} from q_{final} in a TPDA.
- There exists a computation q_{init} from q_{final} in the corresponding SRTA.

It is required to assume that $\langle q, \nu, w \rangle \rightsquigarrow^* \langle q', \nu', \epsilon \rangle$ never occur. Since TPDA can perform a transition from a configuration with *the empty stack*, but SRTA cannot. Without loss of generality, we build the corresponding TPDA which satisfies the condition.

2.2 Comparison Study: Recursive Timed Automata [10, 4]

We review the recursive timed automata (RTA), which are independently introduced by Trivedi and Wojtczak in [10] and Benerecetti et al. in [4]. A RTA can be seen as a structure (Q, X, Δ) and a configuration is a pair $\langle q, w \rangle$ of a location q and a stack $w \in \mathcal{V}_\mathcal{X}^*$. There are two significant differences between RTA and SRTA:

- RTA has the *local* time elapse transition $\langle p, \mathbf{Ltime}, q \rangle \in \Delta$, which invokes the transition $\langle p, w \nu \rangle \xrightarrow{\mathbf{Ltime}} \langle q, w(\nu + \delta) \rangle$ with some $\delta \in \mathbb{Q}^+$.
- Two clocks are compared by diagonal constraints such as $x_1 = x_2$, $x_1 < x_2$, etc.

The combination of local time elapse transition and the diagonal constraints is very powerful. Indeed, the undecidability of the reachability problem of RTA was proved by showing that RTA can encode two-counter machines in [10, 4]. However, the expressiveness of only using local time elapse transition, or the combination of **time** and diagonal constraints has not been studied yet. This will be discussed in the conclusion section.

3 Lazy Semantics and What Good is the Reference Clock?

The denseness of the rationals makes SRTA an infinite model, and the entire modification of the stack in the **time** transition makes SRTA beyond the theory of pushdown systems. As a starting point in attacking these problems, we distill the entire modification into the lazy semantics $\mathcal{S}_{\text{lazy}}$. By considering the lazy semantics, the key role of the reference clock is revealed.

We quickly review the lazy time elapsing technique introduced by Abdulla et al. [1]. This removes the entire stack modification in the **time** transition.

For a preparation, we define the quasi-ordering on pairs of valuations: $\langle \nu, \mu \rangle \preceq \langle \nu', \mu' \rangle$ if $\exists \delta \in \mathbb{Q}^+ . \nu' = \nu + \delta$ and $\mu' = \mu + \delta$. We say that $\langle \nu, \mu_1 \rangle$ and $\langle \mu_2, \lambda \rangle$ are compatible if $\mu_1 = \mu_2$.

Let us explain the key idea of their construction by considering the following:

$$\omega \nu \mu \xrightarrow{\text{push}} \omega \nu \mu' 0_\Lambda \rightarrow \dots \rightarrow \omega \nu \mu' \lambda \xrightarrow{\text{time}} \omega' \nu' \mu'' \lambda' \xrightarrow{\text{return}} \omega' \nu' \lambda'.$$

This is simulated as follows by pairing the valuations:

$$w \langle \nu, \mu \rangle \xrightarrow{\text{push}} w \langle \nu, \mu' \rangle \langle \mu', 0_\Lambda \rangle \rightarrow \dots \rightarrow w \langle \nu, \mu' \rangle \langle \mu', \lambda \rangle \xrightarrow{\text{time}} w \langle \nu, \mu' \rangle \langle \mu'', \lambda' \rangle \xrightarrow{\text{return}} w \langle \nu', \lambda' \rangle.$$

In the transition $w \langle \nu, \mu' \rangle \langle \mu'', \lambda' \rangle \xrightarrow{\text{return}} w \langle \nu', \lambda' \rangle$, we first increase $\langle \nu, \mu' \rangle$ into $\langle \nu', \mu'' \rangle$ with $\langle \nu, \mu' \rangle \preceq \langle \nu', \mu'' \rangle$ to be compatible with $\langle \mu'', \lambda' \rangle$, and then we remove μ'' . This lazy time elapsing technique ensures the top pair $\langle \mu'', \lambda' \rangle$ in the simulating side is same as the top valuation λ' and the second valuation μ'' in the collapsed semantics. This idea derives the lazy semantics.

Definition 4 (Lazy Semantics $\mathcal{S}_{\text{lazy}}$). A stack element is a pair $\langle \nu, \mu \rangle$ of valuations $\nu, \mu \in \Lambda_\Gamma$. A configuration is a pair $\langle q, w \rangle$ of a location $q \in Q$ and a stack $w \in (\Lambda_\Gamma \times \Lambda_\Gamma)^*$. The set of configurations $\text{Conf}(\mathcal{S}_{\text{lazy}})$ is $Q \times (\Lambda \times \Lambda)^*$.

The action $w \xrightarrow{\tau} w'$ between stacks is defined by case analysis on $\tau \in \text{Act}$:

Case time: $w \langle \nu, \mu \rangle \rightarrow w \langle \nu', \mu' \rangle$ where $\langle \nu, \mu \rangle \preceq \langle \nu', \mu' \rangle$.

Case push: $w \langle \nu, \mu \rangle \rightarrow w \langle \nu, \mu' \rangle \langle \mu', 0_\Lambda \rangle$ where $\mu' = \mu[\mathbb{C} := 0]$.

Case return: $w \langle \nu, \mu \rangle \langle \mu', \lambda \rangle \rightarrow w \langle \nu', \lambda \rangle$ where $\langle \nu, \mu \rangle \preceq \langle \nu', \mu' \rangle$.

Case dig(x, y): $w \langle \nu, \mu \rangle \langle \mu', \lambda \rangle \rightarrow w \langle \nu, \mu \rangle \langle \mu', \lambda[x := \mu'(y)] \rangle$.

Case upd(X, φ): $w \langle \nu, \mu \rangle \rightarrow w \langle \nu, \mu' \rangle$ where $\mu' \in \mu[X \leftarrow \varphi]$. ■

We define the well-formedness of a stack, $\text{WF}(w)$, inductively by

$$\text{WF}(\langle \nu, \mu \rangle), \quad \text{WF}(w \langle \nu, \mu \rangle \langle \mu', \lambda \rangle) \text{ if } \mu(\mathbb{C}) = 0, \mu \preceq \mu', \text{ and } \text{WF}(w \langle \nu, \mu \rangle).$$

From the definition of the actions, it is clear that every transition preserves well-formedness. That is, if $\text{WF}(w)$ and $w \rightarrow w'$, then $\text{WF}(w')$.

The Correspondence between $\mathcal{S}_{\text{coll}}$ and $\mathcal{S}_{\text{lazy}}$. Let ω and \mathbf{w} be stacks of $\mathcal{S}_{\text{coll}}$ and $\mathcal{S}_{\text{lazy}}$ with $\text{WF}(\mathbf{w})$. We introduce the correspondence relation $\omega \sim \mathbf{w}$.

First, we define the concretization function Φ to concretize a stack of lazy semantics. For a well-formed stack $\text{WF}(\mathbf{w})$, the function $\Phi : (\Lambda \times \Lambda)^* \rightarrow \Lambda^*$ is defined inductively as follows:

$$\Phi(\langle \nu, \mu \rangle) \triangleq \mu, \quad \Phi(\mathbf{w} \langle \nu, \mu \rangle \langle \mu', \lambda \rangle) \triangleq \Phi(\mathbf{w} \langle \nu', \mu' \rangle) \lambda,$$

where ν' is the valuation such that $\langle \nu, \mu \rangle \preceq \langle \nu', \mu' \rangle$.

The role of the reference clock. Note that such ν' is unique because $\mu(\mathbb{C}) = 0$ for $\text{WF}(\mathbf{w} \langle \nu, \mu \rangle \langle \mu', \lambda \rangle)$. The following proposition ensures this uniqueness.

Proposition 5. If $\mu(\mathbb{C}) = 0$ and $\langle \nu, \mu \rangle \preceq \langle \nu_1, \mu' \rangle, \langle \nu_2, \mu' \rangle$, then $\nu_1 = \nu_2$.

If we lack the reference clock, then the following bad situation occurs with $M = 2$:

$$\langle \{x \mapsto 1.0\}, \{y \mapsto 2.0\} \rangle \preceq \langle \{x \mapsto 2.0\}, \{y \mapsto \infty.0\} \rangle, \langle \{x \mapsto \infty.0\}, \{y \mapsto \infty.0\} \rangle.$$

The concretization function derives the forward simulation properties.

Theorem 6. Let ω and \mathbf{w} be stacks of $\mathcal{S}_{\text{coll}}$ and $\mathcal{S}_{\text{lazy}}$, respectively.

If $\text{WF}(\mathbf{w})$ and $\omega = \Phi(\mathbf{w})$, then the following hold:

1. If $\omega \xrightarrow{\tau} \omega'$, then there exists \mathbf{w}' such that $\mathbf{w} \xrightarrow{\tau} \mathbf{w}'$ and $\omega' = \Phi(\mathbf{w}')$.
2. If $\mathbf{w} \xrightarrow{\tau} \mathbf{w}'$, then there exists ω' such that $\omega \xrightarrow{\tau} \omega'$ and $\omega' = \Phi(\mathbf{w}')$.

This result states that we can use the lazy semantics instead of the collapsed semantics to solve the reachability problem. However, the lazy semantics is infinite from the denseness of the fractional parts of clocks. In the next section, we abstract the lazy semantics into a finite one by applying the region abstraction technique.

4 Digital Valuation: Revisiting Abdulla's Region

We define a finite abstraction of valuations Λ to abstract the lazy semantics. To ensure the finiteness of our abstraction, we adopt the same construction technique, region abstraction, used by Abdulla et al. [1]. It abstracts the concrete fractional values of clocks away and only keeps the ordering information between the clocks.

First, we define the digital valuations as the abstract domain of valuations. The structure of digital valuations is basically the same as the region considered in [1]. Since the regions considered by Abdulla et al. contain additional information, we use the term *digital* valuations to distinguish theirs.

Definition 7 (Digital Valuations). Let X be a clock set and D be a sequence of clock sets $c_0 c_1 \dots c_n$ where $c_i \subseteq X \times \mathbb{N}_M^\infty$. First, two useful notations are defined:

$$\begin{aligned} x \in c_i & \quad \text{if } \exists v \in \mathbb{N}_M^\infty. (x, v) \in c_i, \\ (x, v) \in D & \quad \text{if } \exists i. (x, v) \in c_i. \end{aligned}$$

D is a digital valuation if it satisfies the following:

- Every clock appears: if $x \in X$, then $x \in c_i$ with $0 \leq i \leq n$.
- Every clock appears once: if $(x, v) \in c_i$ and $(x, v') \in c_j$, then $i = j$ and $v = v'$.

- D is almost normalized: $c_i \neq \emptyset$ for $1 \leq i \leq n$. Note c_0 may be \emptyset .
- M appears at the head position only: if $(x, M) \in D$, then $(x, M) \in c_0$.

We use \mathcal{D}_X to denote the set of digital valuations over X . \blacksquare

Intuitively, a valuation $\mathbf{v} = \{x \mapsto 0.8, y \mapsto 1.2, z \mapsto \infty.5\}$ is abstracted into the digital valuation $D = \emptyset \{(y, 1)\} \{(z, \infty)\} \{(x, 0)\}$. Then, we formalize this intuition and define the abstraction function α from a valuation \mathbf{v} to the corresponding digital valuation D .

Abstraction. Let \mathbf{v} be a valuation and $D = c_0 c_1 \dots c_n$ be a digital valuation. Then, the realization relation $\mathbf{v} \models D$ holds if the following are satisfied:

- (a) $(x, \lfloor \mathbf{v}(x) \rfloor) \in D$, (b) $\{\mathbf{v}(x)\} = 0$ iff $x \in c_0$, (c) $\{\mathbf{v}(x)\} \bowtie \{\mathbf{v}(y)\}$ iff $x \in c_i$ and $y \in c_j$ with $i \bowtie j$.

Proposition 8. Valuations and digital valuations are closely related as follows.

- For a digital valuation D , there exists a valuation \mathbf{v} such that $\mathbf{v} \models D$.
- For a valuation \mathbf{v} , there exists the unique digital valuation $\alpha(\mathbf{v})$ with $\mathbf{v} \models \alpha(\mathbf{v})$.

4.1 Basic Operations of Digital Valuations

We define the basic operations of digital valuations that correspond to $\mathbf{v} \models \varphi$, $\mathbf{v} \preceq \mathbf{v}'$, $\mathbf{v}[x := y]$, $\mathbf{v}[X \leftarrow \varphi]$, and $\mathbf{v}|X$ for valuations.

Let X be a clock set and $D = c_0 \dots c_n$ be a digital valuation over X .

Satisfiability $D \models \varphi$. Let $x \in X$ be a clock and $\varphi \in \mathcal{C}_X$ be a constraint. We define $D \models \varphi$ as follows:

$$\begin{aligned} D \models [x] \bowtie k & \quad \text{if } (x, k') \in D \text{ and } k' \bowtie k, \\ D \models \{x\} \bowtie \{y\} & \quad \text{if } x \in c_i \text{ and } y \in c_j \text{ with } i \bowtie j, \\ D \models \{x\} = 0 & \quad \text{if } x \in c_0. \end{aligned}$$

The inductive cases are defined naturally. Note that c_0 plays a distinguished role to define $\{x\} = 0$. From the definition, it is clear that $\mathbf{v} \models \varphi$ iff $\alpha(\mathbf{v}) \models \varphi$.

Time Passage. We define the time-passage relation $D \vdash D'$, which corresponds to $\mathbf{v} \preceq \mathbf{v}'$, as follows:

Small elapse: if $c_0 \neq \emptyset$, then $c_0 c_1 \dots c_n \vdash \emptyset c'_0 c_1 \dots c_n$ where c'_0 is defined by:

$$(x, M) \in c_0 \Rightarrow (x, \infty) \in c'_0, \quad (x, v) \in c_0 \Rightarrow (x, v) \in c'_0 \ (v \in \mathbb{N}_M^\infty \wedge v \neq M).$$

Carry: $\emptyset c_1 \dots c_n c_{n+1} \vdash c'_0 c_1 \dots c_n$ where c'_0 is defined as follows:

$$(x, \infty) \in c_{n+1} \Rightarrow (x, \infty) \in c'_0, \quad (x, v) \in c_{n+1} \wedge v < M \Rightarrow (x, v+1) \in c'_0.$$

We use $D \vdash^* D'$ to denote the reflexive transitive closure of \vdash .

Proposition 9. The correspondences between the relation \vdash and \preceq are:

1. If $\mathbf{v} \models D$ and $D \vdash D'$, then there exists \mathbf{v}' such that $\mathbf{v}' \models D'$ and $\mathbf{v} \preceq \mathbf{v}'$.
2. If $\mathbf{v} \models D$ and $\mathbf{v} \preceq \mathbf{v}'$, then there exists D' such that $\mathbf{v}' \models D'$ and $D \vdash^* D'$.
3. If $\mathbf{v}' \models D'$ and $D \vdash D'$, then there exists \mathbf{v} such that $\mathbf{v} \models D$ and $\mathbf{v} \preceq \mathbf{v}'$.
4. If $\mathbf{v}' \models D'$ and $\mathbf{v} \preceq \mathbf{v}'$, then there exists D such that $\mathbf{v} \models D$ and $D \vdash^* D'$.

The forward correspondences 1 and 2 are standard for regions considered in the theory of timed automata [3]. However, the backward correspondences 3 and 4 are missed in *concrete* valuations. Let us explain the example with $M = 1$, $D = \{(x, 0), (y, 1)\} \vdash D' = \emptyset \{(x, 0), (y, \infty)\}$.

Then $\nu' = \{x \mapsto 0.1, y \mapsto 2.1\} \models D'$, but there is no ν such that $\nu \leq \nu'$ and $\nu \models D$. For the collapsed valuation $\hat{\nu}' = \{x \mapsto 0.1, y \mapsto \infty.1\} \models D'$, we can find $\nu = \{x \mapsto 0.0, y \mapsto 1.0\}$ such that $\nu \preceq \hat{\nu}'$ and $\nu \models D$.

The backward correspondences 3 and 4 are quite important in our proof of the main Theorem 14. Instead of forward simulation, Theorem 14 establishes a backward simulation which requires the above backward correspondences.

Updating $D[x := y]$ and $D[X \leftarrow \varphi]$. A digital valuation $D[x := y] \in \mathcal{D}_X$ is obtained by copying the value of y into x : $D[x := y] \triangleq \{\alpha(\nu') : \nu \models D, \nu' = \nu[x := y]\}$. Note that $D[x := y]$ is singleton and we use $D[x := y]$ to denote such the element.

A finite set of digital valuations $D[X \leftarrow \varphi]$ is obtained by updating X to satisfy the constraint φ : $D[X \leftarrow \varphi] \triangleq \{\alpha(\nu') : \nu \models D, \nu' \in \nu[X \leftarrow \varphi]\}$. We can define $D[x := y]$ and $D[X \leftarrow \varphi]$ in a more concrete manner, but here we adopt the above one to save space.

Restriction. Let $Y \subseteq X$. The restriction $D|Y$ of D to Y is defined by $D|Y \triangleq \{\alpha(\nu|Y) : \nu \models D\}$. Then $D|Y$ is singleton and we use $D|Y$ to denote the element.

Proposition 10. The following are basic properties of the restriction.

- If $\nu \in \Lambda_X$, $Y \subseteq X$, $\nu \models D$, then $\nu|Y \models D|Y$.
- Let $D \in \mathcal{D}_X$, $D' \in \mathcal{D}_Y$, and $X \subseteq Y$.
If $\nu \models D$ and $D = D'|X$, then there exists ν' such that $\nu = \nu'|X$ and $\nu' \models D'$.

These notions have been considered in the theory of timed automata, and thus they are not intrinsic operations for the pushdown extension of timed automata. By contrast, the notations below are introduced for the pushdown extension. Basically they are already considered by Abdulla et al., but we present the same thing in a simpler way.

Renaming Clock. Let $\rho : X \rightleftharpoons Y$ be a renaming (bijection) from X to Y . For a digital valuation $D \in \mathcal{D}_X$, we define the renamed digital valuation $\rho(D) \in \mathcal{D}_Y$:

$$\rho(c_0 c_1 \dots c_n) \triangleq \rho(c_0) \rho(c_1) \dots \rho(c_n),$$

where $\rho(c_i) \triangleq \{(\rho(x), v) : (x, v) \in c_i\} \subseteq Y \times \mathbb{N}_M^\infty$.

Compatibility and Join. Let $D_1 \in \mathcal{D}_X$ and $D_2 \in \mathcal{D}_Y$ be digital valuations.

D_1 and D_2 are compatible or joinable if $D_1|X \cap Y = D_2|X \cap Y$, and this is written by $D_1 \parallel D_2$. We define the join operation $D_1 \oplus D_2$ in order to obtain a large digital valuation from small digital valuations:

$$D_1 \oplus D_2 \triangleq \{D \in \mathcal{D}_{X \cup Y} : D_1 = D|X, D_2 = D|Y\}.$$

It is clear that $D_1 \parallel D_2$ iff $D_1 \oplus D_2$ is not empty.

Proposition 11. Let $D_1 \in \mathcal{D}_X$ and $D_2 \in \mathcal{D}_Y$ be digital valuations.

- If $\nu \models D_1$, $\mu \models D_2$, and $\nu \oplus \mu$ is defined, then $\alpha(\nu \oplus \mu) \in D_1 \oplus D_2$.
- If $D \in D_1 \oplus D_2$, then we can find ν_i such that $\nu_i \models D_i$ and $\nu_1 \oplus \nu_2 \models D$.

5 Digitized Semantics

In the lazy semantics, a stack element is a pair of valuations, $\langle \nu, \mu \rangle$. In order to abstract the pair into a *single* digital valuation D , we define a marker.

Marked Clock. We write \dot{x} and \dot{x} to denote the marked clocks of x . The marked clock set \dot{X} is defined by $\dot{X} \triangleq \{\dot{x} : x \in X\}$, and \dot{X} is also defined. For a valuation $\mathbf{v} \in \Lambda_X$, we use $\dot{\mathbf{v}} \in \Lambda_{\dot{X}}$ and $\dot{\mathbf{v}} \in \Lambda_{\dot{X}}$ to denote the marked valuations. For a constraint $\varphi \in \mathcal{C}_X$, $\dot{\varphi} \in \mathcal{C}_{\dot{X}}$ is the constraint obtained by marking all clocks in φ .

A pair of valuations $\langle \mathbf{v}, \mu \rangle$ is isomorphic to $\dot{\mathbf{v}} \oplus \dot{\mu}$. By using this, $\langle \mathbf{v}, \mu \rangle$ is abstracted into $\alpha(\dot{\mathbf{v}} \oplus \dot{\mu})$ and we write $\langle \mathbf{v}, \mu \rangle \models \dot{D}$ if $\dot{\mathbf{v}} \oplus \dot{\mu} \models D$.

For example, the following holds:

$$\langle \{x \mapsto 0.0, y \mapsto 1.5\}, \{x \mapsto \infty.2, y \mapsto 0.7\} \rangle \models \{(x, 0)\} \{(\dot{x}, \infty)\} \{(y, 1)\} \{(\dot{y}, 0)\}.$$

Next, we prepare the marker removing operation by using renaming.

Removing Marker. Let $D \in \mathcal{D}_{\Gamma \cup \dot{\Gamma}}$ be a digital valuation. We write $\dot{D} \in \mathcal{D}_{\Gamma \cup \Gamma}$ to denote the digital valuation $\rho(D)$ where $\rho : \Gamma \cup \dot{\Gamma} \hookrightarrow \Gamma \cup \Gamma$ is defined by $\rho(x) \triangleq x$, $\rho(\dot{x}) \triangleq x$. In a similar way, we define $\dot{D} \in \mathcal{D}_{\Gamma \cup \dot{\Gamma}}$.

Recall the definition of **return** of the lazy semantics:

$$\mathbf{w} \langle \mathbf{v}, \mu \rangle \langle \mu', \lambda \rangle \rightarrow \mathbf{w} \langle \mathbf{v}', \lambda \rangle \quad \text{where } \langle \mathbf{v}, \mu \rangle \preceq \langle \mathbf{v}', \mu' \rangle.$$

We encode this transition as:

1. Let $D_1, D_2 \in \mathcal{D}_{\Gamma \cup \dot{\Gamma}}$ such that $\langle \mathbf{v}, \mu \rangle \models D_1$ and $\langle \mu', \lambda \rangle \models D_2$.
2. Evolving D_1 to \dot{D}'_1 for $\dot{D}'_1 \parallel \dot{D}_2$ corresponds to $\langle \mathbf{v}, \mu \rangle \preceq \langle \mathbf{v}', \mu' \rangle$.
3. Join and obtain $D \in \dot{D}'_1 \oplus \dot{D}_2$, then $D \in \mathcal{D}_{\Gamma \cup \Gamma \cup \dot{\Gamma}}$ and $\dot{\mathbf{v}}' \oplus \dot{\mu}' \oplus \dot{\lambda} \models D$.
4. Removing a middle $D' = D|_{\Gamma \cup \dot{\Gamma}}$, and $\langle \mathbf{v}', \lambda \rangle \models D'$.

In summary, we define the digitized semantics $\mathcal{S}_{\text{digi}}$ as follows.

Definition 12 (Digitized Semantics $\mathcal{S}_{\text{digi}}$). A configuration is a pair $\langle q, \mathbf{W} \rangle$ of a location q and a stack $\mathbf{W} \in \mathcal{D}_{\Gamma \cup \dot{\Gamma}}^*$. Hence the set of configurations $\text{Conf}(\mathcal{S}_{\text{digi}})$ is $Q \times \mathcal{D}^*$. In the following, we write $D(x) = v$ if $(x, v) \in c_0$ for $D = c_0 c_1 \dots c_n$.

For $\tau \in \text{Act}$, we define the action $\mathbf{W} \xrightarrow{\tau} \mathbf{W}'$ by case analysis on τ as follows:

Case time: $\mathbf{W}D \rightarrow \mathbf{W}D'$ where $D \vdash^* D'$.

Case push: $\mathbf{W}D_1 \rightarrow \mathbf{W}D'_1 D_2$ where $D'_1 = D_1[\dot{\mathbf{c}} := 0]$, $\dot{D}'_1 \parallel \dot{D}_2$, $D_2(\dot{x}) = 0$ ($\forall x \in \Gamma$).

Case return: $\mathbf{W}D_1 D_2 \rightarrow \mathbf{W}(D|_{\Gamma \cup \dot{\Gamma}})$ where $D_1 \vdash^* D'_1$, $D \in \dot{D}'_1 \oplus \dot{D}_2$.

Case dig(x, y): $\mathbf{W}D_1 D_2 \rightarrow \mathbf{W}D_1(D_2[\dot{x} := y])$.

Case upd(X, φ): $\mathbf{W}D \rightarrow \mathbf{W}D'$ where $D' \in D[\dot{X} \leftarrow \dot{\varphi}]$. ■

It should be noted that the marked digital valuations over $\Gamma \cup \dot{\Gamma}$ are basically the same as the regions considered by Abdulla et al.

We define the well-formedness of a stack, $\text{WF}(\mathbf{W})$, inductively:

$$\text{WF}(D), \quad \text{WF}(\mathbf{W}D_1 D_2) \text{ if } D_1(\dot{\mathbf{c}}) = 0, D_1 \vdash^* D'_1, \dot{D}'_1 \parallel \dot{D}_2, \text{ and } \text{WF}(\mathbf{W}D_1).$$

Every transition preserves well-formedness: if $\text{WF}(\mathbf{W})$ and $\mathbf{W} \rightarrow \mathbf{W}'$, then $\text{WF}(\mathbf{W}')$.

5.1 The Correspondence between $\mathcal{S}_{\text{lazy}}$ and $\mathcal{S}_{\text{digi}}$

We extend the abstraction $\alpha(\mathbf{w}) \in \mathcal{D}_{\Gamma \cup \dot{\Gamma}}^n$ for $\mathbf{w} \in (\Lambda \times \Lambda)^n$ inductively as follows:

$$\alpha(\epsilon) \triangleq \epsilon, \quad \alpha(\mathbf{w} \langle \mathbf{v}, \mu \rangle) \triangleq \alpha(\mathbf{w}) \alpha(\langle \mathbf{v}, \mu \rangle).$$

Proposition 13. If \mathbf{w} is well-formed, then $\alpha(\mathbf{w})$ is also well-formed.

If \mathbf{W} is well-formed, then there exists a well-formed stack \mathbf{w} with $\alpha(\mathbf{w}) = \mathbf{W}$.

Theorem 14. The extended abstraction derives the simulation properties.

1. Let \mathbf{w} be a well-formed stack of $\mathcal{S}_{\text{lazy}}$. If $\mathbf{w} \xrightarrow{\tau} \mathbf{w}'$, then $\alpha(\mathbf{w}) \xrightarrow{\tau} \alpha(\mathbf{w}')$.
2. Let \mathbf{W} be a well-formed stack of $\mathcal{S}_{\text{digi}}$. If $\mathbf{W} \xrightarrow{\tau} \mathbf{W}'$, $\alpha(\mathbf{w}') = \mathbf{W}'$, and $\text{WF}(\mathbf{w}')$, then there exists \mathbf{w} such that $\mathbf{w} \xrightarrow{\tau} \mathbf{w}'$, $\text{WF}(\mathbf{w})$, and $\alpha(\mathbf{w}) = \mathbf{W}$.

Consider the following forward simulation instead of the later of Theorem 14:

- 2'. Let \mathbf{w} and \mathbf{W} be well-formed stacks. If $\alpha(\mathbf{w}) = \mathbf{W}$ and $\mathbf{W} \xrightarrow{\tau} \mathbf{W}'$, then there exists \mathbf{w}' such that $\mathbf{w} \xrightarrow{\tau} \mathbf{w}'$ and $\alpha(\mathbf{w}') = \mathbf{W}'$.

Then there is a counter-example, which is caused by the ambiguity appeared in the **return** transition.

$$\frac{\{\dot{\mathbb{C}} \mapsto 0, \dot{x} \mapsto 0.2, \underline{x} \mapsto 1.6\}}{\{\dot{\mathbb{C}} \mapsto 0, \underline{x} \mapsto 0.3, \dot{x} \mapsto 1.6\}_{\mathbf{w}}} \models \{(\dot{\mathbb{C}}, 0)\} \{(\dot{x}, 0)\} \{(\underline{x}, 1)\} \xrightarrow{\text{return}} \emptyset \{(\underline{x}, 0)\} \{(\dot{x}, 0)\} \quad \downarrow$$

$$\{(\dot{\mathbb{C}}, 0)\} \{(\underline{x}, 0)\} \{(\dot{x}, 1)\} \xrightarrow{\text{return}} \emptyset \{(\underline{x}, 0)\} \{(\dot{x}, 0)\} \quad \uparrow$$

Then $\mathbf{w} \xrightarrow{\text{return}} \{\dot{x} \mapsto 0.2, \underline{x} \mapsto 0.3\}$ and this is the only valuation obtained from \mathbf{w} . But, it does not realize \mathbf{W}' . On the other hand, Abdulla et al. introduced the flattening operator of the stack of regions and gave the elaborated proof of a statement similar to the statement 2'.

The role of the reference clock. The reference clock plays an important role in the proof of Theorem 14. See the following counter-example without the reference clocks:

$$\frac{\emptyset \{(\underline{x}, 1)\} \{(\dot{x}, 0)\}}{\emptyset \{(\underline{x}, 0)\} \{(\dot{x}, 1)\}_{\mathbf{W}}} \xrightarrow{\text{time}} \frac{\emptyset \{(\dot{x}, 1)\} \{(\underline{x}, 1)\}}{\emptyset \{(\underline{x}, 0)\} \{(\dot{x}, 1)\}_{\mathbf{W}'}} \models \{\dot{x} \mapsto 1.4, \underline{x} \mapsto 1.9\}$$

$$\emptyset \{(\underline{x}, 0)\} \{(\dot{x}, 1)\}_{\mathbf{W}} \models \{\underline{x} \mapsto 0.3, \dot{x} \mapsto 1.6\}_{\mathbf{w}'}$$

There is no *well-formed* stack \mathbf{w} such that $\mathbf{w} \xrightarrow{\text{time}} \mathbf{w}'$ and $\alpha(\mathbf{w}) = \mathbf{W}$ because we lost the ordering information between $(\underline{x}, 0)$ and $(\dot{x}, 0)$ of \mathbf{W} in \mathbf{W}' . Put differently, the separating role of $(\dot{x}, 1)$ and $(\underline{x}, 1)$ is missed in \mathbf{W}' . However, if we insert the reference clocks into the head position, then the **time**-transition preserves the ordering information:

$$\frac{\{(\dot{\mathbb{C}}, 0)\} \{(\underline{x}, 1)\} \{(\dot{x}, 0)\}}{\{(\dot{\mathbb{C}}, 0)\} \{(\underline{x}, 0)\} \{(\dot{x}, 1)\}_{\mathbf{W}}} \xrightarrow{\text{time}} \frac{\emptyset \{(\dot{x}, 1)\} \{(\dot{\mathbb{C}}, 0)\} \{(\underline{x}, 1)\}}{\{(\dot{\mathbb{C}}, 0)\} \{(\underline{x}, 0)\} \{(\dot{x}, 1)\}_{\mathbf{W}'}}$$

Then it can be verified that if $\mathbf{w}' = \langle \nu, \mu \rangle \langle \mu', \lambda \rangle \models \mathbf{W}'$ and $\{\lambda(x)\} \geq \{\mu(x)\}$ then \mathbf{w}' is not well-formed. The following lemma is crucial in the proof of Theorem 14 and the reference clock is important to show this.

Lemma 15. Let $\theta, \vartheta, \eta \in \Lambda_{\Gamma \cup \dot{\Gamma}}$ be valuations such that $\alpha(\theta) \vdash^* \alpha(\vartheta) \vdash^* \alpha(\eta)$, $\theta \preceq \eta$, and $\vartheta \preceq \eta$. If $\theta(\dot{\mathbb{C}}) = 0$, then $\theta \preceq \vartheta$.

Solving Reachability Problem. We consider the reachability problem from q_{init} to q_{final} of SRTA. The problem can be solved by considering the corresponding problem in the digitized semantics as follows.

If there exists a computation from $\langle q_{\text{init}}, 0_{\Lambda} \rangle$ to $\langle q_{\text{final}}, \omega \rangle$, then there exists one from $\langle q_{\text{init}}, \langle 0_{\Lambda}, 0_{\Lambda} \rangle \rangle$ to $\langle q_{\text{final}}, \mathbf{w} \rangle$ with $\omega = \Phi(\mathbf{w})$ by Theorem 6. Then there exists one from $\langle q_{\text{init}}, \alpha(\langle 0_{\Lambda}, 0_{\Lambda} \rangle) \rangle$ to $\langle q_{\text{final}}, \mathbf{W} \rangle$ with $\alpha(\mathbf{w}) = \mathbf{W}$ by Theorem 14.

Conversely, if there exists a computation from $\langle q_{\text{init}}, \alpha(\langle 0_{\Lambda}, 0_{\Lambda} \rangle) \rangle$ to $\langle q_{\text{final}}, \mathbf{W}' \rangle$ for some stack \mathbf{W}' , then there exists one from $\langle q_{\text{init}}, \langle 0_{\Lambda}, 0_{\Lambda} \rangle \rangle$ to $\langle q_{\text{final}}, \mathbf{w}' \rangle$ with $\alpha(\mathbf{w}') = \mathbf{W}'$. Then there exists one from $\langle q_{\text{init}}, 0_{\Lambda} \rangle$ to $\langle q_{\text{final}}, \omega' \rangle$ with $\omega' = \Phi(\mathbf{w}')$. We used Theorem 14 and 6 in this order. The choice of 0_{Λ} is important because there is no $\langle \nu, \mu \rangle$ such that $\langle \nu, \mu \rangle \neq \langle 0_{\Lambda}, 0_{\Lambda} \rangle$ and $\langle \nu, \mu \rangle \models \alpha(\langle 0_{\Lambda}, 0_{\Lambda} \rangle)$.

Solving Reachability Problem of SRTA. Let $q_{\text{init}}, q_{\text{final}}$ be control locations of SRTA. The reachability problem from q_{init} to q_{final} can be solved as the above discussion. Since the digitized semantics $\mathcal{S}_{\text{digi}}$ can be implemented by a pushdown system and the reachability problem of pushdown systems is decidable [6, 9], we can solve the reachability problem.

6 Conclusion and Future Work

We have introduced the three semantics (collapsed, lazy, and digitized semantics) for our new model, SRTA, and established the correspondences among these semantics. This step-wise construction reveals the essence of regions considered by Abdulla et al. As a result, our proofs are directly derived from the basic correspondence between collapsed valuations and digital valuations. Since the digitized semantics is finite and can be considered as pushdown systems, the reachability problem of SRTA is decidable. As a corollary, the decidability of the reachability problem of TPDA is proved again in a simpler manner.

As future work, considering two extensions is natural, (i) Adopting the local time elapse transition rather than our **time** transition. Then, is the reachability problem still decidable? Are there any relations about expressiveness between this and ours? (ii) Can we add the diagonal constraints into synchronized recursive timed automata within the reachability problem is still decidable? In the theory of timed automata, it is well known that a timed automaton with diagonal constraints can be encoded into a diagonal-free timed automaton [3, 5]. We believe our new formulation is good clue to investigate these problems.

References

1. Abdulla, P., Atig, M., Stenman, J.: Dense-timed pushdown automata. In: LICS '12. pp. 35–44. IEEE (2012)
2. Abdulla, P., Čerāns, K., Jonsson, B., Tsay, Y.: Algorithmic analysis of programs with well quasi-ordered domains. *Inf. & Comp.* 160(1–2), 109–127 (2000)
3. Alur, R., Dill, D.: A theory of timed automata. *TCS.* 126(2), 183–235 (1994)
4. Benerecetti, M., Minopoli, S., Peron, A.: Analysis of Timed Recursive State Machines. In: TIME. pp. 61–68. IEEE (2010)
5. Bérard, B., Diekert, V., Gastin, P., Petit, A.: Characterization of the expressive power of silent transitions in timed automata. *Fundamenta Informaticae* 36(2), 145–182 (1998)
6. Bouajjani, A., Esparza, J., Maler, O.: Reachability analysis of pushdown automata: Application to model-checking. In: CONCUR '97, LNCS, vol. 1243, pp. 135–150. Springer (1997)
7. Cai, X., Ogawa, M.: Well-structured pushdown system: Case of dense timed pushdown automata. In: FLOPS '14, LNCS, vol. 8475, pp. 336–352. Springer (2014)
8. Finkel, A., Schnoebelen, P.: Well-structured transition systems everywhere! *TCS.* 256(1–2), 63–92 (2001)
9. Finkel, A., Willems, B., Wolper, P.: A direct symbolic approach to model checking pushdown systems. In: INFINITY '97, ENTCS, vol. 9, pp. 27–37. Elsevier (1997)
10. Trivedi, A., Wojtczak, D.: Recursive timed automata. In: ATVA '10, LNCS, vol. 6252, pp. 306–324. Springer (2010)