

1 Notations about Eviction Set

Let $ES = \{ES(0), \dots, ES(m-1)\}$ to denote an eviction set of m consecutive cache sets. Under our assumption that the attacker wants to monitor an entire page, $m = \frac{\text{page-size}}{\text{cache-line-size}}$. Each cache set in ES is composed of n cache lines: $ES(s) = \{ES(s, 0), \dots, ES(s, n-1)\}$. For a given l , all $ES(s, l)$ s are located in the same page. The warmup section is composed of the extra $m \times (n-a)$ cache lines, these accesses are used to manipulate the replacement state of the cache, making the final cache state after priming more predictable. For simplicity, we use the higher a lines ($ES(i, n-a), \dots, ES(i, n-1)$) to denote the occupation section and use upper lines ($ES(i, 0), \dots, ES(i, n-a-1)$) to denote warmup section.

2 Pseudo-code Algorithms

Algorithm 1 Occupancy Profiling for cache set s

```

1:  $ES \leftarrow$  Eviction set of size  $m \times n$ 
2: for all Cache line  $(s', l') \in ES$  do
3:   loop
4:     Flush  $ES$  from CPU cache.
5:     Prime set  $s$  by accessing all cache lines in  $ES(s)$ 
6:     Access cache line  $ES(s', l')$ . Determine hit or miss.
7:   end loop
8:   Compute hit rate of  $ES(s', l')$ .
9: end for
```

Algorithm 2 Replacement Policy Profiling for Cache Set s

```

1:  $ES \leftarrow$  Eviction set of size  $m \times n$ 
2: for all Cache line  $l \in ES(s)$  do
3:   loop
4:     Flush  $ES$  from CPU cache.
5:     Prime set  $s$  by accessing all cache lines in  $ES(s)$ 
6:     Access cache line  $ES(s, l)$ . Determine hit or miss.
7:     Flush  $ES$  from CPU cache.
8:     Prime set  $s$  by accessing all cache lines in  $ES(s)$ 
9:     Access memory  $m \notin ES$  that's also indexed to  $s'$ 
10:    Access cache line  $ES(s, l)$ . Determine hit or miss.
11:   end loop
12:   Compute miss rate increase of  $ES(s, l)$ .
13: end for
```

Algorithm 3 Probe Sequence Generation

```
1:  $ES \leftarrow$  Eviction set of size  $m \times n$ 
2:  $a \leftarrow$  Cache associativity.
3:  $seq_{probe} \leftarrow ()$ .
4: for all Cache set  $s \in ES$  do
5:   for all Cache line  $l \in \{n - a, \dots, n - 1\}$  do
6:     if Accessing  $seq_{probe}$  does not prefetch  $ES(s, l)$  then
7:        $seq_{probe} \vdash = ES(s, l)$ 
8:       break
9:     end if
10:  end for
11: end for
```

Algorithm 4 Prime Sequence Generation

```
1:  $ES \leftarrow$  Eviction set of size  $m \times n$ 
2:  $a \leftarrow$  Cache associativity.
3:  $seq_{probe} \leftarrow$  Probe sequence.
4:  $Occ \leftarrow$  Occupation matrix.
5:  $Rpl \leftarrow$  Replacement matrix.
6:  $seq_{prime} \leftarrow ()$ 
7: for all Cache set  $s \in ES$  do
8:   for  $0 \leq l < n$  do
9:     if  $Rpl(s, l) = 1$  then
10:       $seq_{prime} \vdash = ES(s, seq_{probe}(s))$ 
11:     else if  $Occ(s, l) = 1$  then
12:       $seq_{prime} \vdash =$  An unused cache line in  $ES(s, n - a), \dots, ES(s, n - 1)$ , except
         $ES(s, seq_{probe}(s))$ 
13:     else
14:       $seq_{prime} \vdash =$  An unused cache line in  $ES(s, 0), \dots, ES(s, n - a - 1)$ 
15:     end if
16:   end for
17: end for
```
