```
******* ili9341.h
#ifndef _ILI9341_H
#define _ILI9341_H
#include <stdint.h>
#include "pico/stdlib.h"
#include "hardware/spi.h"
typedef struct {
    spi_inst_t *port;
    uint pin_miso;
       uint pin_cs;
      uint pin_sck;
       uint pin_mosi;
       uint pin_reset;
      uint pin dc;
       uint pin_rd;
      uint pin_wr;
uint pin_d0;
       uint pin_d1;
      uint pin d2;
       uint pin_d3;
       uint pin_d4;
      uint pin_d5;
       uint pin_d6;
uint pin_d7;
} ili9341_config_t;
extern ili9341_config_t ili9341_config;
#define ILI9341_TFTWIDTH 240 ///< ILI9341 max TFT width #define ILI9341_TFTHEIGHT 320 ///< ILI9341 max TFT height
#define ILI9341_SLPIN 0x10 ///< Enter Sleep Mode #define ILI9341_SLPOUT 0x11 ///< Sleep Out #define ILI9341_PTLON 0x12 ///< Partial Mode ON #define ILI9341_NORON 0x13 ///< Normal Display Mode ON
#define ILI9341_RDMODE 0x0A ///< Read Display Power Mode
#define ILI9341_RDMADCTL 0x0B ///< Read Display MADCTL
#define ILI9341_RDPIXFMT 0x0C ///< Read Display Pixel Format
#define ILI9341_RDIMGFMT 0x0D ///< Read Display Image Format
#define ILI9341_RDSELFDIAG 0x0F ///< Read Display Self-Diagnostic Result
#define ILI9341_INVOFF 0x20 ///< Display Inversion OFF
#define ILI9341_INVOFF 0x20 /// Display Inversion OF #define ILI9341_INVOFF 0x26 /// Gamma Set #define ILI9341_DISPOFF 0x28 /// Display OFF #define ILI9341_DISPON 0x29 /// Display ON
#define ILI9341_CASET 0x2A ///< Column Address Set
#define ILI9341_PASET 0x2B ///< Page Address Set #define ILI9341_RAMWR 0x2C ///< Memory Write #define ILI9341_RAMRD 0x2E ///< Memory Read
#define ILI9341_PIXFMT 0x3A ///< COLMOD: Pixel Format Set
#define ILI9341_FRMCTR1 0xB1 ///< Frame Rate Control (In Normal Mode/Full Colors)
#define ILI9341_FRMCTR2 0xB2 ///< Frame Rate Control (In Idle Mode/8 colors)
#define ILI9341_FRMCTR3 0xB3 ///< Frame Rate control (In Partial Mode/Full Colors)
#define ILI9341_INVCTR 0xB4 ///< Display Inversion Control
#define ILI9341_DFUNCTR 0xB6 ///< Display Function Control
#define ILI9341_PWCTR1 0xC0 ///< Power Control 1
#define ILI9341_PWCTR2 0xC1 ///< Power Control 2
#define ILI9341_PWCTR3 0xC2 ///< Power Control 3
#define ILI9341_PWCTR4 0xC3 ///< Power Control 4
#define ILI9341_PWCTR5 0xC4 ///< Power Control 5 #define ILI9341_VMCTR1 0xC5 ///< VCOM Control 1 #define ILI9341_VMCTR2 0xC7 ///< VCOM Control 2
#define ILI9341_RDID1 0xDA ///< Read ID 1 #define ILI9341_RDID2 0xDB ///< Read ID 2 #define ILI9341_RDID3 0xDC ///< Read ID 3 #define ILI9341_RDID4 0xDD ///< Read ID 4
#define ILI9341_GMCTRP1 0xE0 ///< Positive Gamma Correction
#define ILI9341_GMCTRN1 0xE1 ///< Negative Gamma Correction
//#define ILI9341_PWCTR6 0xFC</pre>
extern const uint8 t font6x8[]:
void ili9341_init();
void iii9341_set_command(uint8_t cmd);
void iii9341_command_param(uint8_t data);
void ili9341_write_data(void *buffer, int bytes);
#endif
```

```
****** ili9341.c
#include <stdint.h>
#include <stdio.h>
#include <stdio.h>
#include <string.h>
#include "pico/stdlib.h"
#include "ili9341/ili9341.h"
ili9341_config_t ili9341_config = {
       .pin_cs = 0,
.pin_dc = 1,
       .pin_wr = 2,
.pin_rd = 3,
       .pin_reset = 4,
       .pin_d0 = 5
       .pin_d1 = 6,
.pin_d2 = 7,
        .pin_d3 = 8,
        .pin d4 = 9
        .pin_d5 = 10,
        .pin_d6 = 11,
       .pin_d7 = 12
static inline void pen_down();
static inline void pen_up();
int sio_write(const uint8_t *src, size_t len) {
    do {
              gpio_put_masked((0xff << 5), (*src << 5));</pre>
              pen_down();
              pen_up();
              len--:
              src++;
       } while (len > 0);
       return 0;
void init_pins() {
    gpio_init_mask(0x1fff);
    gpio_set_dir_out_masked(0x1fff);
    gpio_set_mask(0x1fff);
static inline void cs_select() {
    //asm volatile("nop \n nop \n nop");
    gpio_put(ili9341_config.pin_cs, 0);    // Active low
    //asm volatile("nop \n nop \n nop");
static inline void cs_deselect() {
   //asm volatile("nop \n nop \n nop");
   gpio_put(ili9341_config.pin_cs, 1);
   //asm volatile("nop \n nop \n nop");
}
static inline void pen_down() {
    //asm volatile("nop \n nop \n nop");
    gpio_put(ili9341_config.pin_wr, 0);    // writing
    //asm volatile("nop \n nop \n nop");
static inline void pen_up() {
    //asm volatile("nop \n nop \n nop");
    gpio_put(ili9341_config.pin_wr, 1); // not writing
    //asm volatile("nop \n nop \n nop");
void ili9341_set_command(uint8_t cmd) {
      cs_select();
gpio_put(ili9341_config.pin_dc, 0);
      spin_config.pin_dc, 0);
sio_write(&cmd, 1);
gpio_put(ili9341_config.pin_dc, 1);
cs_deselect();
void ili9341_command_param(uint8_t data) {
       cs_select();
       sio write(&data, 1);
      cs_deselect();
void ili9341_write_data(void *buffer, int bytes) {
      cs_select();
sio_write(buffer, bytes);
      cs_deselect();
void ili9341_init() {
      init_pins();
ili9341_set_command(0x01); //soft reset
sleep_ms(1000);
       ili9341 set command(ILI9341 GAMMASET);
       ili9341_command_param(0x01);
       // positive gamma correction
```

```
ili9341_set_command(ILI9341_GMCTRP1);
ili9341_write_data((uint8_t[15]){ 0x0f, 0x31, 0x2b, 0x0c, 0x0e, 0x08, 0x4e, 0xf1, 0x37, 0x07, 0x10, 0x03, 0x0e, 0x09, 0x00 }, 15);
      // negative gamma correction ili9341_set_command(ILI9341_GMCTRN1); ili9341_write_data((uint8_t[15]){ 0x00, 0x0e, 0x14, 0x03, 0x11, 0x07, 0x31, 0xc1, 0x48, 0x08, 0x0f, 0x0c, 0x31, 0x36,
0x0f }, 15);
      // memory access control
ili9341_set_command(ILI9341_MADCTL);
ili9341_command_param(0x48);
      // pixel format
ili9341_set_command(ILI9341_PIXFMT);
      ili9341_command_param(0x55); // 16-bit
      // frame rate; default, 70 Hz
ili9341_set_command(ILI9341_FRMCTR1);
ili9341_command_param(0x00);
ili9341_command_param(0x1B);
      // exit sleep
ili9341_set_command(ILI9341_SLPOUT);
          display on
      ili9341_set_command(ILI9341_DISPON);
          column address set
      ili9341_set_command(ILI9341_CASET);
      ili9341_command_param(0x00);
ili9341_command_param(0x00); // start column
ili9341_command_param(0x00);
ili9341_command_param(0x00);
ili9341_command_param(0xef); // end column -> 239
       // page address set
      ili9341_set_command(ILI9341_PASET);
ili9341_command_param(0x00);
      ili9341_command param(0x00);  // start page
ili9341_command_param(0x01);
ili9341_command_param(0x3f);  // end page -> 319
      ili9341_set_command(ILI9341_RAMWR);
}
uint16_t swap_bytes(uint16_t color) {
      return (color>>8) | (color<<8);
****** mode0_demo.c
#include "pico/stdlib.h"
#include "mode0/mode0.h"
int main() {
    mode0_init();
     mode0_set_cursor(0, 0);
mode0_color_t fg = MODE0_WHITE;
mode0_color_t bg = MODE0_BLACK;
      while (1) {
   mode0_print("Retro Computer (c) 2021, Shawn Hyam\n");
            sleep_ms(500);
            fg = (fg+1) % 16;
if (fg == 0) {
bg = (bg+1) % 16;
                  mode0_set_background(bg);
            mode0_set_foreground(fg);
     }
***** mode0.h
#ifndef _TEXT_MODE_H
#define _TEXT_MODE_H
// ARNE-16 palette converted to RGB565 -- https://lospec.com/palette-list/arne-16
typedef enum {
    MODE0_BLACK,
    MODE0_BROWN,
      MODE0_RED,
MODE0_BLUSH,
MODE0_GRAY,
      MODE0_DESERT,
MODE0_ORANGE,
MODE0_YELLOW,
      MODEO WHITE,
      MODEO MIDNIGHT,
      MODEO_DARK_SLATE_GRAY,
      MODEO_GREEN,
MODEO YELLOW GREEN,
      MODEO_BLUE,
      MODE0_PICTON_BLUE,
MODE0_PALE_BLUE
```

```
} mode0_color_t;
void mode0_init();
void mode0_clear(mode0_color_t color);
void modeO_draw_screen();
void modeO_draw_region(uint8_t x, uint8_t y, uint8_t width, uint8_t height);
void mode0_scroll_vertical(int8_t amount);
void mode0_set_foreground(mode0_color_t color);
void mode0_set_background(mode0_color_t color);
 void mode0_set_cursor(uint8_t x, uint8_t y);
uint8_t mode0_get_cursor_x();
uint8_t mode0_get_cursor_y();
void mode0_print(const char *s);
void mode0_write(const char *s, int len);
void mode0_putc(char c);
void mode0_show_cursor();
void mode0_hide_cursor();
// Won't redraw until the matching <code>_end</code> is invoked. {\bf void\ mode0\_begin()} ;
 void mode0_end();
 #endif
 ***** mode0.c
 #include "pico/stdlib.h"
 #include <string.h>
#include "hardware/spi.h"
#include "ili9341/ili9341.h"
 #include "mode0/mode0.h"
 /* Character graphics mode */
 // Characters are 8x12 -- characters start at (x:1,y:1) and are 5x7 in size, so // it is possible to not display the full area. This display mode actually treats
        them as 6x10, starting at (x:1,y:0)
tic const uint8 t font_data[95][12] = {
   (0x00, 0x00, 0x00
        ...bla... { 0x00, 0x28, 0x50, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00 }
#define TEXT_HEIGHT 24
#define TEXT WIDTH 53
 #define SWAP_BYTES(color) ((uint16_t)(color>>8) | (uint16_t)(color<<8))</pre>
static mode0_color_t screen_bg_color = MODE0_BLACK;
static mode0_color_t screen_fg_color = MODE0_WHITE; // TODO need to store a color per cell
 static int cursor_x = 0;
 static int cursor_y = 0;
static int cursory - 0,
static uint8_t screen[TEXT_HEIGHT * TEXT_WIDTH] = { 0 };
static uint8_t colors[TEXT_HEIGHT * TEXT_WIDTH] = { 0 };
 static uint8_t show_cursor = 0;
 static int depth = 0;
static uint16_t palette[16] = {
    SWAP_BYTES(0x0000),
            SWAP_BYTES (0x49E5),
           SWAP_BYTES(0xB926),
SWAP_BYTES(0xE371),
            SWAP_BYTES (0x9CF3),
           SWAP_BYTES (0xA324),
SWAP_BYTES (0xEC46),
            SWAP_BYTES (0xF70D),
           SWAP_BYTES (0xffff),
SWAP_BYTES (0x1926),
            SWAP_BYTES (0x2A49),
           SWAP_BYTES (0x4443),
SWAP_BYTES (0xA664),
            SWAP_BYTES (0x02B0),
           SWAP_BYTES (0x351E)
SWAP_BYTES (0xB6FD)
void mode0_clear(mode0_color_t color) {
          mode0_begin();
int size = TEXT_WIDTH*TEXT_HEIGHT;
memset(screen, 0, size);
memset(colors, color, size);
mode0_set_cursor(0, 0);
mode0_end();
 void mode0_set_foreground(mode0_color_t color) {
          mode0_begin();
screen_fg_color = color;
mode0_end();
}
 void mode0_set_background(mode0_color_t color) {
           mode0_begin();
           screen_bg_color = color;
mode0_end();
void mode0_set_cursor(uint8_t x, uint8_t y) {
           cursor_x = x;
cursor_y = y;
```

```
void mode0 show cursor() {
     mode0_begin();
     show_cursor = 1;
mode0_end();
void mode0_hide_cursor() {
     mode0_begin();
show_cursor = 0;
mode0_end();
uint8_t mode0_get_cursor_x() {
     return cursor_x;
uint8_t mode0_get_cursor_y() {
     return cursor y;
void mode0_putc(char c) {
      mode0_begin();
      if (cursor_y >= TEXT_HEIGHT) {
   mode0_scroll_vertical(cursor_y-TEXT_HEIGHT+1);
   cursor_y = TEXT_HEIGHT-1;
     int idx = cursor_y*TEXT_WIDTH + cursor_x;
if (c == '\n') {
    // fill the rest of the line with empty content + the current bg color
    memset(screen+idx, 0, TEXT_WIDTH-cursor_x);
    memset(colors+idx, screen_bg_color, TEXT_WIDTH-cursor_x);
    restaurant to the colors of the cursor to the current bg color.
      cursor_y++;

cursor_x = 0;

} else if (c == '\r') {

//cursor_x = 0;

} else if (c>=32 && c<=127) {
            see in (c>-12/) && c-12/) {
screen[idx] = c-32;
colors[idx] = ((screen_fg_color & 0xf) << 4) | (screen_bg_color & 0xf);</pre>
            cursor x++;
            if (cursor_x >= TEXT_WIDTH) {
    cursor_x = 0;
                  cursor_y++;
     }
     mode0_end();
}
void mode0_print(const char *str) {
     mode0_begin();
     char c;
while (c = *str++) {
           mode0_putc(c);
     mode0_end();
void mode0_write(const char *str, int len) {
     mode0_begin();
for (int i=0; i<len; i++) {
    mode0_putc(*str++);</pre>
      mode0_end();
}
inline void mode0_begin() {
    depth++;
inline void mode0_end() {
      if (--depth == 0) {
            mode0_draw_screen();
void mode0_draw_region(uint8_t x, uint8_t y, uint8_t width, uint8_t height) {
     mode0_draw_screen();
void mode0_draw_screen() {
      // assert depth == 0?
      depth = 0;
      // setup to draw the whole screen
          column address set
      ili9341_set_command(ILI9341_CASET);
      ili9341_command_param(0x00);
ili9341_command_param(0x00); // start column
ili9341_command_param(0x00);
      ili9341_command_param(0xef); // end column -> 239
      // page address set
ili9341_set_command(ILI9341_PASET);
ili9341_command_param(0x00);
```

```
ili9341_command_param(0x00); // start page
      ili9341_command_param(0x01);
ili9341_command_param(0x3f); // end page -> 319
      // start writing
ili9341_set_command(ILI9341_RAMWR);
      uint16_t buffer[6*240]; // 'amount' pixels wide, 240 pixels tall
      int screen_idx = 0;
for (int x=0; x<TEXT_WIDTH; x++) {
    // create one column of screen information</pre>
            uint16_t *buffer_idx = buffer;
            for (int bit=0; bit<6; bit++) {</pre>
                  uint5_t mask = 64>>bit;
for (int y=TEXT_HEIGHT-1; y>=0; y--) {
    uint8_t character = screen[y*53+x];
    uint16_t fg_color = palette[colors[y*53+x] >> 4];
    uint16_t bg_color = palette[colors[y*53+x] & 0xf];
                        if (show_cursor && (cursor_x == x) && (cursor_y == y)) {
   bg_color = MODEO_GREEN;
                         const uint8_t* pixel_data = font_data[character];
                         // draw the character into the buffer
                         // draw the character into the bursel
for (int j=10; j>=1; j--) {
    *buffer_idx++ = (pixel_data[j] & mask) ? fg_color : bg_color;
                  }
            }
             // now send the slice
            ili9341_write_data(buffer, 6*240*2);
      uint16_t extra_buffer[2*240] = { 0 };
ili9341_write_data(extra_buffer, 2*240*2);
}
void mode0_scroll_vertical(int8_t amount) {
    mode0_begin();
      if (amount > 0) {
  int size1 = TEXT_WIDTH*amount;
  int size2 = TEXT_WIDTH*TEXT_HEIGHT - size1;
            memmove(screen, screen+size1, size2);
memmove(colors, colors+size1, size2);
memset(screen+size2, 0, size1);
      memset(colors+size2, screen_bg_color, size1);
} else if (amount < 0) {
   amount = -amount;</pre>
            int size1 = TEXT_WIDTH*amount;
int size2 = TEXT_WIDTH*TEXT_HEIGHT - size1;
            memmove(screen+size1, screen, size2);
memmove(colors+size1, colors, size2);
memset(screen, 0, size1);
            memset(colors, screen_bg_color, size1);
      }
      mode0_end();
}
void mode0_init() {
    stdio_init_all();
      ili9341_init();
****** CMakeLists.txt
cmake_minimum_required(VERSION 3.22)
include(/pico/pico-sdk/external/pico_sdk_import.cmake)
set(CMAKE_C_STANDARD 11)
set(CMAKE_CXX_STANDARD 17)
set(PROJ_NAME fish)
project(${PROJ_NAME} C CXX ASM)
pico sdk init()
add_executable(${PROJ_NAME}
      mode0_demo.c
add_library(ili9341
ili9341.c
target_link_libraries(ili9341 pico_stdlib hardware_spi)
target_include_directories(ili9341 PUBLIC include)
```

```
add_library(mode0
    mode0.c
)
target_link_libraries(mode0
    ili9341 pico_stdlib hardware_spi)
target_include_directories(mode0 PUBLIC include)

target_link_libraries(${PROJ_NAME}
    mode0)

pico_enable_stdio_usb(${PROJ_NAME} 1)
pico_add_extra_outputs(${PROJ_NAME})
```