

Low Dropout Voltage Regulators

The LM2931 series consists of positive fixed and adjustable output voltage regulators that are specifically designed to maintain proper regulation with an extremely low input–to–output voltage differential. These devices are capable of supplying output currents in excess of 100 mA and feature a low bias current of 0.4 mA at 10 mA output.

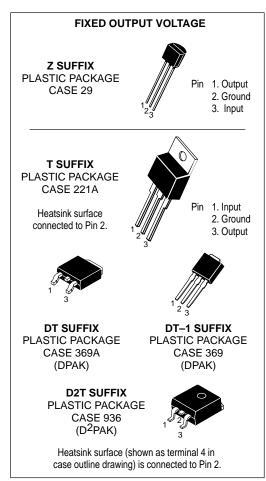
Designed primarily to survive in the harsh automotive environment, these devices will protect all external load circuitry from input fault conditions caused by reverse battery connection, two battery jump starts, and excessive line transients during load dump. This series also includes internal current limiting, thermal shutdown, and additionally, is able to withstand temporary power—up with mirror—image insertion.

Due to the low dropout voltage and bias current specifications, the LM2931 series is ideally suited for battery powered industrial and consumer equipment where an extension of useful battery life is desirable. The 'C' suffix adjustable output regulators feature an output inhibit pin which is extremely useful in microprocessor–based systems.

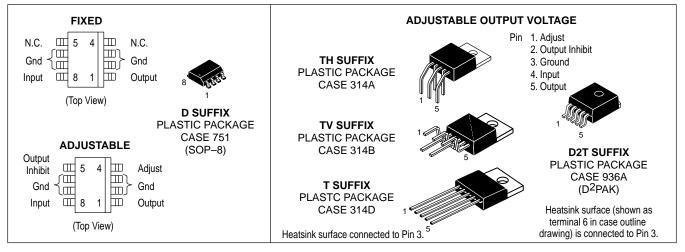
- Input-to-Output Voltage Differential of < 0.6 V @ 100 mA
- Output Current in Excess of 100 mA
- Low Bias Current
- 60 V Load Dump Protection
- −50 V Reverse Transient Protection
- Internal Current Limiting with Thermal Shutdown
- Temporary Mirror-Image Protection
- Ideally Suited for Battery Powered Equipment
- Economical 5-Lead TO-220 Package with Two Optional Leadforms
- Available in Surface Mount SOP–8, D²PAK and DPAK Packages
- High Accuracy (±2%) Reference (LM2931AC) Available

LM2931 Series

LOW DROPOUT VOLTAGE REGULATORS



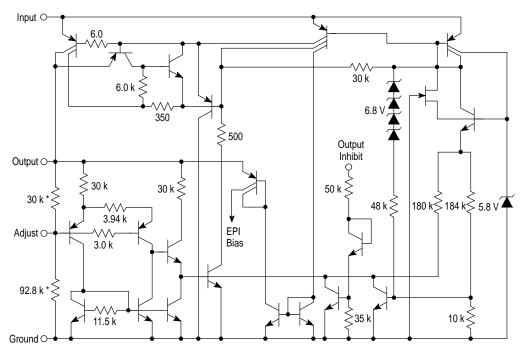
(See Following Page for Ordering Information.)



ORDERING INFORMATION

	Ou	tput		
Device	Voltage	Tolerance	Case	Package
LM2931AD-5.0			751	SOP-8 Surface Mount
LM2931ADT-5.0			369A	Surface Mount DPAK
LM2931ADT-1-5.0		±3.96/%	369	DPAK
LM2931AD2T-5.0		±3.6%	936	Surface Mount D ² PAK
LM2931AT-5.0			221A	TO-220 Type
LM2931AZ-5.0	5.0 V		29	TO-92 Type
LM2931D-5.0	5.0 V		751	SOP-8 Surface Mount
LM2931D2T-5.0			936	Surface Mount D ² PAK
LM2931DT-5.0			369A	Surface Mount DPAK
LM2931DT-1-5.0			369	DPAK
LM2931T-5.0			221A	TO-220 Type
LM2931Z-5.0		±5.0%	29	TO-92 Type
LM2931CD			751	SOP-8 Surface Mount
LM2931CD2T			936A	Surface Mount D ² PAK
LM2931CT	Adjustable		314D	5-Pin TO-220 Type
LM2931CTH			314A	5-Pin Horizontal Leadform
LM2931CTV			314B	5-Pin Vertical Leadform
LM2931ACD			751	SOP-8 Surface Mount
LM2931ACD2T			936A	Surface Mount D ² PAK
LM2931ACT	Adjustable	±2.0%	314D	5-Pin TO-220 Type
LM2931ACTH			314A	5-Pin Horizontal Leadform
LM2931ACTV			314B	5-Pin Vertical Leadform

Representative Schematic Diagram



^{*}Deleted on Adjustable Regulators

This device contains 26 active transistors.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage Continuous	VI	40	Vdc
Transient Input Voltage (τ ≤ 100 ms)	V _I (τ)	60	Vpk
Transient Reverse Polarity Input Voltage 1.0% Duty Cycle, τ ≤ 100 ms	-V _I (τ)	-50	Vpk
Power Dissipation Case 29 (TO–92 Type) T _A = 25°C Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case	PD R ₀ JA R ₀ JC	Internally Limited 178 83	W °C/W °C/W
Case 221A, 314A, 314B and 314D (TO–220 Type) T _A = 25°C Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case Case 369 and 369A (DPAK) [Note 1]	P _D R _{θJA} R _{θJC}	Internally Limited 65 5.0	W °C/W °C/W
T _A = 25°C Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case Case 751 (SOP–8) [Note 2]	P _D R _θ JA R _θ JC	Internally Limited 92 6.0	°C/W
T _A = 25°C Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case Case 936 and 936A (D ² PAK) [Note 3]	P _D R _θ JA R _θ JC	Internally Limited 160 25	°C/W
T _A = 25°C Thermal Resistance, Junction–to–Ambient Thermal Resistance, Junction–to–Case	P _D R _{θJA} R _{θJC}	Internally Limited 70 5.0	°C/W
Tested Operating Junction Temperature Range Storage Temperature Range	T _J	-40 to +125 -65 to +150	°C

NOTES: 1. DPAK Junction–to–Ambient Thermal Resistance is for vertical mounting. Refer to Figure 24 for board mounted Thermal Resistance.

- 2. SOP–8 Junction–to–Ambient Thermal Resistance is for minimum recommended pad size. Refer to Figure 23 for Thermal Resistance variation versus pad size.
- D²PAK Junction—to—Ambient Thermal Resistance is for vertical mounting. Refer to Figure 25 for board mounted Thermal Resistance.
- 4. ESD data available upon request.

ELECTRICAL CHARACTERISTICS (V_{in} = 14 V, I_O = 10 mA, C_O = 100 μ F, $C_{O(ESR)}$ = 0.3 Ω , T_J = 25°C [Note 1].)

		LM2931-5.0		LM2931A-5.0				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
FIXED OUTPUT	•	•				•		
Output Voltage V_{in} = 14 V, I_{O} = 10 mA, T_{J} = 25°C V_{in} = 6.0 V to 26 V, I_{O} ≤ 100 mA, T_{J} = -40° to +125°C	VO	4.75 4.50	5.0 –	5.25 5.50	4.81 4.75	5.0 –	5.19 5.25	V
Line Regulation $V_{in} = 9.0 \text{ V to } 16 \text{ V}$ $V_{in} = 6.0 \text{ V to } 26 \text{ V}$	Reg _{line}	- -	2.0 4.0	10 30	- -	2.0 4.0	10 30	mV
Load Regulation (I _O = 5.0 mA to 100 mA)	Reg _{load}	-	14	50	-	14	50	mV
Output Impedance $I_O = 10$ mA, $\Delta I_O = 1.0$ mA, $f = 100$ Hz to 10 kHz	ZO	-	200	_	-	200	-	mΩ
Bias Current $V_{in} = 14 \text{ V}, I_O = 100 \text{ mA}, T_J = 25^{\circ}\text{C}$ $V_{in} = 6.0 \text{ V}$ to 26 V, $I_O = 10 \text{ mA}, T_J = -40^{\circ}$ to +125°C	IB	- -	5.8 0.4	30 1.0	_ _	5.8 0.4	30 1.0	mA
Output Noise Voltage (f = 10 Hz to 100 kHz)	V _n	-	700	-	-	700	-	μVrms
Long Term Stability	S	-	20	_	_	20	-	mV/kHR

NOTES: 1. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible.

2. The reference voltage on the adjustable device is measured from the output to the adjust pin across R₁.

ELECTRICAL CHARACTERISTICS (V_{in} = 14 V, I_O = 10 mA, C_O = 100 μ F, $C_{O(ESR)}$ = 0.3 Ω , T_J = 25°C [Note 1].)

		LM2931-5.0		LM2931A-5.0				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
FIXED OUTPUT								
Ripple Rejection (f = 120 Hz)	RR	60	90	_	60	90	_	dB
Dropout Voltage IO = 10 mA IO = 100 mA	VI-VO	- -	0.015 0.16	0.2 0.6	- -	0.015 0.16	0.2 0.6	V
Over-Voltage Shutdown Threshold	V _{th} (OV)	26	29.5	40	26	29.5	40	V
Output Voltage with Reverse Polarity Input (V _{in} = -15 V)	-Vo	-0.3	0	-	-0.3	0	_	V

ELECTRICAL CHARACTERISTICS (V_{in} = 14 V, I_O = 10 mA, C_O = 100 μ F, $C_{O(ESR)}$ = 0.3 Ω , T_J = 25°C [Note 1].)

			LM2931C	;	L	M2931A	;	
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Тур	Unit
ADJUSTABLE OUTPUT	•				•	•		
Reference Voltage (Note 2, Figure 18) $I_O = 10$ mA, $T_J = 25^{\circ}C$ $I_O \le 100$ mA, $T_J = -40$ to $+125^{\circ}C$	V _{ref}	1.14 1.08	1.20 _	1.26 1.32	1.17 1.15	1.20 _	1.23 1.25	V
Output Voltage Range	VOrange	3.0 to 24	2.7 to 29.5	-	3.0 to 24	2.7 to 29.5	-	V
Line Regulation (V _{in} = V _O + 0.6 V to 26 V)	Regline	-	0.2	1.5	-	0.2	1.5	mV/V
Load Regulation (I _O = 5.0 mA to 100 mA)	Reg _{load}	-	0.3	1.0	_	0.3	1.0	%/V
Output Impedance $I_O = 10$ mA, $\Delta I_O = 1.0$ mA, $f = 10$ Hz to 10 kHz	ZO	-	40	-	-	40	-	mΩ/V
Bias Current $I_O = 100 \text{ mA}$ $I_O = 10 \text{ mA}$ Output Inhibited ($V_{th(OI)} = 2.5 \text{ V}$)	IB	- - -	6.0 0.4 0.2	- 1.0 1.0	- - -	6.0 0.4 0.2	- 1.0 1.0	mA
Adjustment Pin Current	l _{Adj}	_	0.2	-	_	0.2	-	μА
Output Noise Voltage (f = 10 Hz to 100 kHz)	Vn	-	140	-	_	140	-	μVrms/V
Long-Term Stability	S	_	0.4	-	_	0.4	-	%/kHR
Ripple Rejection (f = 120 Hz)	RR	0.10	0.003	-	0.10	0.003	-	%/V
Dropout Voltage IO = 10 mA IO = 100 mA	VI-VO	- -	0.015 0.16	0.2 0.6	_ _	0.015 0.16	0.2 0.6	V
Over-Voltage Shutdown Threshold	V _{th} (OV)	26	29.5	40	26	29.5	40	V
Output Voltage with Reverse Polarity Input ($V_{in} = -15 \text{ V}$)	-VO	-0.3	0	-	-0.3	0	-	V
Output Inhibit Threshold Voltages Output "On": $T_J = 25^{\circ}C$ $T_J = -40^{\circ}$ to +125°C Output "Off": $T_J = 25^{\circ}C$ $T_J = -40^{\circ}$ to +125°C	V _{th(OI)}	- - 2.50 3.25	2.15 - 2.26 -	1.90 1.20 –	- - 2.50 3.25	2.15 - 2.26 -	1.90 1.20 –	V
Output Inhibit Threshold Current (V _{th(OI)} = 2.5 V)	I _{th(OI)}	-	30	50	-	30	50	μΑ

NOTES: 1. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible. 2. The reference voltage on the adjustable device is measured from the output to the adjust pin across R₁.

Figure 1. Dropout Voltage versus Output Current

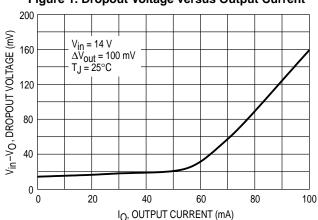


Figure 2. Dropout Voltage versus

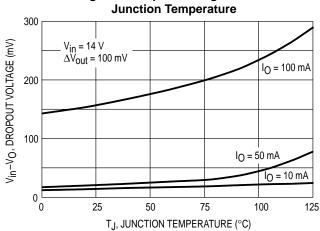


Figure 3. Peak Output Current versus Input Voltage

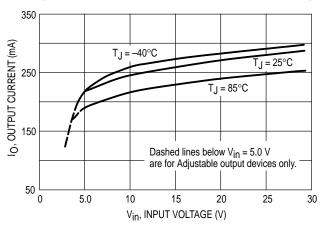


Figure 4. Output Voltage versus Input Voltage

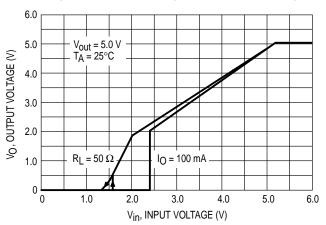


Figure 5. Output Voltage versus Input Voltage

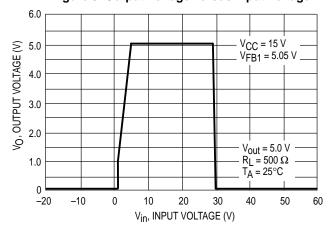


Figure 6. Load Dump Characteristics

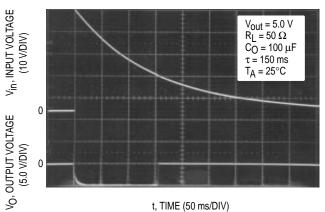


Figure 7. Bias Current versus Input Voltage 12 $V_{out} = 5.0 \text{ V}$ T_J = 25°C 10 IB, BIAS CURRENT (mA) $R_L = 50 \Omega$ 6.0 4.0 $R_L = 100 \Omega$ 2.0 $R_I = 500 \Omega$ 0 0 -20 -10 10 20 30 40 50 60 Vin, INPUT VOLTAGE (V)

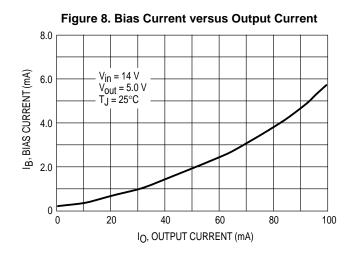
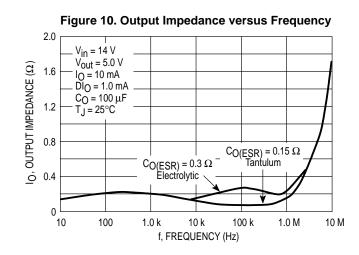
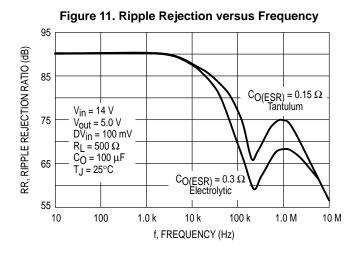
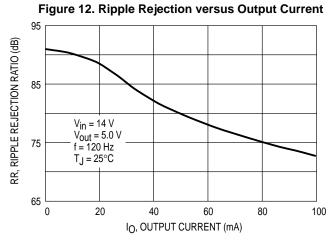
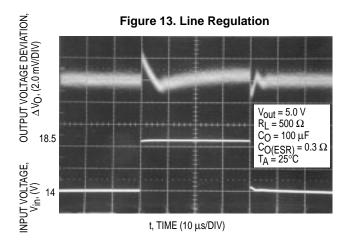


Figure 9. Bias Current versus Junction Temperature 8.0 $V_{in} = 14 V$ $V_{out} = 5.0 V$ $I_0 = 100 \text{ mA}$ BIAS CURRENT (mA) 6.0 4.0 $I_0 = 50 \text{ mA}$ <u>å</u> 2.0 $I_O = 0 \text{ mA}$ 0 -25 100 -55 25 50 75 125 T_J, JUNCTION TEMPERATURE (°C)









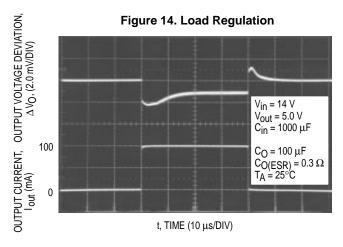


Figure 15. Reference Voltage versus Output Voltage

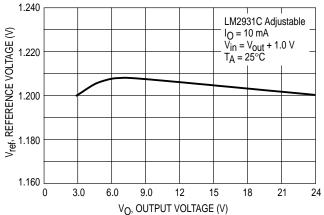
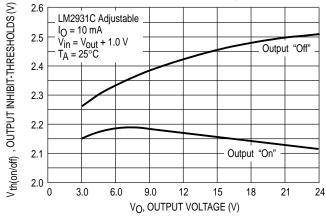


Figure 16. Output Inhibit-Thresholds versus Output Voltage



APPLICATIONS INFORMATION

The LM2931 series regulators are designed with many protection features making them essentially blow-out proof. These features include internal current limiting, thermal shutdown, overvoltage and reverse polarity input protection, and the capability to withstand temporary power-up with mirror-image insertion. Typical application circuits for the fixed and adjustable output device are shown in Figures 17 and 18

The input bypass capacitor C_{in} is recommended if the regulator is located an appreciable distance ($\geq 4''$) from the supply input filter. This will reduce the circuit's sensitivity to the input line impedance at high frequencies.

This regulator series is not internally compensated and thus requires an external output capacitor for stability. The capacitance value required is dependent upon the load current, output voltage for the adjustable regulator, and the type of capacitor selected. The least stable condition is encountered at maximum load current and minimum output voltage. Figure 22 shows that for operation in the "Stable" region, under the conditions specified, the magnitude of the output capacitor impedance $|Z_{\mbox{\scriptsize O}}|$ must not exceed 0.4 Ω . This limit must be observed over the entire operating temperature range of the regulator circuit.

With economical electrolytic capacitors, cold temperature operation can pose a serious stability problem. As the electrolyte freezes, around -30°C , the capacitance will decrease and the equivalent series resistance (ESR) will increase drastically, causing the circuit to oscillate. Quality electrolytic capacitors with extended temperature ranges of -40° to +85°C and -55° to +105°C are readily available. Solid tantalum capacitors may be a better choice if small size is a requirement, however, the maximum $|Z_{\rm O}|$ limit over temperature must be observed.

Note that in the stable region, the output noise voltage is linearly proportional to $\left\lceil Z_O \right\rceil$. In effect, C_O dictates the high frequency roll–off point of the circuit. Operation in the area titled "Marginally Stable" will cause the output of the regulator to exhibit random bursts of oscillation that decay in an under–damped fashion. Continuous oscillation occurs when operating in the area titled "Unstable". It is suggested that oven testing of the entire circuit be performed with maximum load, minimum input voltage, and minimum ambient temperature.

Figure 17. Fixed Output Regulator

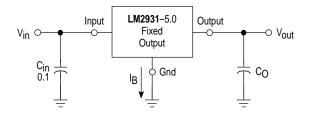
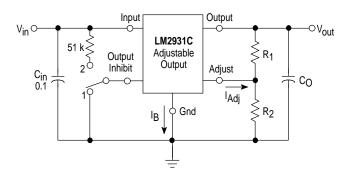


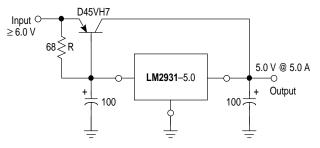
Figure 18. Adjustable Output Regulator



Switch Position 1 = Output "On", 2 = Output "Off"

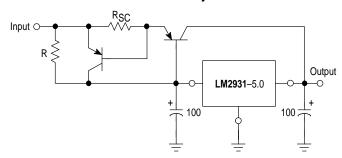
$$V_{out} = V_{ref} \left(1 + \frac{R_2}{R_1} \right) + I_{Adj} R_2$$
 22.5 k $\geq \frac{R_1 R_2}{R_1 + R_2}$

Figure 19. (5.0 A) Low Differential Voltage Regulator



The LM2931 series can be current boosted with a PNP transistor. The D45VH7, on a heatsink, will provide an output current of 5.0 A with an input to output voltage differential of approximately 1.0 V. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting. This circuit is not short circuit proof.

Figure 20. Current Boost Regulator with Short Circuit Projection



The circuit of Figure 19 can be modified to provide supply protection against short circuits by adding the current sense resistor R_{SC} and an additional PNP transistor. The current sensing PNP must be capable of handling the short circuit current of the LM2931. Safe operating area of both transistors must be considered under worst case conditions.

Figure 21. Constant Intensity Lamp Flasher

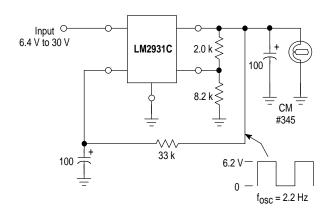


Figure 22. Output Noise Voltage versus Output Capacitor Impedance

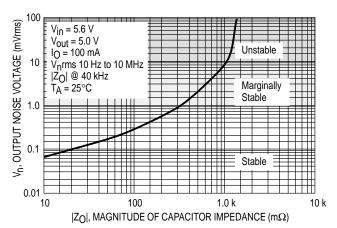


Figure 23. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

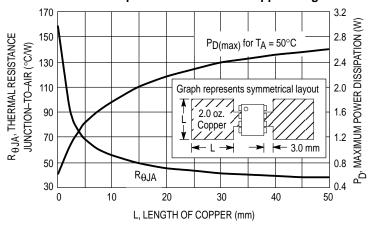


Figure 24. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

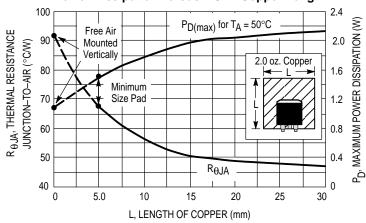
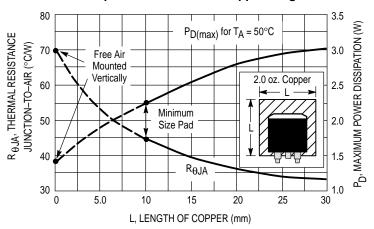


Figure 25. 3–Pin and 5–Pin D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length



LM2931 Series DEFINITIONS

Dropout Voltage – The input/output voltage differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output decreases 100 mV from nominal value at 14 V input, dropout voltage is affected by junction temperature and load current.

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Bias Current – That part of the input current that is not delivered to the load.

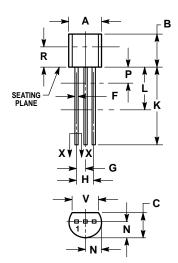
Output Noise Voltage – The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

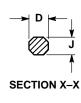
Long-Term Stabliity – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices electrical characteristics and maximum power dissipation.

OUTLINE DIMENSIONS

Z SUFFIX

PLASTIC PACKAGE CASE 29-04 (TO-92 Type) ISSUE AD





NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

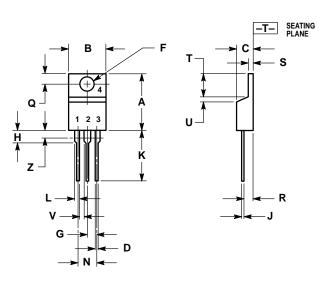
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION TO APPLY BETWEEN LAND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	INCHES		IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	-	12.70	-
L	0.250	-	6.35	-
N	0.080	0.105	2.04	2.66
Р	_	0.100		2.54
R	0.115	_	2.93	_
٧	0.135	-	3.43	-



PLASTIC PACKAGE CASE 221A-06 (TO-220 Type) ISSUE Y



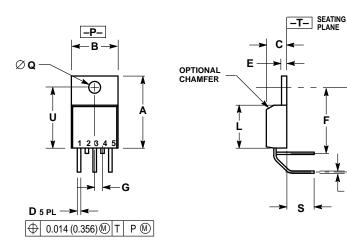
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
 3. DIM Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045	_	1.15	_
Z	_	0.080	_	2.04

OUTLINE DIMENSIONS





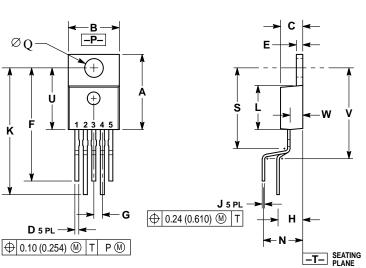
NOTES:

- OTES.
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION D DOES NOT INCLUDE INTERCONNECT BAR (DAMBAR) PROTRUSION.
 DIMENSION D INCLUDING PROTRUSION SHALL
 NOT EXCEED 0.043 (1.092) MAXIMUM.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.572	0.613	14.529	15.570
В	0.390	0.415	9.906	10.541
С	0.170	0.180	4.318	4.572
D	0.025	0.038	0.635	0.965
Е	0.048	0.055	1.219	1.397
F	0.570	0.585	14.478	14.859
G	0.067	' BSC	1.702	BSC
J	0.015	0.025	0.381	0.635
K	0.730	0.745	18.542	18.923
L	0.320	0.365	8.128	9.271
Q	0.140	0.153	3.556	3.886
S	0.210	0.260	5.334	6.604
U	0.468	0.505	11.888	12.827



PLASTIC PACKAGE CASE 314B-05 (TO-220 Type) ISSUE J

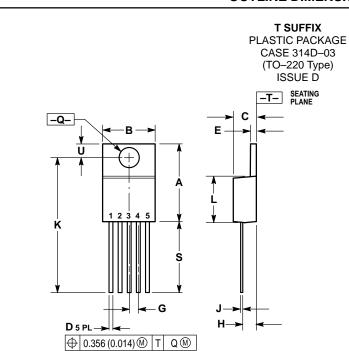


NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
- DIMENSION D DOES NOT INCLUDE INTERCONNECT BAR
 (DAMBAR) PROTRUSION. DIMENSION D INCLUDING
 PROTRUSION SHALL NOT EXCEED 0.043 (1.092) MAXIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.572	0.613	14.529	15.570	
В	0.390	0.415	9.906	10.541	
С	0.170	0.180	4.318	4.572	
D	0.025	0.038	0.635	0.965	
E	0.048	0.055	1.219	1.397	
F	0.850	0.935	21.590	23.749	
G	0.067	BSC	1.702	2 BSC	
Н	0.166	BSC	4.216	SBSC	
J	0.015	0.025	0.381	0.635	
K	0.900	1.100	22.860	27.940	
L	0.320	0.365	8.128	9.271	
N	0.320	BSC	8.128	BSC 388	
Q	0.140	0.153	3.556	3.886	
S	-	0.620	-	15.748	
U	0.468	0.505	11.888	12.827	
V	-	0.735	-	18.669	
W	0.090	0.110	2.286	2.794	

OUTLINE DIMENSIONS

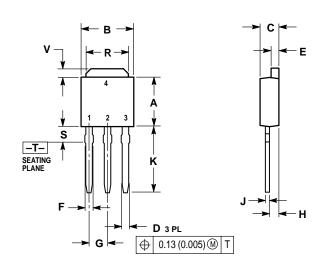


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION D DOES NOT INCLUDE INTERCONNECT BAR (DAMBAR) PROTRUSION. DIMENSION D INCLUDING PROTRUSION SHALL NOT EXCEED 10.92 (0.043) MAXIMUM.

	INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.572	0.613	14.529	15.570
В	0.390	0.415	9.906	10.541
С	0.170	0.180	4.318	4.572
D	0.025	0.038	0.635	0.965
Е	0.048	0.055	1.219	1.397
G	0.067	BSC	1.702 BSC	
Н	0.087	0.112	2.210	2.845
J	0.015	0.025	0.381	0.635
K	1.020	1.065	25.908	27.051
L	0.320	0.365	8.128	9.271
Q	0.140	0.153	3.556	3.886
U	0.105	0.117	2.667	2.972
S	0.543	0.582	13.792	14.783



PLASTIC PACKAGE CASE 369-07 (DPAK) ISSUE L

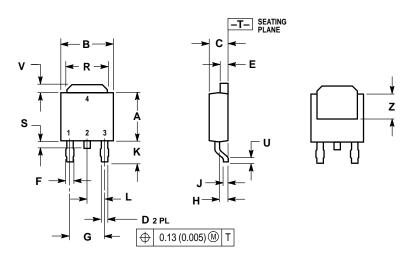


- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.250	5.97	6.35
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.090	BSC	2.29	BSC
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.175	0.215	4.45	5.46
S	0.050	0.090	1.27	2.28
٧	0.030	0.050	0.77	1.27

OUTLINE DIMENSIONS

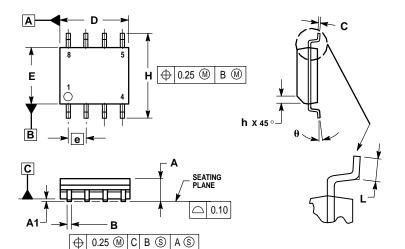




	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.250	5.97	6.35
В	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.180	BSC	4.58 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090	BSC	2.29	BSC
R	0.175	0.215	4.45	5.46
S	0.020	0.050	0.51	1.27
c	0.020		0.51	
٧	0.030	0.050	0.77	1.27
Z	0.138		3.51	



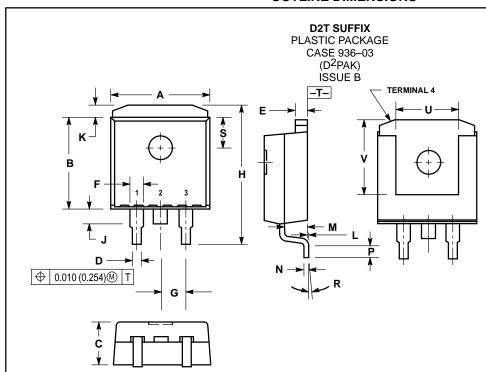
PLASTIC PACKAGE CASE 751-05 (SOP-8) ISSUE S



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. DIMENSIONS ARE IN MILLIMETERS.
 3. DIMENSION DAND E DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
 5. DIMENSION B DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.12T TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS			
DIM	MIN	MAX		
Α	1.35	1.75		
A1	0.10	0.25		
В	0.35	0.49		
C	0.18	0.25		
D	4.80	5.00		
Е	3.80	4.00		
е	1.27 BSC			
Н	5.80	6.20		
h	0.25	0.50		
L	0.40	1.25		
A	0.0	7 °		

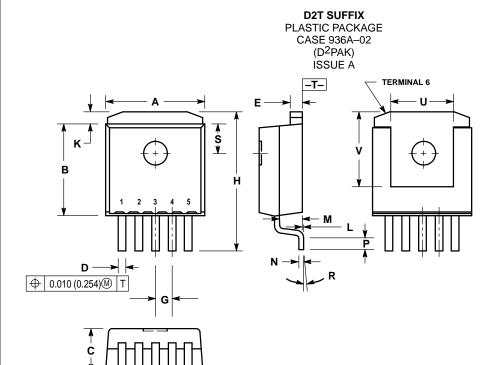
OUTLINE DIMENSIONS



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
- 3 TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
- 4 DIMENSIONS U AND V ESTABLISH A MINIMUM MOUNTING SURFACE FOR TERMINAL 4.
- 5 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
Е	0.045	0.055	1.143	1.397
F	0.051 REF		1.295 REF	
G	0.100 BSC		2.540 BSC	
Н	0.539	0.579	13.691	14.707
J	0.125 MAX		3.175 MAX	
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
Р	0.058	0.078	1.473	1.981
R	5° REF		5°REF	
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
٧	0.250 MIN		6.350 MIN	



NOTES:

- 6 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 7 CONTROLLING DIMENSION: INCH.
- 8 TAB CONTOUR OPTIONAL WITHIN DIMENSIONS
- A AND K.

 9 DIMENSIONS U AND V ESTABLISH A MINIMUM
 MOUNTING SURFACE FOR TERMINAL 6.

 10 DIMENSIONS A AND B DO NOT INCLUDE MOLD
- FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
Е	0.045	0.055	1.143	1.397
G	0.067 BSC		1.702 BSC	
Н	0.539	0.579	13.691	14.707
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5°REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
٧	0.250 MIN		6.350 MIN	

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights or others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and A are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 303–675–2140 or 1–800–441–2447 **JAPAN**: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4–32–1, Nishi–Gotanda, Shinagawa–ku, Tokyo 141, Japan. 81–3–5487–8488

Mfax™: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609 – US & Canada ONLY 1–800–774–1848

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

Mfax is a trademark of Motorola, Inc.

INTERNET: http://motorola.com/sps



LM2931/D