# MIFARE Classic 1K Access Bits Calculator

Byte Number	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Description	KEY A						Access Bits					KEY B (optional)				
							0x78	0x77	0x88	USER						

#### Access conditions for data block 0

P	Acces	ss bit	ts		·	Amuliaatian					
	C1 <sub>0</sub>	C2 <sub>0</sub>	C3 <sub>0</sub>	read	read write		decrement, transfer, restore	Application			
	0	0	0	key A B <sup>1</sup>	transport configuration						
	0	1	0	key A B <sup>1</sup>	never	never	never	read/write block			
•	1	0	0	key A B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block			
$\bigcirc$	1	1	0	key A B <sup>1</sup>	key B <sup>1</sup>	key B <sup>1</sup>	key A B1	value block			
$\bigcirc$	0	0	1	key A B <sup>1</sup>	never	never	key A B <sup>1</sup>	value block			
	0	1	1	key B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block			
	1	0	1	key B <sup>1</sup>	never	never	never	read/write block			
	1	1	1	never	never	never	never	read/write block			

<sup>&</sup>lt;sup>1</sup> if Key B may be read in the corresponding Sector Trailer it cannot serve for authentication (all grey marked lines in last table). As a consequences, if the reader authenticates any block of a sector which uses the grey marked access conditions and using key B, the card will refuse any subsequent memory access after authentication.

#### Access conditions for data block 1

	Access conditions for data block 1										
F	Acces	ss bit	ts			Amuliantiam					
	C1 <sub>1</sub>	C2 <sub>1</sub>	C3 <sub>1</sub>	read	write	increment	decrement, transfer, restore	Application			
$\bigcirc$	0	0	0	key A B <sup>1</sup>	transport configuration						
	0	1	0	key A B <sup>1</sup>	never	never	never	read/write block			
•	1	0	0	key A B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block			
	1	1	0	key A B <sup>1</sup>	key B <sup>1</sup>	key B <sup>1</sup>	key A B1	value block			
	0	0	1	key A B <sup>1</sup>	never	never	key A B <sup>1</sup>	value block			
$\bigcirc$	0	1	1	key B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block			
	1	0	1	key B <sup>1</sup>	never	never	never	read/write block			
	1	1	1	never	never	never	never	read/write block			

<sup>&</sup>lt;sup>1</sup> if Key B may be read in the corresponding Sector Trailer it cannot serve for authentication (all grey marked lines in last table). As a consequences, if the reader authenticates any block of a sector which uses the grey marked access conditions and using key B, the card will refuse any subsequent memory access after authentication.

## Access conditions for data block 2

	Access bits				,	Annlination				
	C1 <sub>2</sub>	C2 <sub>2</sub>	C3 <sub>2</sub>	read	read write		decrement, transfer, restore	Application		
	0	0	0	key A B <sup>1</sup>	transport configuration					
	0	1	0	key A B <sup>1</sup>	never	never	never	read/write block		
•	1	0	0	key A B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block		
	1	1	0	key A B <sup>1</sup>	key B <sup>1</sup>	key B <sup>1</sup>	key A B1	value block		
	0	0	1	key A B <sup>1</sup>	never	never	key A B <sup>1</sup>	value block		
	0	1	1	key B <sup>1</sup>	key B <sup>1</sup>	never	never	read/write block		
	1	0	1	key B <sup>1</sup>	never	never	never	read/write block		
	1	1	1	never	never	never	never	read/write block		

<sup>&</sup>lt;sup>1</sup> if Key B may be read in the corresponding Sector Trailer it cannot serve for authentication (all grey marked lines in last table). As a consequences, if the reader authenticates any block of a sector which uses the grey marked access conditions and using key B, the card will refuse any subsequent memory access after authentication.

### Access conditions for the sector trailer

	۸ ۵ ۵ ۵ ۵	a hi	· o		Ac	cess con	dition	for		
_ ′	Access bits		ıs	KE	YΑ	Access	bits	KEYB		Remark
	C1 <sub>3</sub>	C2 <sub>3</sub>	C3 <sub>3</sub>	read	write	read	write	read	write	
	0	0	0	never	key A	key A	never	key A	key A	Key B may be read <sup>[1]</sup>

,	10000	sc hi	to		Ac	cess con	dition	for		
,	Access bits		KE	ΥA	Access	bits	KEYB		Remark	
	C1 <sub>3</sub>	C2 <sub>3</sub>	C3 <sub>3</sub>	read	write	read	write	read	write	
$\bigcirc$	0	1	0	never	never	key A	never	key A	never	Key B may be read <sup>[1]</sup>
	1	0	0	never	key B	key A B	never	never	key B	
	1	1	0	never	never	key A B	never	never	never	
$\bigcirc$	0	0	1	never	key A	key A	key A	key A	key A	Key B may be read, transport configuration <sup>[1]</sup>
•	0	1	1	never	key B	key A B	key B	never	key B	
	1	0	1	never	never	key A B	key B	never	never	
	1	1	1	never	never	key A B	never	never	never	

 $<sup>^{</sup>f [1]}$  for this access condition key B is readable and may be used for data

 $\label{eq:htmlifted} \text{HTMLified by } \underbrace{\text{Akafugu Corporation}}_{\text{MF1S503x}}.$  The information is taken from  $\underbrace{\text{MF1S503x}}_{\text{MF1S503x}} \text{from NXP Semiconductors}.$