go

1

done

github.com/ReconfigureIO/sdaccel/axi/protocol.WriteDisable

go

1

clientResp_4

1

F

| 95'd0 | M | 74'd4722366482869645213696 | CL |

CL

2

CL

F

1

1

J

clientAddr_4

clientData_4

github.com/ReconfigureIO/sdaccel/axi/protocol.ReadDisable

go

clientData_9

1

1

F

95'd0   M   CL

CL   2   F

1   J

clientAddr_9

github.com/ReconfigureIO/sdaccel/axi/memory.WriteUInt64

clientResp_14

$t:6_14

go

CL

CL

F

F

not (in[0:0])

F

J

J

0

{{0,1},255}

F

CL

$t:7_14

{0,{2'd1,{3'd3,8'd0}}}

{0,{4'd0,{4'd0,{3'd0,{0,{0,1}}}}}}

in[63:0] and 64'd18446744073709551608

J

CL

J

F

CL

J

clientData_14

clientAddr_14

done

github.com/ReconfigureIO/sdaccel/axi/memory.ReadUInt64

$t:15_21   clientData_21

go

CL   CL

F   F   F

0

J   J

{0,{2'd1,{3'd3,8'd0}}}   {0,{4'd0,{4'd0,{3'd0,{0,{0,1}}}}}}   in[63:0] and 64'd18446744073709551608

F   CL

1

J

$t:16_21

F

J   CL

done   clientAddr_21

github.com/ReconfigureIO/sdaccel/axi/memory.WriteUInt32

$t:25_28

go

CL

F

0

1

J

F

{0,{2'd1,{3'd2,8'd0}}}   in[63:0] and 64'd18446744073709551612   {0,{4'd0,{4'd0,{3'd0,{0,1}}}}}   8'd4 and in[7:0]

J

J

F

S

CL

F   F

1

8'd15   240   {32'd0,in[31:0]}

clientAddr_28

M   F

clientResp_28   {32'd0,in[31:0]}   {in[31:0],32'd0}

1

CL   2

F   M

F

2   {0,1}

not (in[0:0])   J

J   F

J

J   CL

1

CL   1

done   clientData_28

1

$t:26_28

github.com/ReconfigureIO/sdaccel/axi/memory.ReadUInt32

$t:34_35

1

CL

F

go

1

F

J

clientData_35

1

CL

0

F

J

{0,{2'd1,{3'd2,8'd0}}}   {0,{4'd0,{4'd0,{3'd0,{0,{0,1}}}}}}   8'd4 and in[7:0]   in[63:0] and 64'd18446744073709551612

J

F

J

F

S

CL

{32'd0,in[31:0]}

1

F

clientAddr_35

M

2

F

J

CL

1

1

done

$t:35_35

github.com/ReconfigureIO/sdaccel/axi/memory.WriteUInt16

github.com/ReconfigureIO/sdaccel/axi/memory.ReadUInt16

$t:53_49

go

clientData_49

CL

F

J

F

J

CL

0

F

{0,{2'd1,{3'd1,8'd0}}}

{0,{4'd0,{4'd0,{3'd0,{0,{0,1}}}}}}

8'd6 and in[7:0]

in[63:0] and 64'd18446744073709551614

F

J

J

F

S

CL

{48'd0,in[15:0]}

{16'd0,in[47:0]}

{32'd0,in[31:0]}

F

F

F

clientAddr_49

M

F

J

CL

done

$t:54_49

github.com/ReconfigureIO/sdaccel/axi/memory.ReadUInt8

github.com/ReconfigureIO/sdaccel/axi/memory.WriteBurstUInt64

github.com/ReconfigureIO/sdaccel/axi/memory.ReadBurstUInt64

github.com/ReconfigureIO/sdaccel/axi/memory.ReadBurstUInt32

github.com/ReconfigureIO/sdaccel/axi/memory.WriteBurstUInt16

github.com/ReconfigureIO/sdaccel/axi/memory.ReadBurstUInt16

github.com/ReconfigureIO/sdaccel/axi/memory.WriteBurstUInt8

github.com/ReconfigureIO/sdaccel/axi/memory.ReadBurstUInt8

github.com/lebu26/fir_100.acc0

y1_170   y2_170

go

1   1

CL   CL

1

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_170

y1_175   **y2_175**

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_175

y1_180 y2_180

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_180

y1_185   y2_185

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_185

y1_190 **y2_190**

go

1   1

1

CL   CL

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_190

y1_195   y2_195

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_195

y1_200 y2_200

go

1 **1**

1

CL CL

I J

in[31:0] + in[63:32]

1

J

CL

1

out_200

y1_205   y2_205

go

1   **1**

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_205

y1_210   y2_210

go

1   1

1

CL   CL

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_210

y1_215   y2_215

go

1   1

CL   CL

1

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_215

github.com/lebu26/fir_100.acc10

y1_220   **y2_220**

go

`1`   `1`

`1`

CL   CL

I

J

`1`   in[31:0] + in[63:32]

J

CL

`1`

out_220

y1_225　　y2_225

1　　**1**

go

1

CL　　CL

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_225

y1_230   y2_230

go

1   **1**

1

CL   CL

I

J

1

in[31:0] + in[63:32]

J

CL

1

out_230

github.com/lebu26/fir_100.acc13

y1_235   y2_235

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_235

github.com/lebu26/fir_100.acc14

y1_240   y2_240

go

1   1

CL   CL

1

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_240

github.com/lebu26/fir_100.acc15

y1_245   y2_245

go

CL   CL

I   J

in[31:0] + in[63:32]

J

CL

out_245

github.com/lebu26/fir_100.acc16

y1_250   y2_250

go

1   **1**

1

CL   CL

I   J

1   in[31:0] + in[63:32]

J

CL

1

out_250

github.com/lebu26/fir_100.acc17

y1_255   y2_255

go

1   1

1

CL   CL

I   J

in[31:0] + in[63:32]

1

J

CL

1

out_255

y1_260   y2_260

go

1   **1**

1

CL   CL

I

J

in[31:0] + in[63:32]

1

J

CL

1

out_260

go   x0_265

1   **1**

I   CL

1   **32'd2 * in[31:0]**

J

CL

1

y0_265

go

x1_270

1

1

I

CL

1

32'd8 * in[31:0]

J

CL

1

y1_270

go   x2_275

1   **1**

I   CL

1   32'd5 * in[31:0]

J

CL

1

y2_275

go   x3_280

1   **1**

I   CL

1

32'd1 * in[31:0]

J

CL

1

y3_280

github.com/lebu26/fir_100.fir4

go   **x4_285**

1   **1**

I   CL

1   **32'd10 * in[31:0]**

J

CL

1

y4_285

github.com/lebu26/fir_100.fir5

go   x5_290

1   **1**

I   CL

1   32'd5 * in[31:0]

J

CL

1

y5_290

go　　　　x6_295

1　　　　**1**

I　　　　CL

1

32'd9 * in[31:0]

J

CL

1

y6_295

go   x7_300

1   **1**

I   CL

1

32'd9 * in[31:0]

J

CL

1

y7_300

go **x8_305**

1 **1**

I CL

1 **32'd3 * in[31:0]**

J

CL

1

y8_305

go   x9_310

1   **1**

I   CL

1   **32'd5 * in[31:0]**

J

CL

1

y9_310

go   **x10_315**

1   **1**

I   CL

1   **32'd6 * in[31:0]**

J

CL

1

y10_315

go **x11_320**

1 **1**

I CL

1 **32'd6 * in[31:0]**

J

CL

1

y11_320

go   **x12_325**

1   **1**

I   CL

1   32'd2 * in[31:0]

J

CL

1

y12_325

go   **x13_330**

1   1

I   CL

1   32'd8 * in[31:0]

J

CL

1

y13_330

go   x14_335

1   **1**

I   CL

1   32'd2 * in[31:0]

J

CL

1

y14_335

go **x15_340**

1 **1**

I CL

1 32'd2 * in[31:0]

J

CL

1

y15_340

github.com/lebu26/fir_100.fir16

go   **x16_345**

1   **1**

I   CL

1   **32'd6 * in[31:0]**

J

CL

1

y16_345

go   x17_350

1   1

I   CL

1   32'd3 * in[31:0]

J

CL

1

y17_350

github.com/lebu26/fir_100.fir18

go **x18_355**

1 **1**

I CL

1 **32'd8 * in[31:0]**

J

CL

1

y18_355

go   **x19_360**

1   1

I   CL

1   32'd7 * in[31:0]

J

CL

1

y19_360

github.com/lebu26/fir_100.input_first

go

1

32'd0

in_370

M

1

2

CL

F

J

CL

CL

1

1

move_370

out_370

go

1

32'd0

in_375

M

1

2

CL

F

CL

CL

J

1

1

move_375

out_375

go

1

32'd0

in_380

M

1

2

CL

F

J

CL

CL

1

1

move_380

out_380

go

1

32'd0

in_385

M

1

2

CL

F

CL

CL

J

1

1

move_385

out_385

go

1

32'd0

in_390

M

1

2

CL

F

J

CL

CL

1

1

move_390

out_390

go

1

32'd0

in_395

M

1

2

CL

F

J

CL

CL

1

1

move_395

out_395

go

1

32'd0

in_400

M

1

2

CL

F

J

CL

CL

1

1

move_400

out_400

go

1

32'd0

in_405

M

1

2

CL

F

J

CL

CL

1

1

move_405

out_405

go

1

32'd0

in_410

M

1

2

CL

F

J

CL

CL

1

1

move_410

out_410

go

1

32'd0

in_415

M

1

2

CL

F

J

CL

CL

1

1

move_415

out_415

go

1

32'd0

in_420

1

2

CL

F

J

CL

CL

1

1

move_420

out_420

go

1

32'd0

in_425

M

1

2

CL

F

J

CL

CL

1

1

move_425   out_425

go

1

32'd0

in_430

1

2

CL

F

J

CL

CL

1

1

move_430

out_430

go

1

32'd0

in_435

M

1

2

CL

F

J

CL

CL

1

1

move_435

out_435

go

1

32'd0

in_440

M

1

2

CL

F

CL

CL

J

1

1

move_440

out_440

go

1

32'd0

in_445

M

1

2

CL

F

CL

CL

J

1

1

move_445

out_445

go

1

32'd0

in_450

M

1

2

CL

F

CL

J

CL

1

1

move_450

out_450

go

1

32'd0

in_455

M

1

2

CL

F

J

CL

CL

1

1

move_455

out_455

go

1

32'd0

M

in_460

2

1

F

CL

J

CL

1

out_460

github.com/lebu26/fir_100.FIR_Top

main.Top