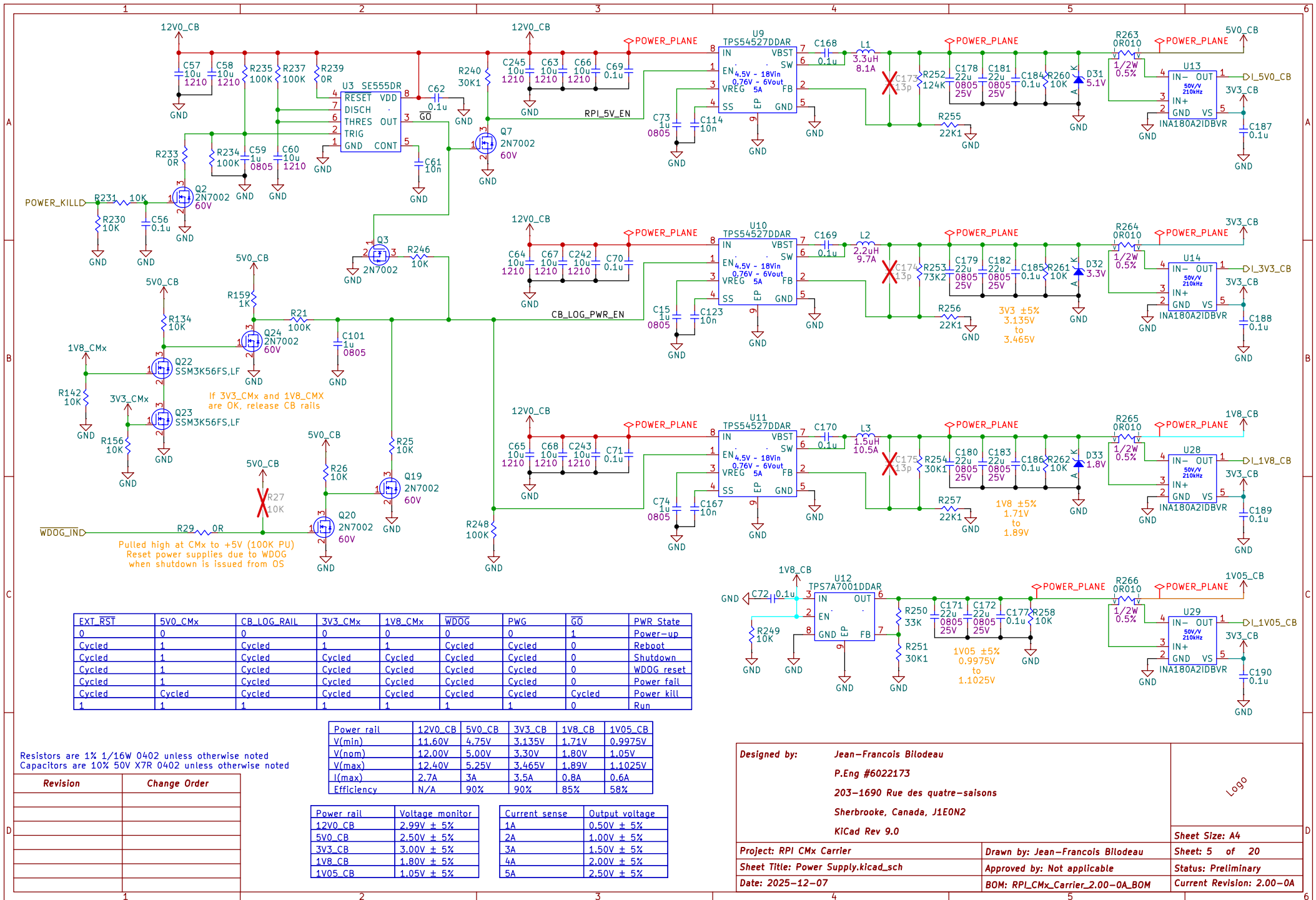


Resistors are 1% 1/16W 0402 unless otherwise noted  
Capacitors are 10% 50V X7R 0402 unless otherwise noted

Revision	Change Order

Designed by: Jean-Francois Bilodeau P.Eng #6022173 203-1690 Rue des quatre-saisons Sherbrooke, Canada, J1E0N2 KiCad Rev 9.0		Logo
Project: RPI CMx Carrier	Drawn by: Jean-Francois Bilodeau	
Sheet Title: USB_3.kicad_sch	Approved by: Not applicable	Sheet: 4 of 20
Date: 2025-12-07	BOM: RPLCMxCarrier_2.00-0A_BOM	Status: Preliminary
		Current Revision: 2.00-0A



EXT_RST	5V0_CMx	CB_LOG_RAIL	3V3_CMx	1V8_CMx	WDOG	PWG	GO	PWR State
0	0	0	0	0	0	0	1	Power-up
Cycled	1	Cycled	1	1	Cycled	Cycled	0	Reboot
Cycled	1	Cycled	Cycled	Cycled	Cycled	Cycled	0	Shutdown
Cycled	1	Cycled	Cycled	Cycled	Cycled	Cycled	0	WDOG reset
Cycled	1	Cycled	Cycled	Cycled	Cycled	Cycled	0	Power fail
Cycled	Cycled	Cycled	Cycled	Cycled	Cycled	Cycled	Cycled	Power kill
1	1	1	1	1	1	1	0	Run

Resistors are 1% 1/16W 0402 unless otherwise noted  
Capacitors are 10% 50V X7R 0402 unless otherwise noted

Revision	Change Order

Power rail	12V0_CB	5V0_CB	3V3_CB	1V8_CB	1V05_CB
V(min)	11.60V	4.75V	3.135V	1.71V	0.9975V
V(nom)	12.00V	5.00V	3.30V	1.80V	1.05V
V(max)	12.40V	5.25V	3.465V	1.89V	1.1025V
I(max)	2.7A	3A	3.5A	0.8A	0.6A
Efficiency	N/A	90%	90%	85%	58%

Power rail	Voltage monitor
12V0_CB	2.99V ± 5%
5V0_CB	2.50V ± 5%
3V3_CB	3.00V ± 5%
1V8_CB	1.80V ± 5%
1V05_CB	1.05V ± 5%

Current sense	Output voltage
1A	0.50V ± 5%
2A	1.00V ± 5%
3A	1.50V ± 5%
4A	2.00V ± 5%
5A	2.50V ± 5%

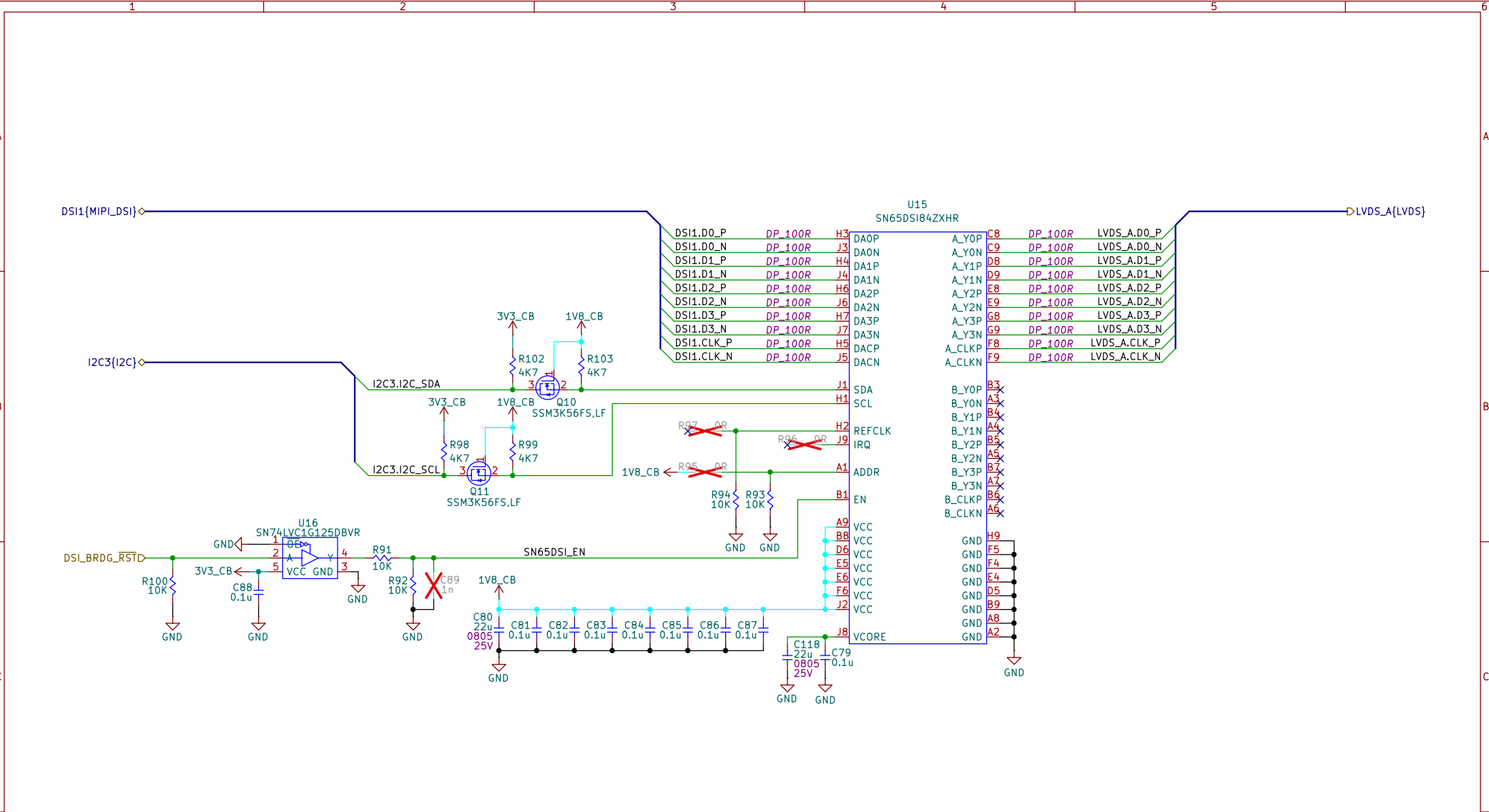
Designed by: Jean-Francois Bilodeau  
P.Eng #6022173  
203-1690 Rue des quatre-saisons  
Sherbrooke, Canada, J1E0N2  
KiCad Rev 9.0

Project: RPi CMx Carrier  
Sheet Title: Power Supply.kicad\_sch  
Date: 2025-12-07

Drawn by: Jean-Francois Bilodeau  
Approved by: Not applicable  
BOM: RPLCMx\_Carrier\_2.00-0A\_BOM

Logo

Sheet Size: A4  
Sheet: 5 of 20  
Status: Preliminary  
Current Revision: 2.00-0A

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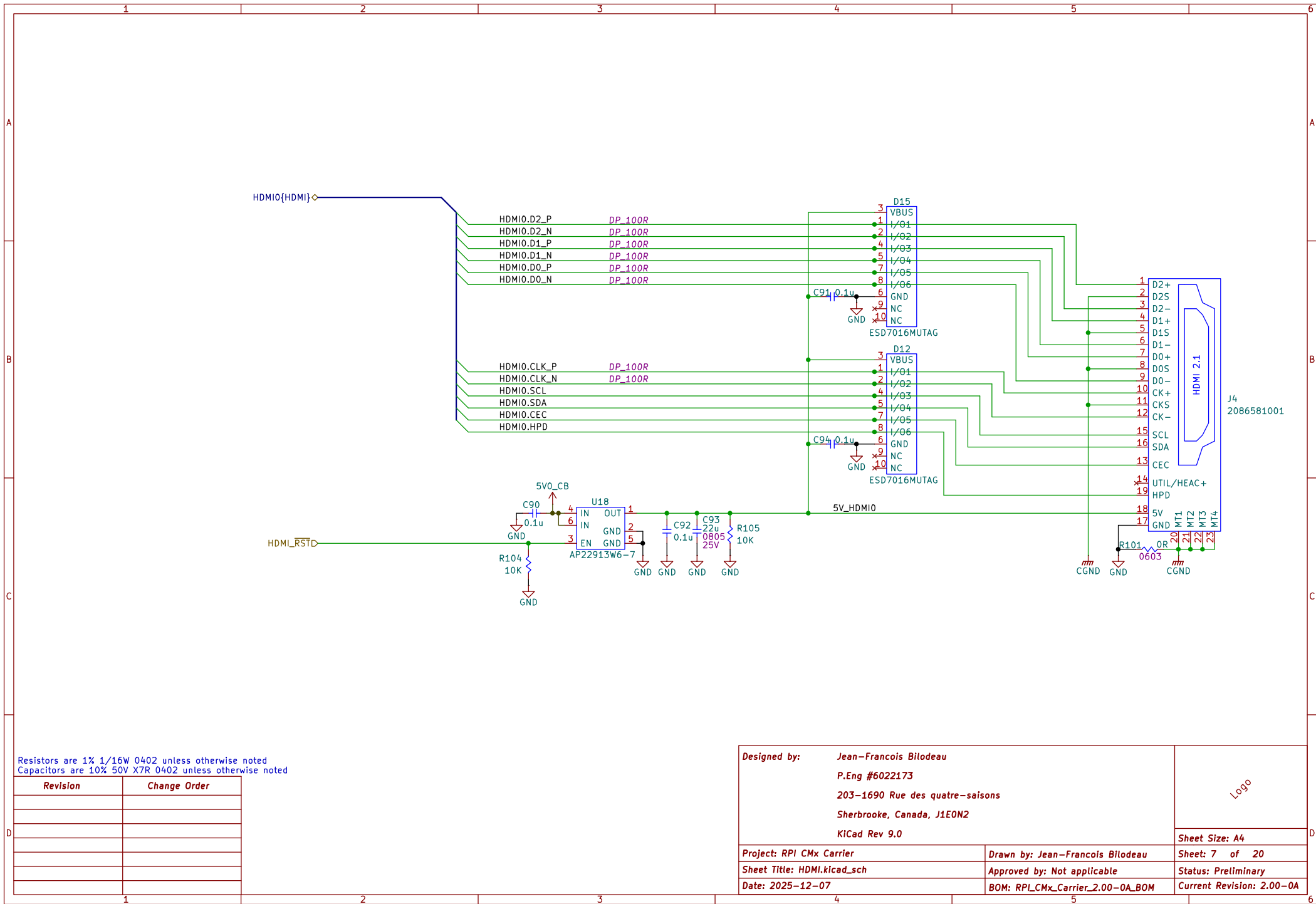
Project: RPI CMx Carrier	Drawn by: Jean-Francois Bilodeau	Sheet: 6 of 20
Sheet Title: DSL_LVDS_bridge.kicad_sch	Approved by: Not applicable	Status: Preliminary
Date: 2025-12-07	BOM: RPI_CMx_Carrier_2.00-0A_BOM	Current Revision: 2.00-0A

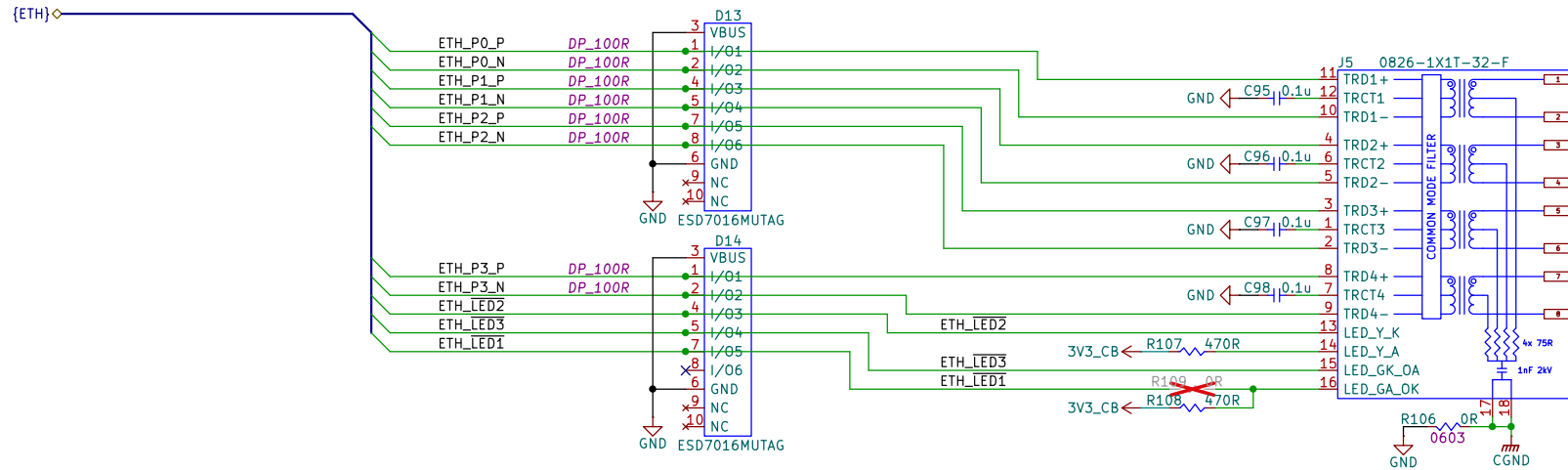
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Sheet: 6 of 20

Status: Preliminary

Current Revision: 2.00-0A



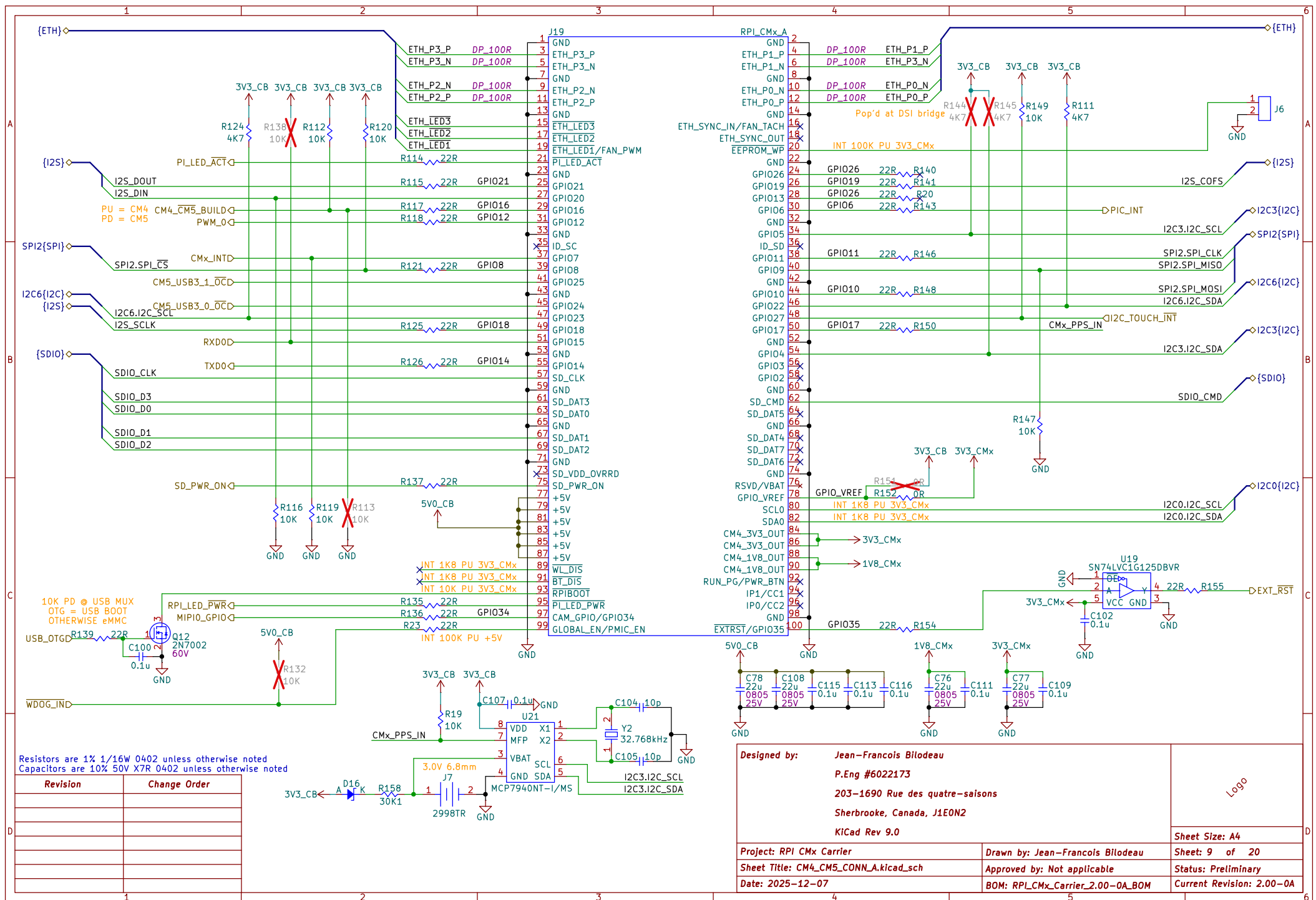


Resistors are 1% 1/16W 0402 unless otherwise noted  
Capacitors are 10% 50V X7R 0402 unless otherwise noted

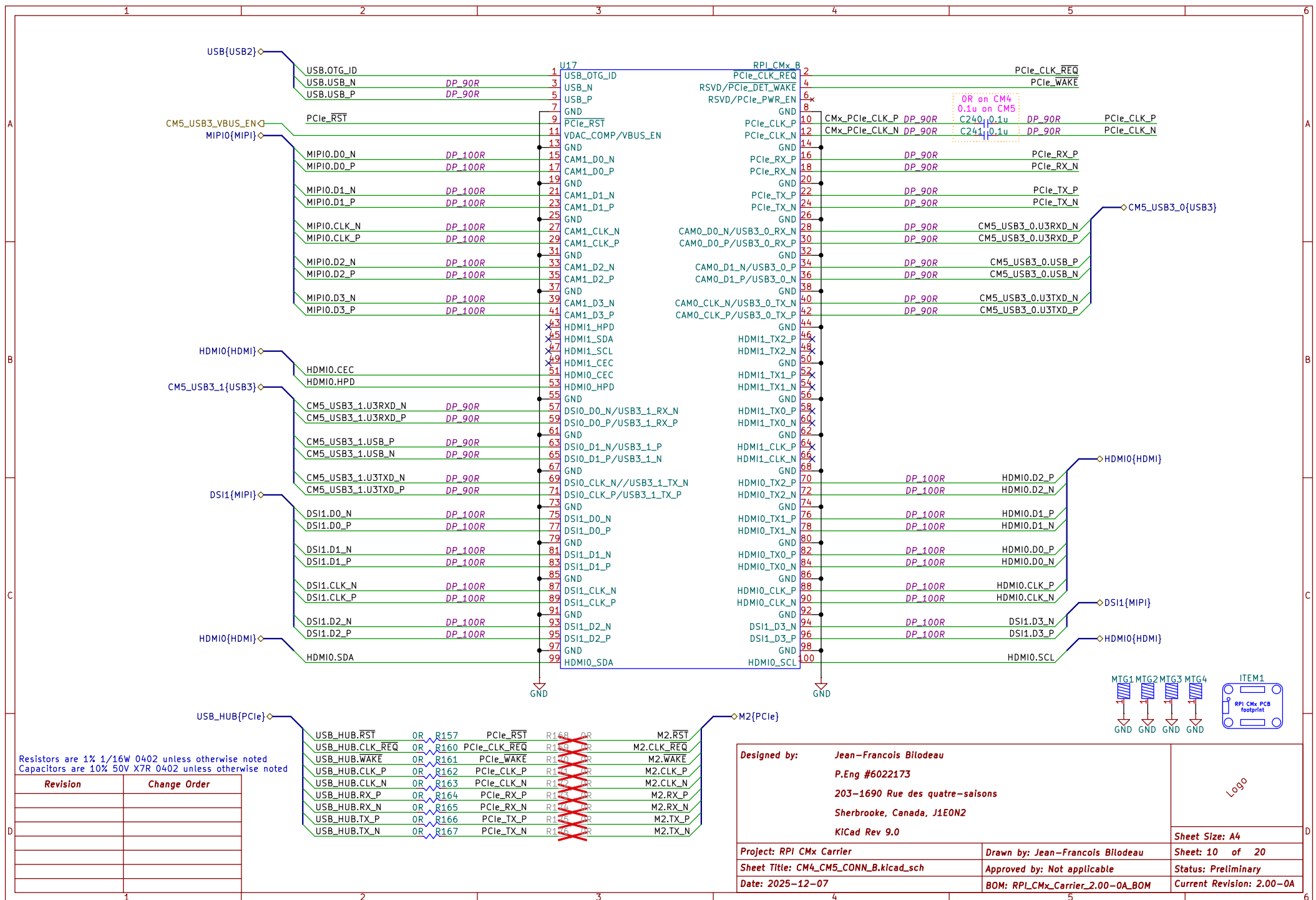
Revision	Change Order

Designed by: Jean-Francois Bilodeau  P.Eng #6022173  203-1690 Rue des quatre-saisons  Sherbrooke, Canada, J1E0N2  KiCad Rev 9.0		<div>Logo</div>
Project: RPI CMx Carrier		Sheet Size: A4
Drawn by: Jean-Francois Bilodeau		Sheet: 8 of 20
Sheet Title: Ethernet.kicad_sch		Approved by: Not applicable
Status: Preliminary		Date: 2025-12-07
BOM: RPLCMx_Carrier_2.00-0A_BOM		Current Revision: 2.00-0A



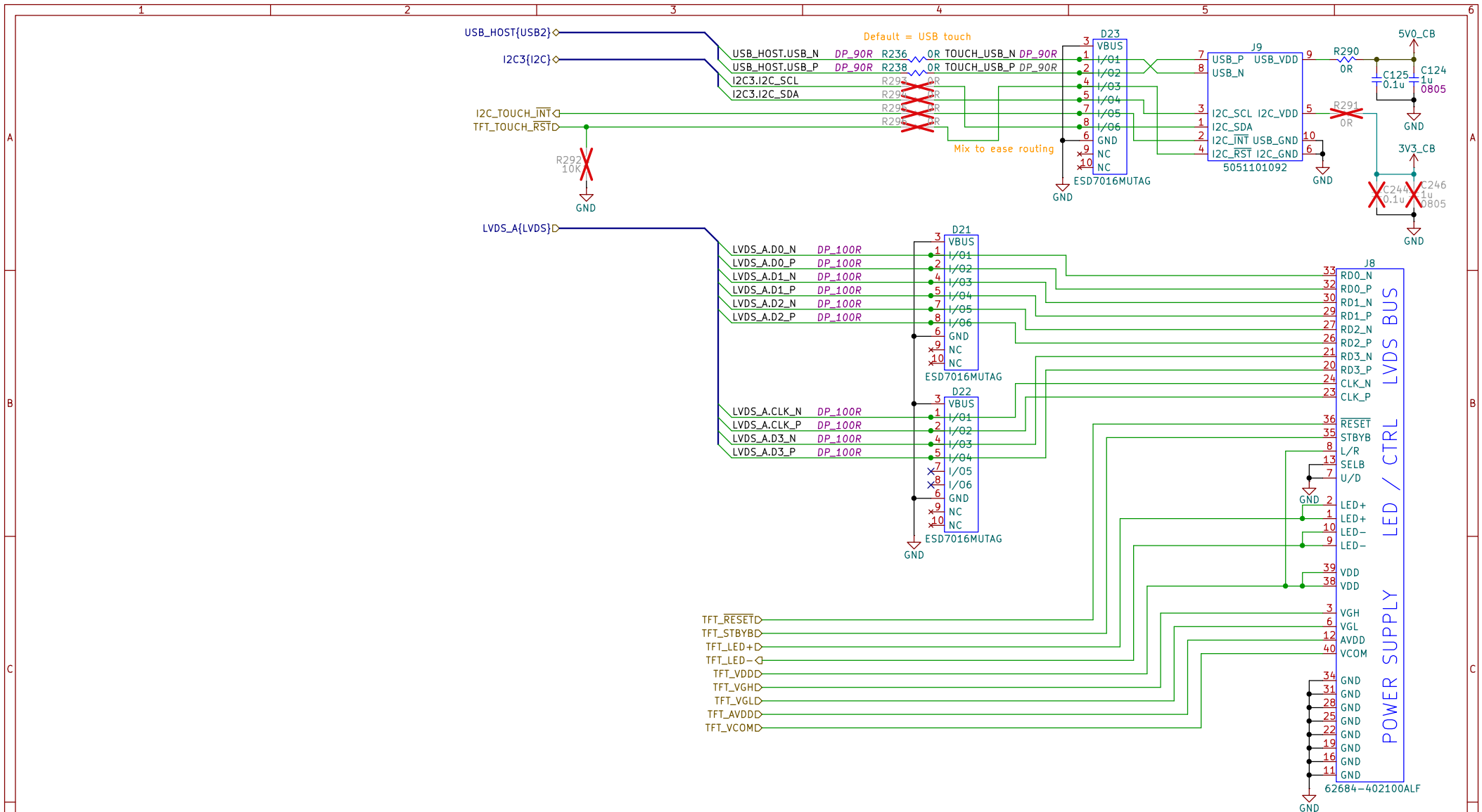


	Revision	Change Order
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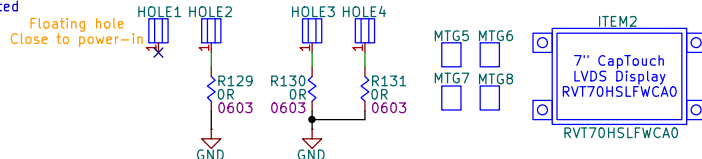




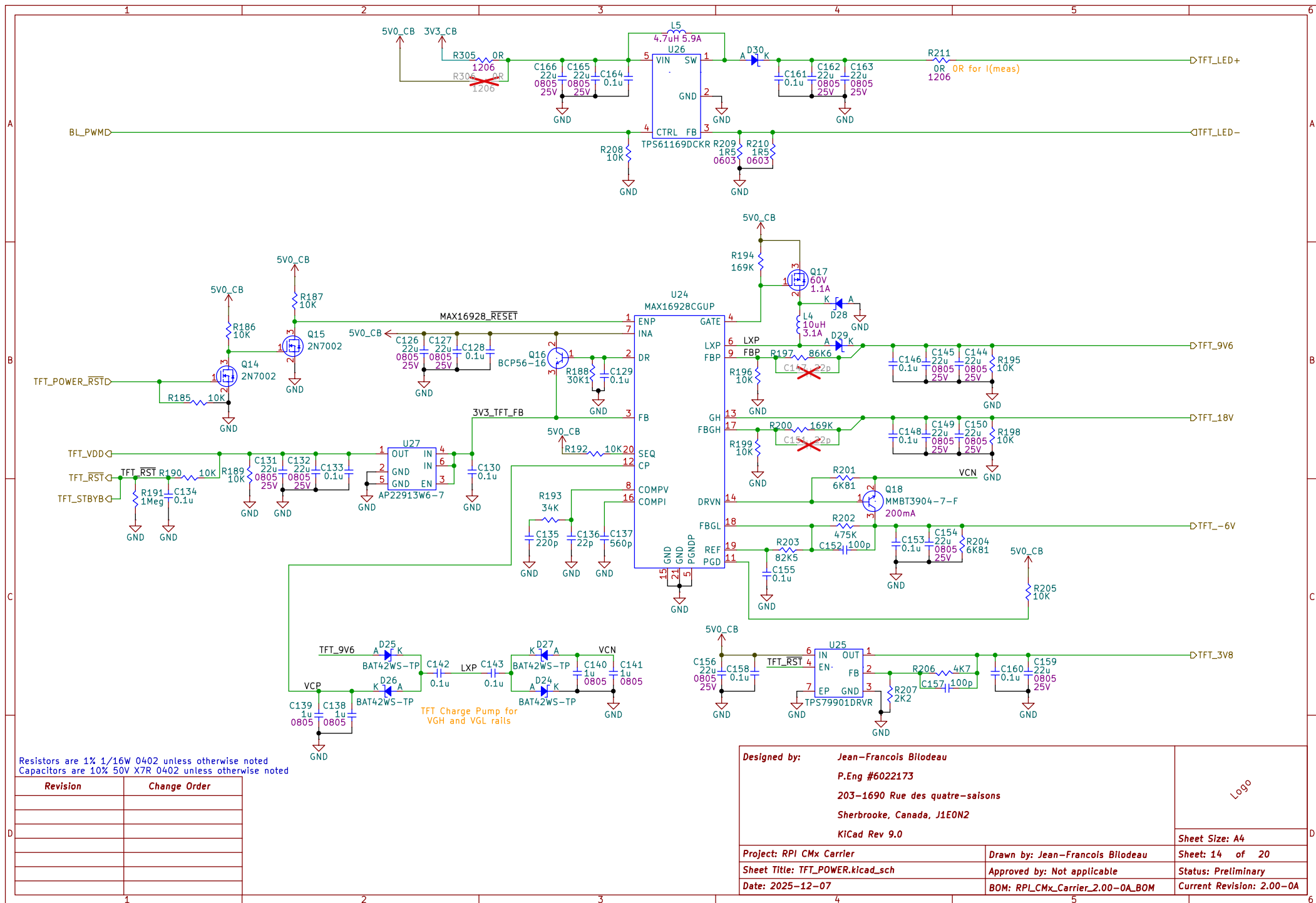


Resistors are 1% 1/16W 0402 unless otherwise noted  
Capacitors are 10% 50V X7R 0402 unless otherwise noted

Revision	Change Order



Designed by: Jean-Francois Bilodeau P.Eng #6022173 203-1690 Rue des quatre-saisons Sherbrooke, Canada, J1E0N2 KICad Rev 9.0		Logo	
			Sheet Size: A4
Project: RPI CMx Carrier	Drawn by: Jean-Francois Bilodeau	Sheet: 13 of 20	D
Sheet Title: TFT_CONN.kicad_sch	Approved by: Not applicable	Status: Preliminary	
Date: 2025-12-07	BOM: RPLCMx_Carrier_2.00-0A_BOM	Current Revision: 2.00-0A	



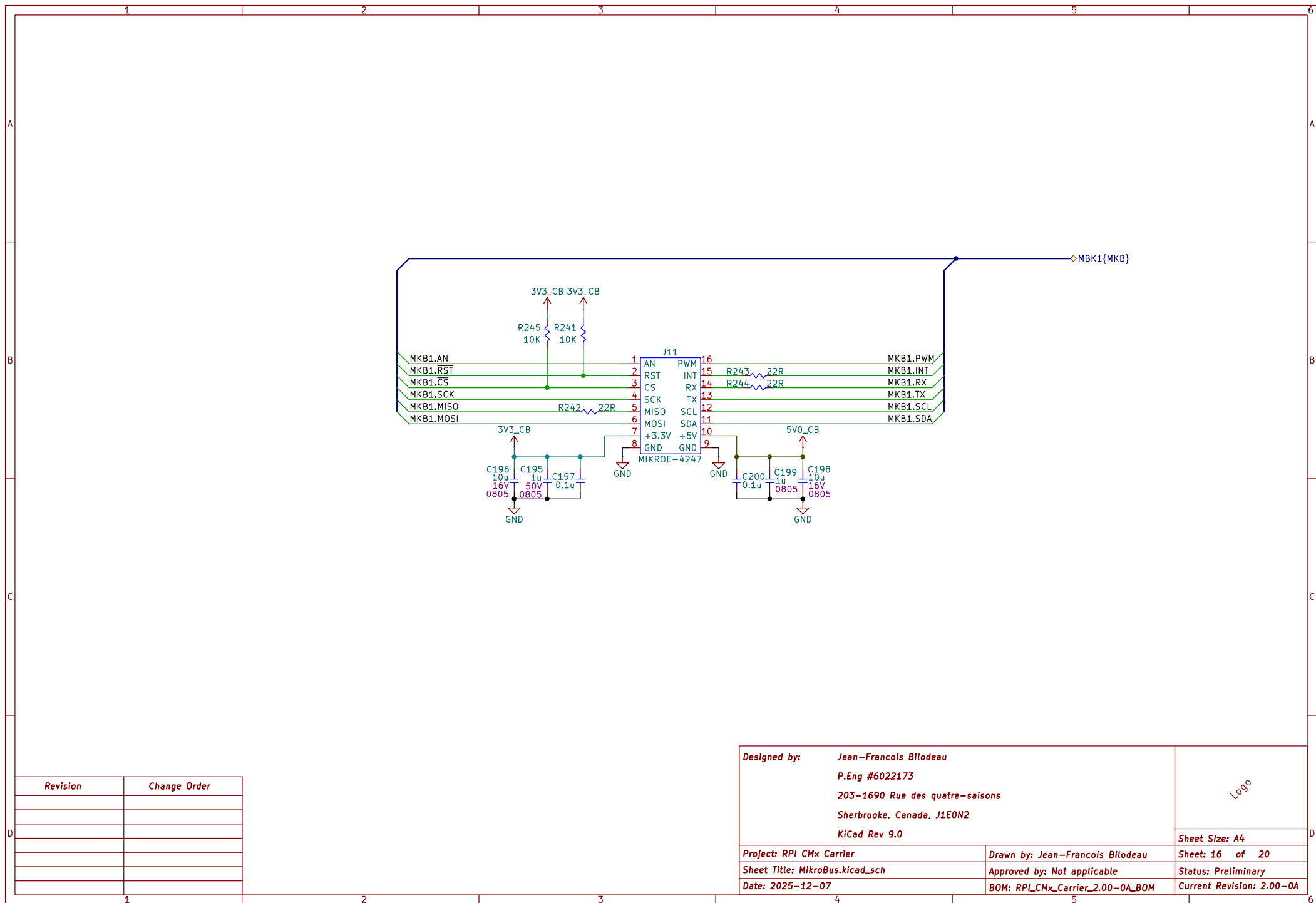
Resistors are 1% 1/16W 0402 unless otherwise noted  
Capacitors are 10% 50V X7R 0402 unless otherwise noted

Revision	Change Order

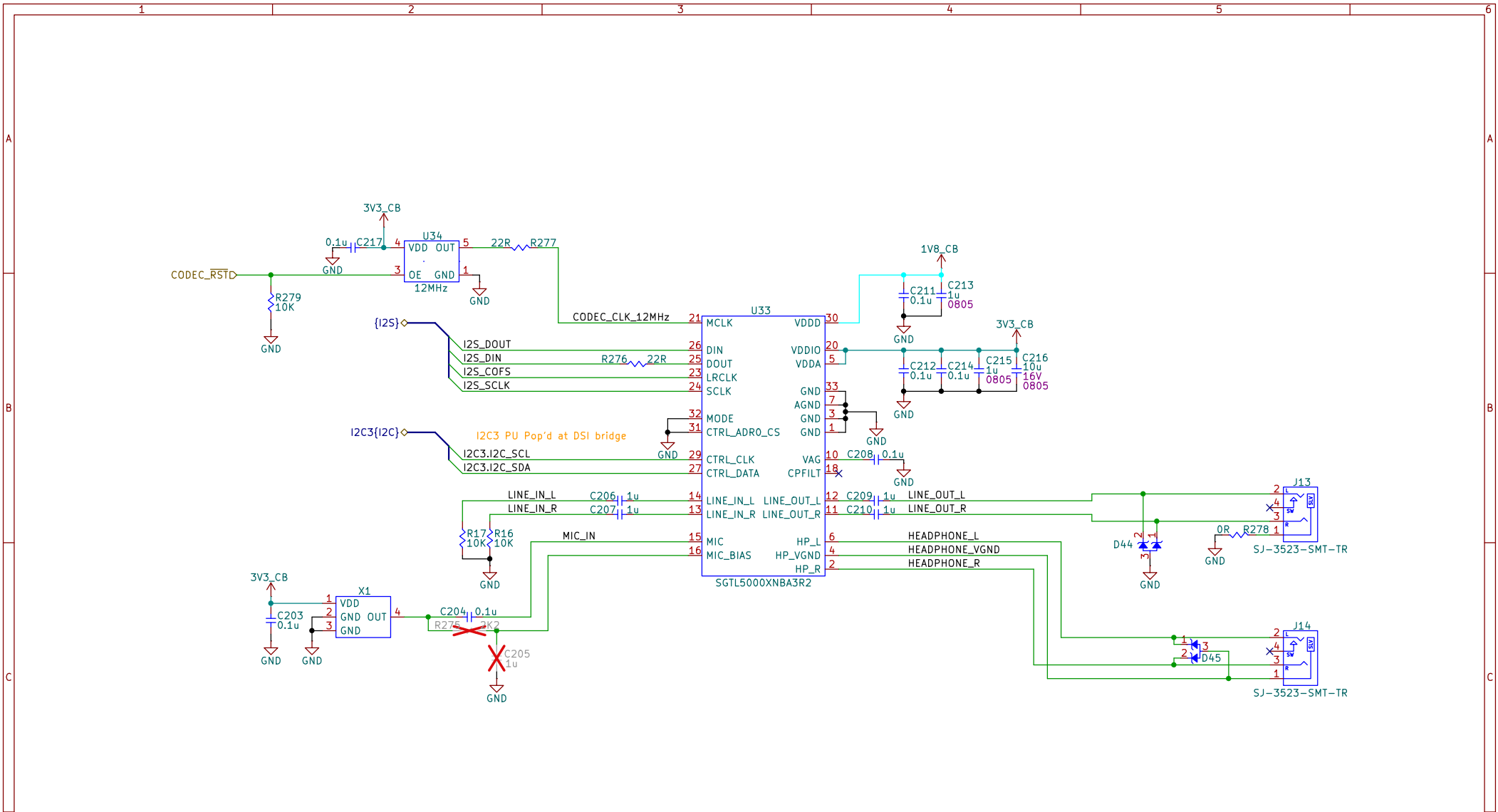
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<b>Project:</b> RPI CMx Carrier		<b>Sheet Size:</b> A4
<b>Sheet Title:</b> TFT_POWER.kicad_sch		<b>Sheet:</b> 14 of 20
<b>Date:</b> 2025-12-07		<b>Status:</b> Preliminary
<b>BOM:</b> RPL_CMx_Carrier_2.00-0A_BOM		<b>Current Revision:</b> 2.00-0A



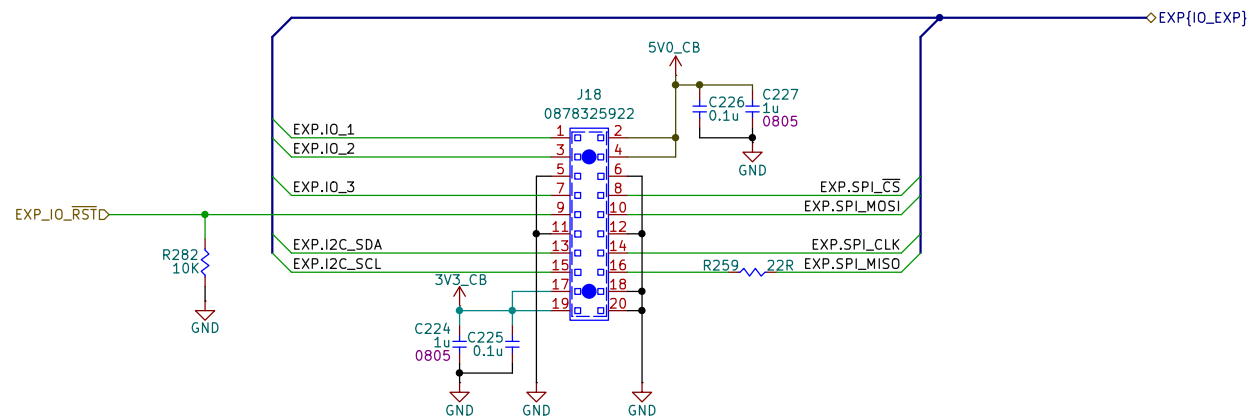
Designed by: Jean-Francois Bilodeau  P.Eng #6022173  203-1690 Rue des quatre-saisons  Sherbrooke, Canada, J1E0N2  KiCad Rev 9.0			D
Project: RPI CMx Carrier	Drawn by: Jean-Francois Bilodeau	Sheet: 15 of 20	
Sheet Title: RESET_SUPERVISOR.kicad_sch	Approved by: Not applicable	Status: Preliminary	
Date: 2025-12-07	BOM: RPLCMx_Carrier_2.00-0A_BOM	Current Revision: 2.00-0A	





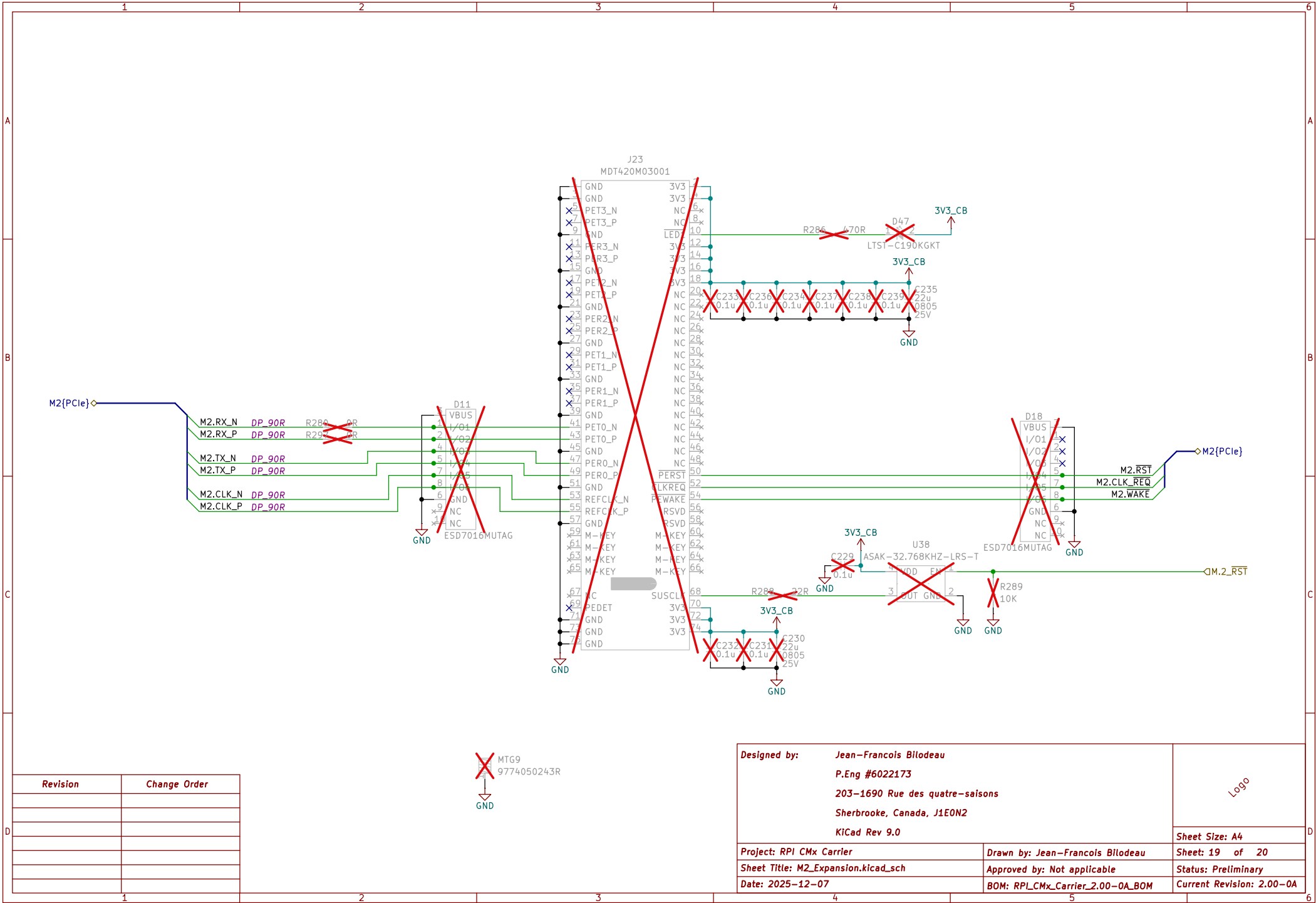
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Designed by: Jean-Francois Bilodeau P.Eng #6022173 203-1690 Rue des quatre-saisons Sherbrooke, Canada, J1E0N2 KiCad Rev 9.0			D
Project: RPI CMx Carrier		Sheet Size: A4	
Drawn by: Jean-Francois Bilodeau		Sheet: 17 of 20	
Sheet Title: AUDIO_CODEC.kicad_sch		Status: Preliminary	
Date: 2025-12-07	Approved by: Not applicable	Current Revision: 2.00-0A	
BOM: RPLCMxCarrier.2.00-0A_BOM			



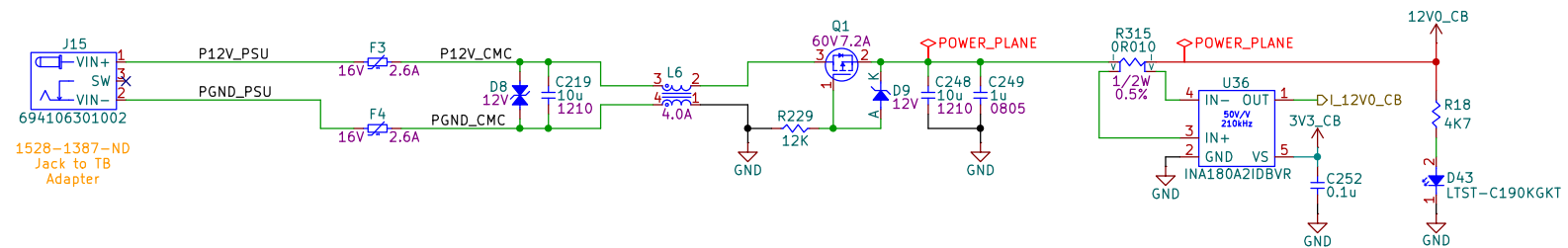
Revision	Change Order

Designed by: Jean-Francois Bilodeau P.Eng #6022173 203-1690 Rue des quatre-saisons Sherbrooke, Canada, J1E0N2 KiCad Rev 9.0		<div>Logo</div>
Project: RPI CMx Carrier		Sheet Size: A4
Sheet Title: Expansion_Io.kicad_sch	Drawn by: Jean-Francois Bilodeau	Sheet: 18 of 20
Date: 2025-12-07	Approved by: Not applicable	Status: Preliminary
BOM: RPL_CMx_Carrier_2.00-0A_BOM		Current Revision: 2.00-0A



Revision	Change Order

Designed by: Jean-Francois Bilodeau P.Eng #6022173 203-1690 Rue des quatre-saisons Sherbrooke, Canada, J1E0N2 KICad Rev 9.0		Logo
Project: RPI CMx Carrier	Drawn by: Jean-Francois Bilodeau	Sheet Size: A4
Sheet Title: M2_Expansion.kicad_sch	Approved by: Not applicable	Sheet: 19 of 20
Date: 2025-12-07	BOM: RPL_CMx_Carrier_2.00-0A_BOM	Status: Preliminary
		Current Revision: 2.00-0A



	Revision	Change Order
0		

Designed by: Jean-Francois Blodeau  
P.Eng #6022173  
203-1690 Rue des quatre-saisons  
Sherbrooke, Canada, J1E0N2  
KICad Rev 9.0

Logo

Sheet Size: A4

Project: RPI CMx Carrier
Sheet Title: POWER INPUT.kicad_sch

Status: Preliminary

Date: 2025-12-07

Current Revision: 2.00-0A
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